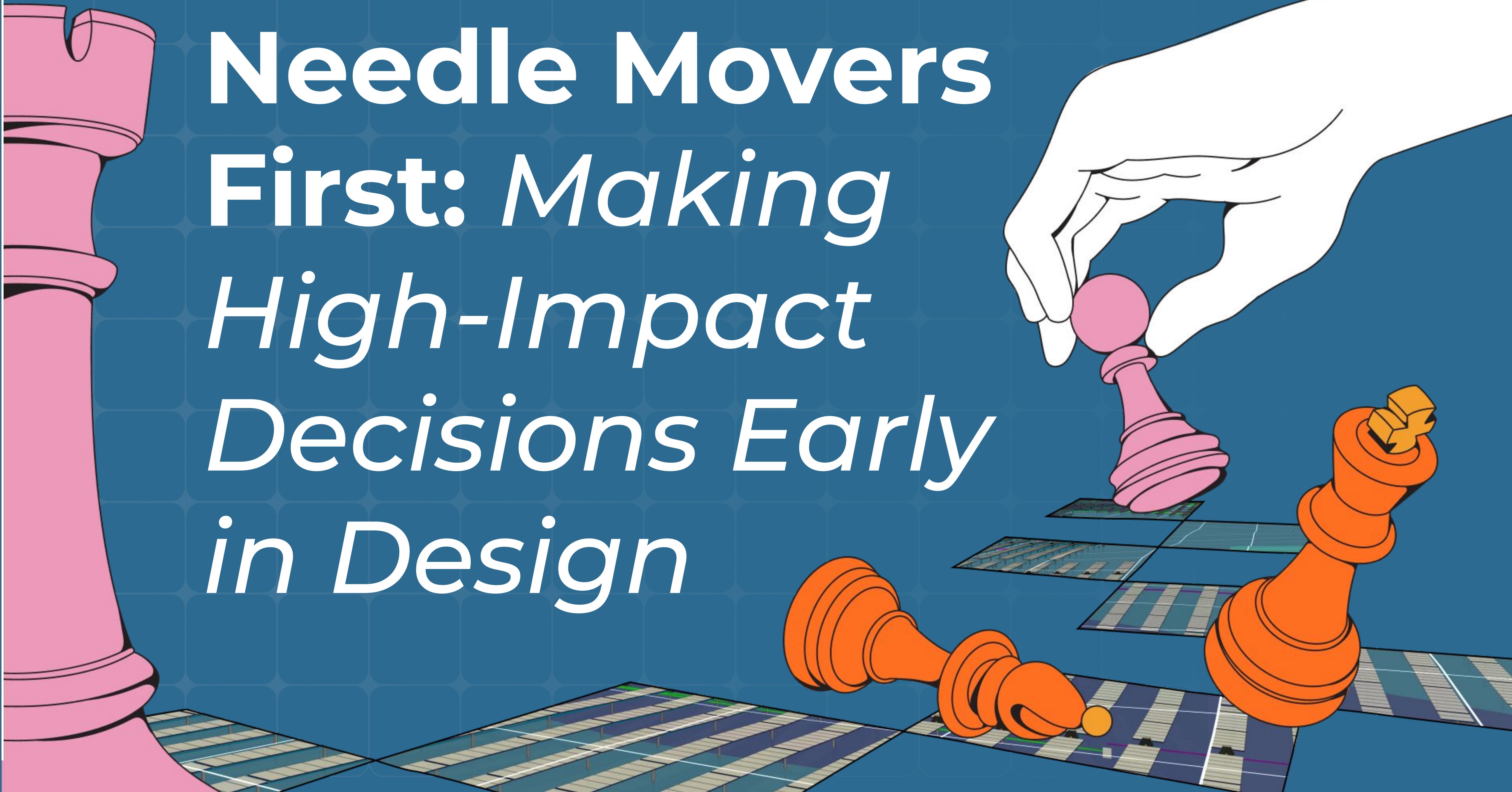
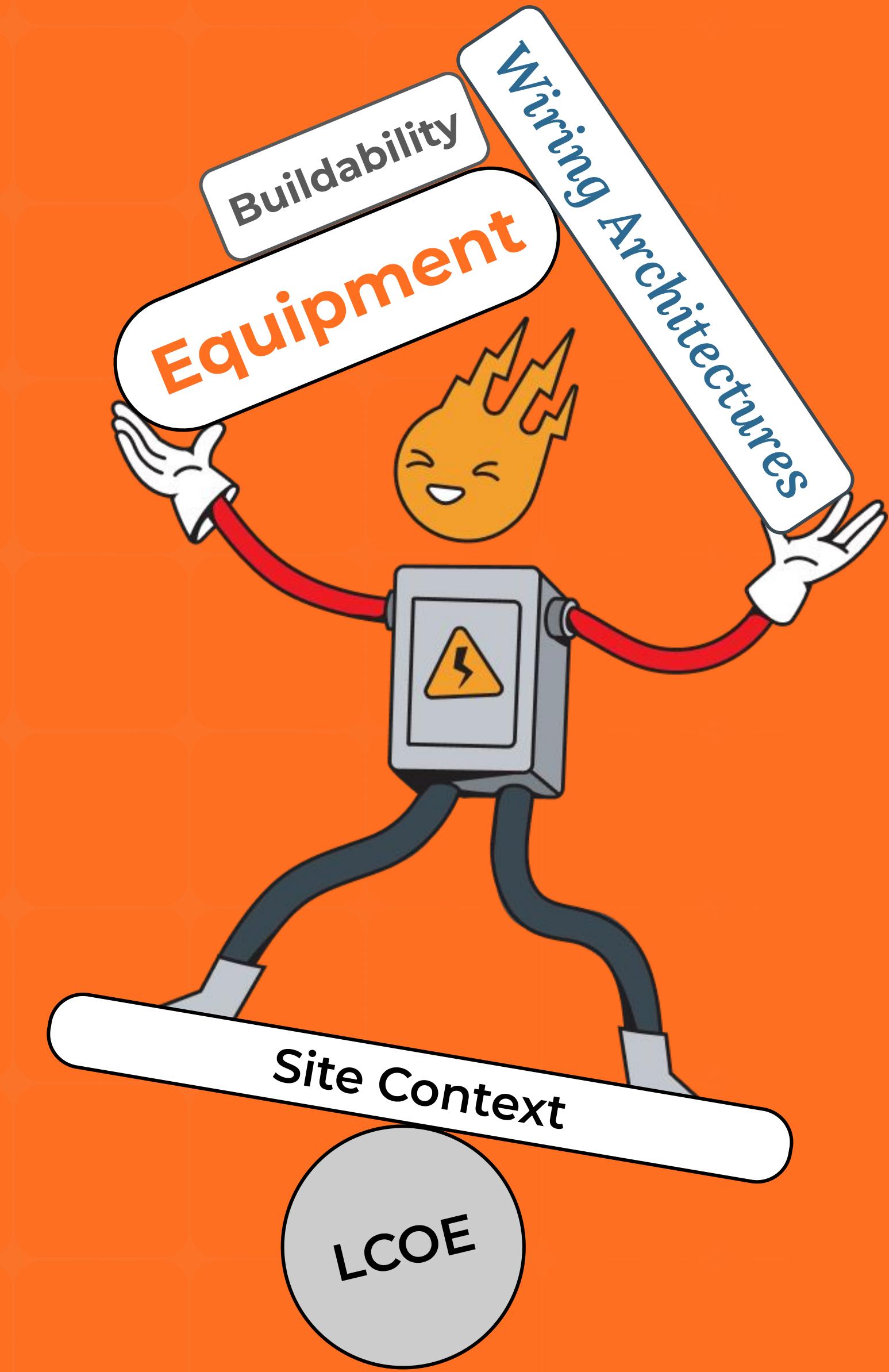


# Needle Movers

## First: Making High-Impact Decisions Early in Design

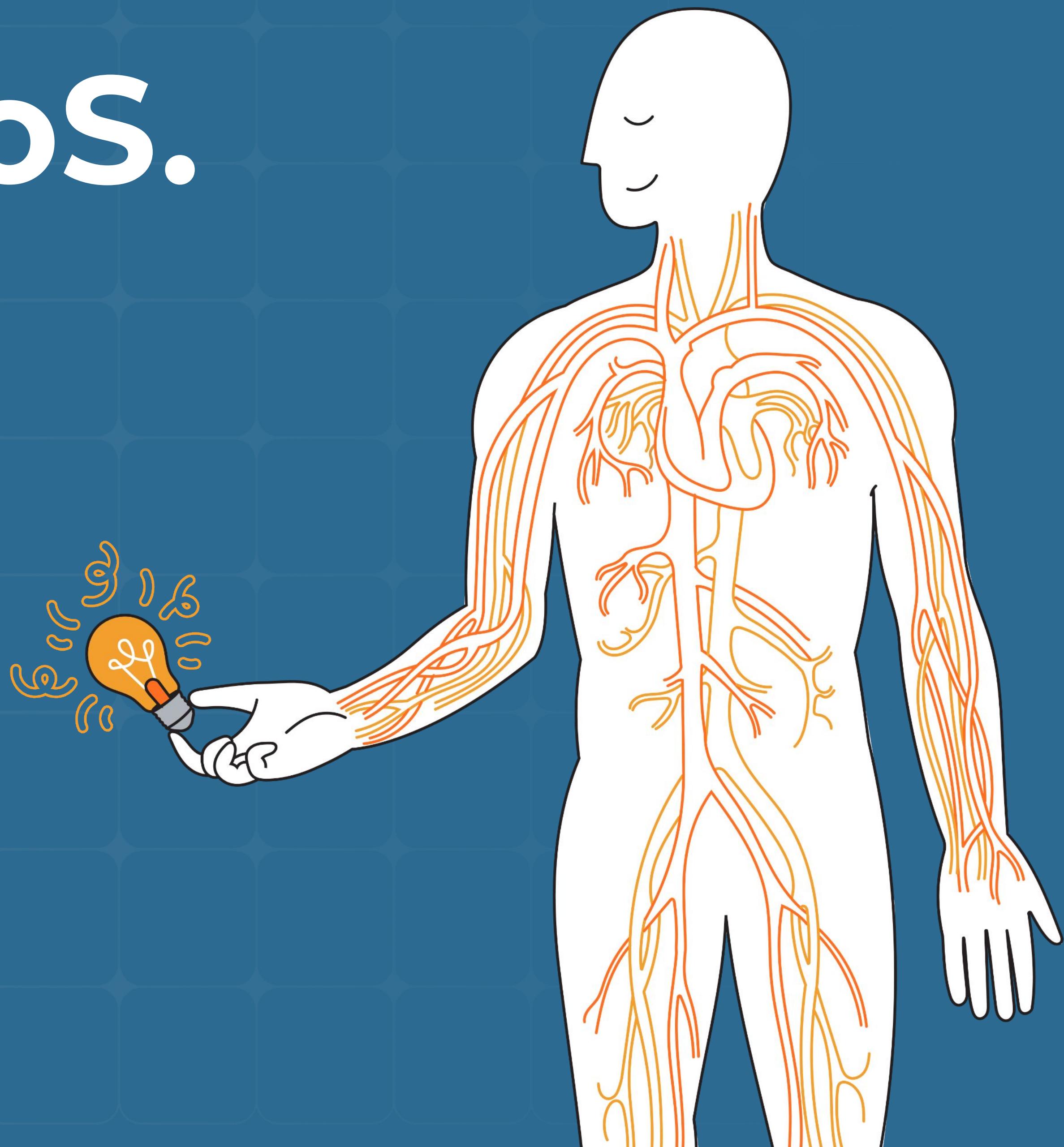


# Balancing Act Context



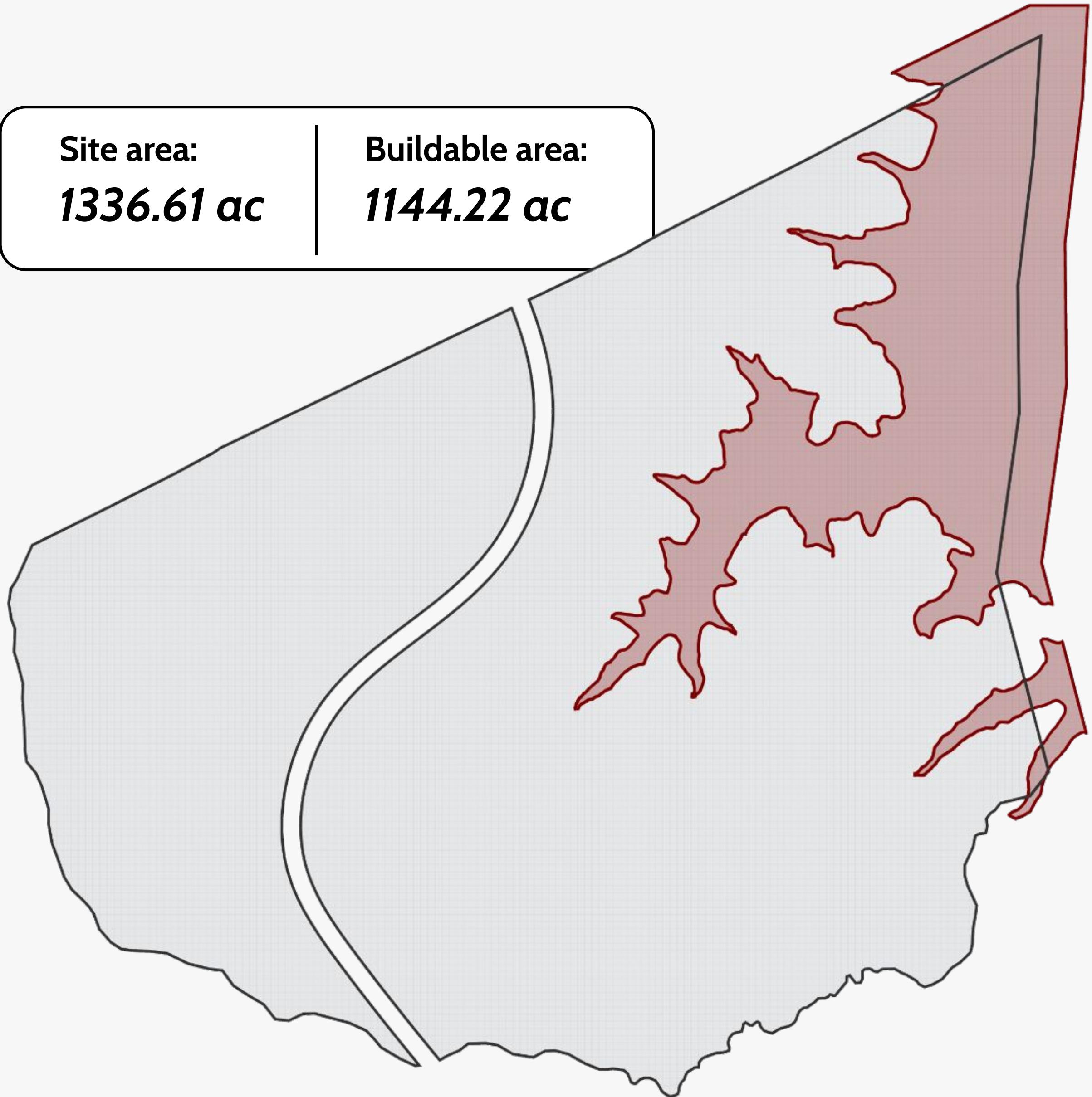
# Electrical BoS. Features

*Moving electricity from  
the solar panels to the  
grid is where the purpose  
meets power*



# Time Value Trade-Off





**Tracker 84 mods**

**PV Module 540W**



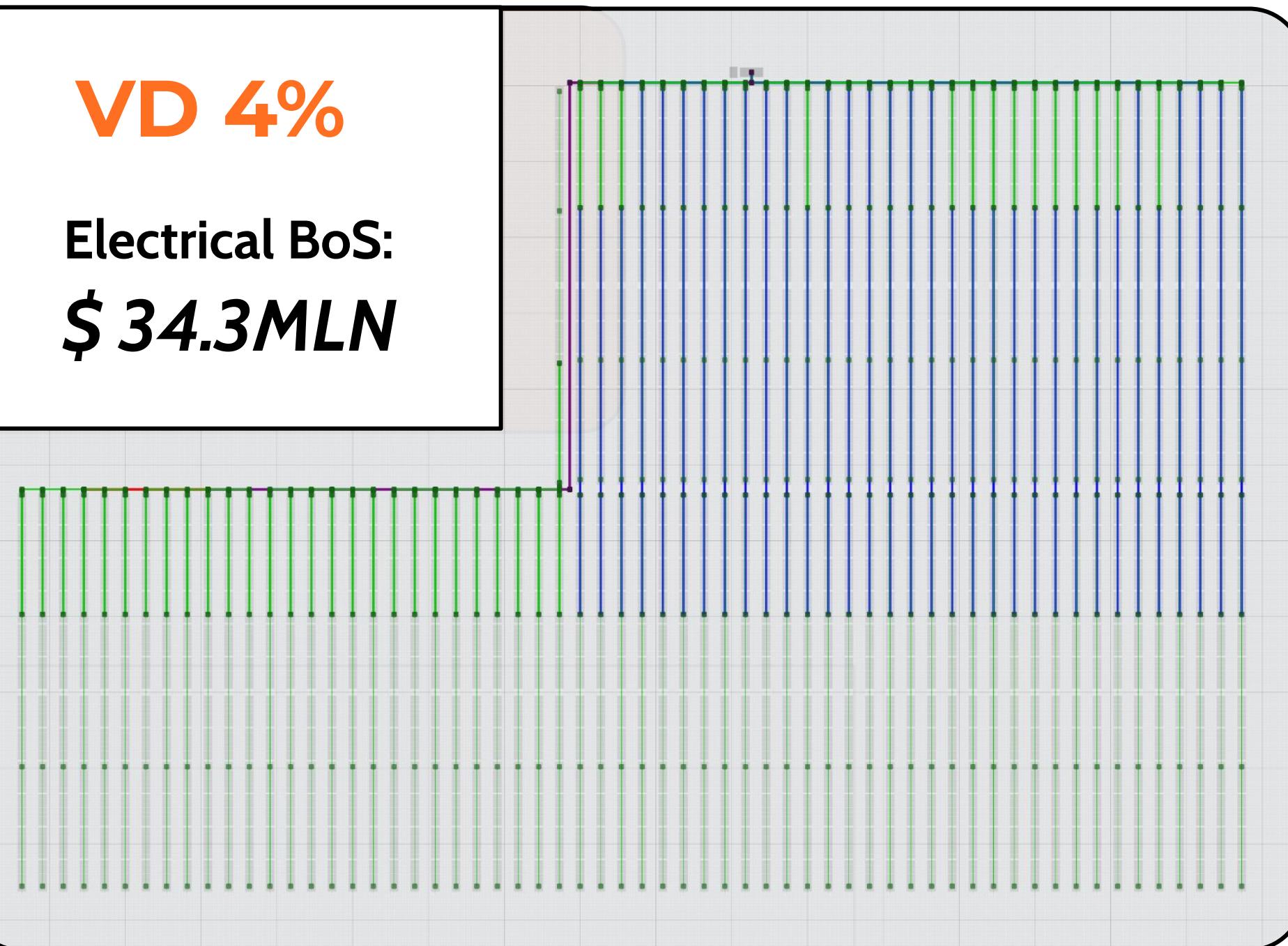
**CBX 400A**



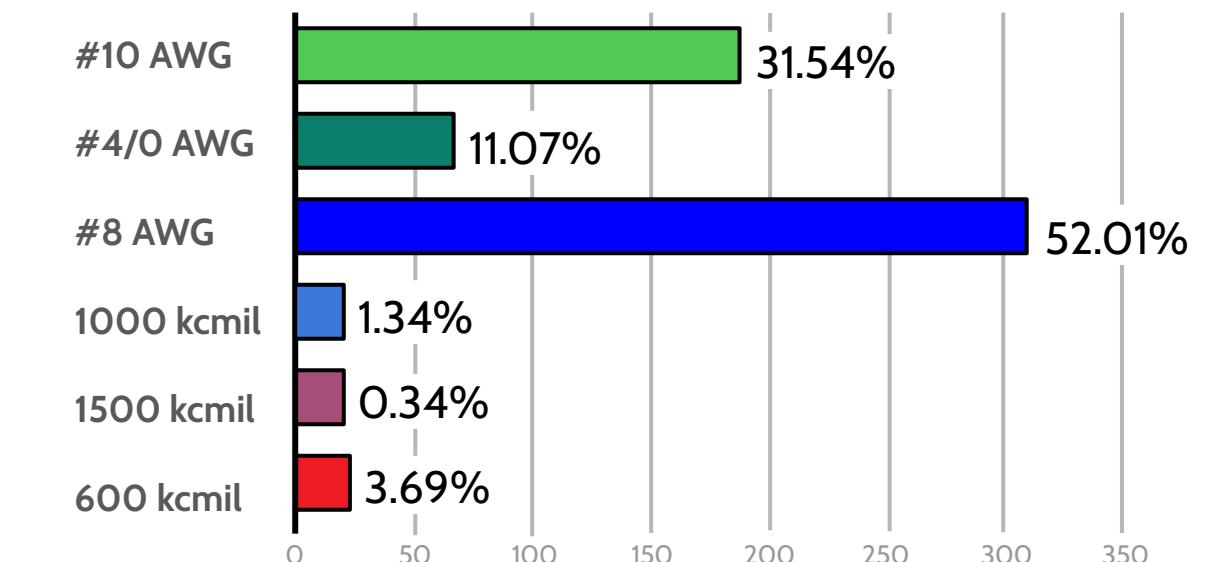
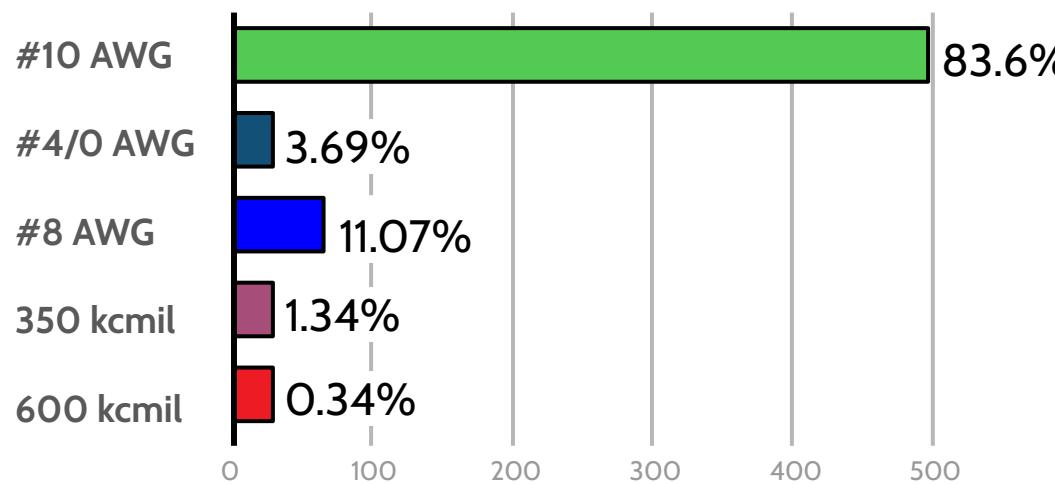
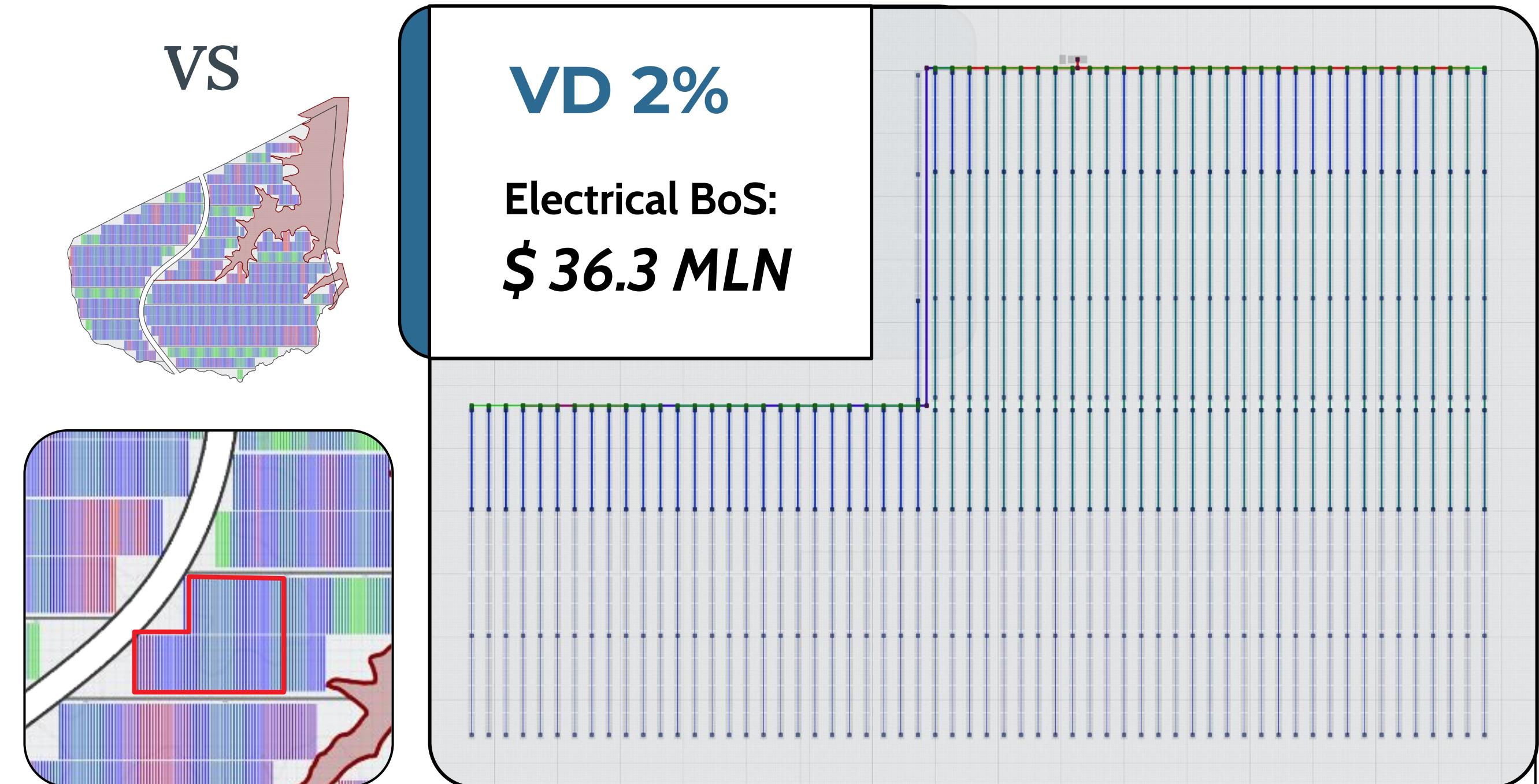
**Inverter  
3600kVA**

**VD 4%**

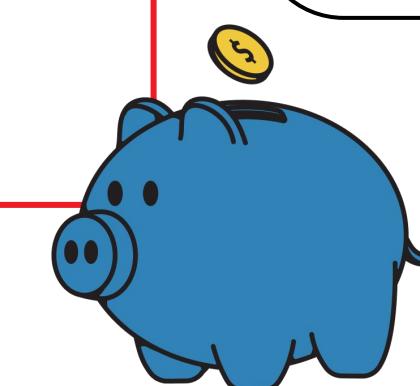
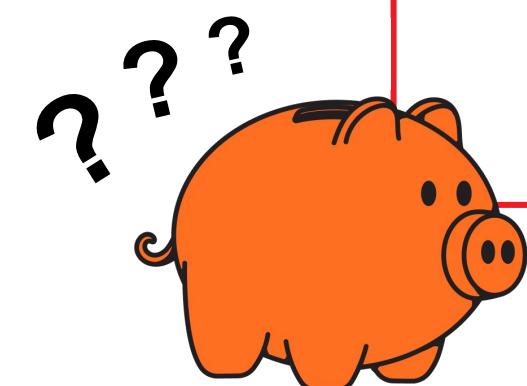
Electrical BoS:  
**\$ 34.3MLN**

**VS****VD 2%**

Electrical BoS:  
**\$ 36.3 MLN**



**A 4% voltage drop saves \$2M in construction — but costs \$40M in lost energy over 30 years**



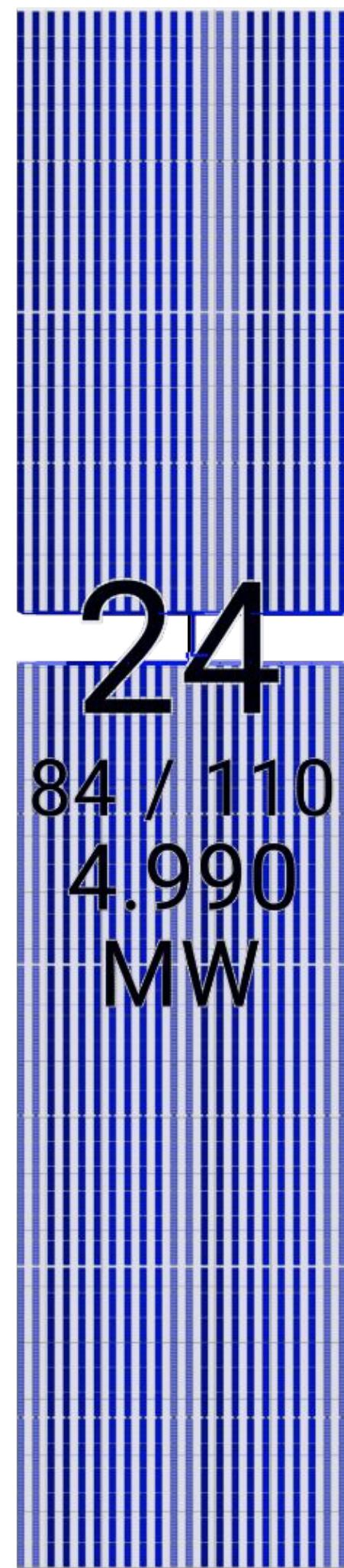


# The gap that costs millions

Bad Design

Good Design

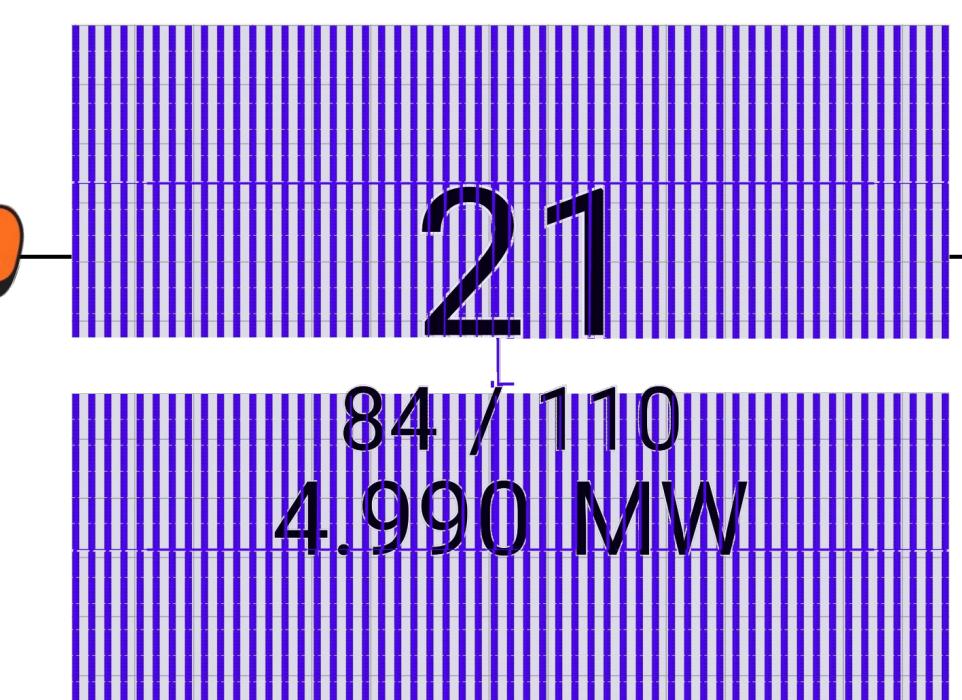
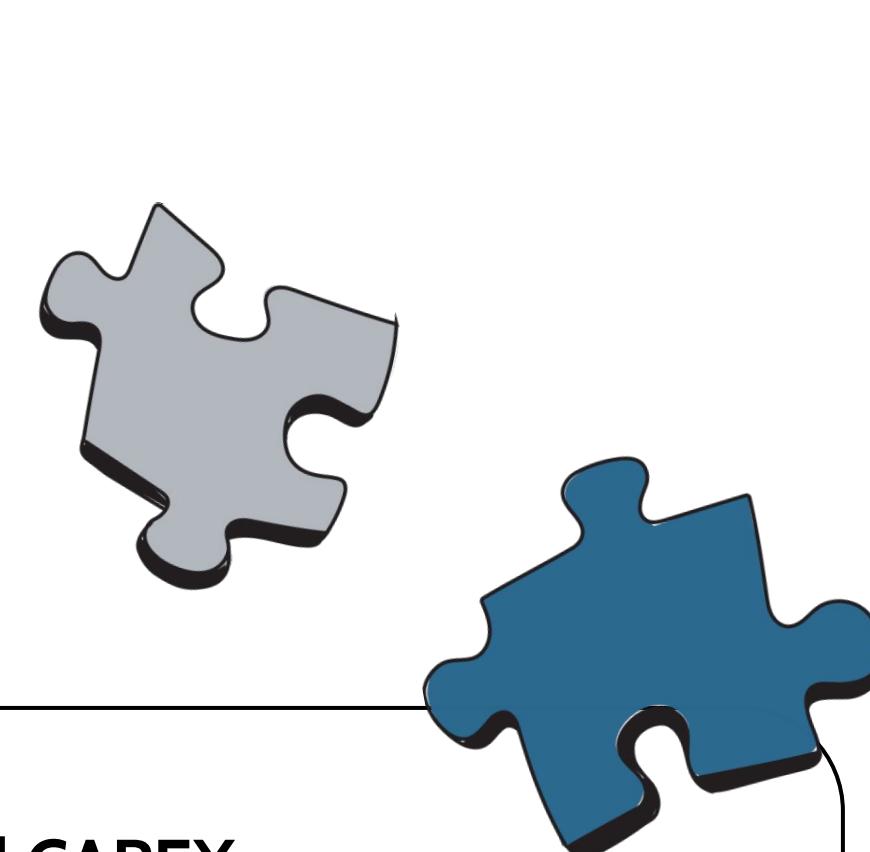
# \$1.3M EBOS Cost Swing per Block



Electrical CAPEX:  
**0.3802 \$/W**

LCOE, \$/MWh:  
**58.43 \$/MWh**

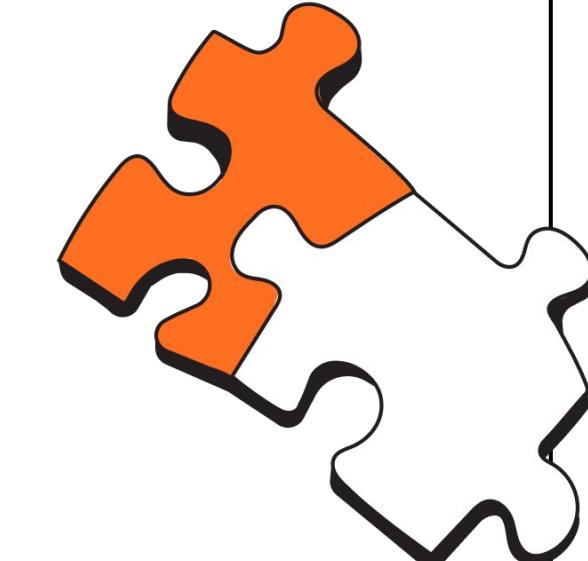
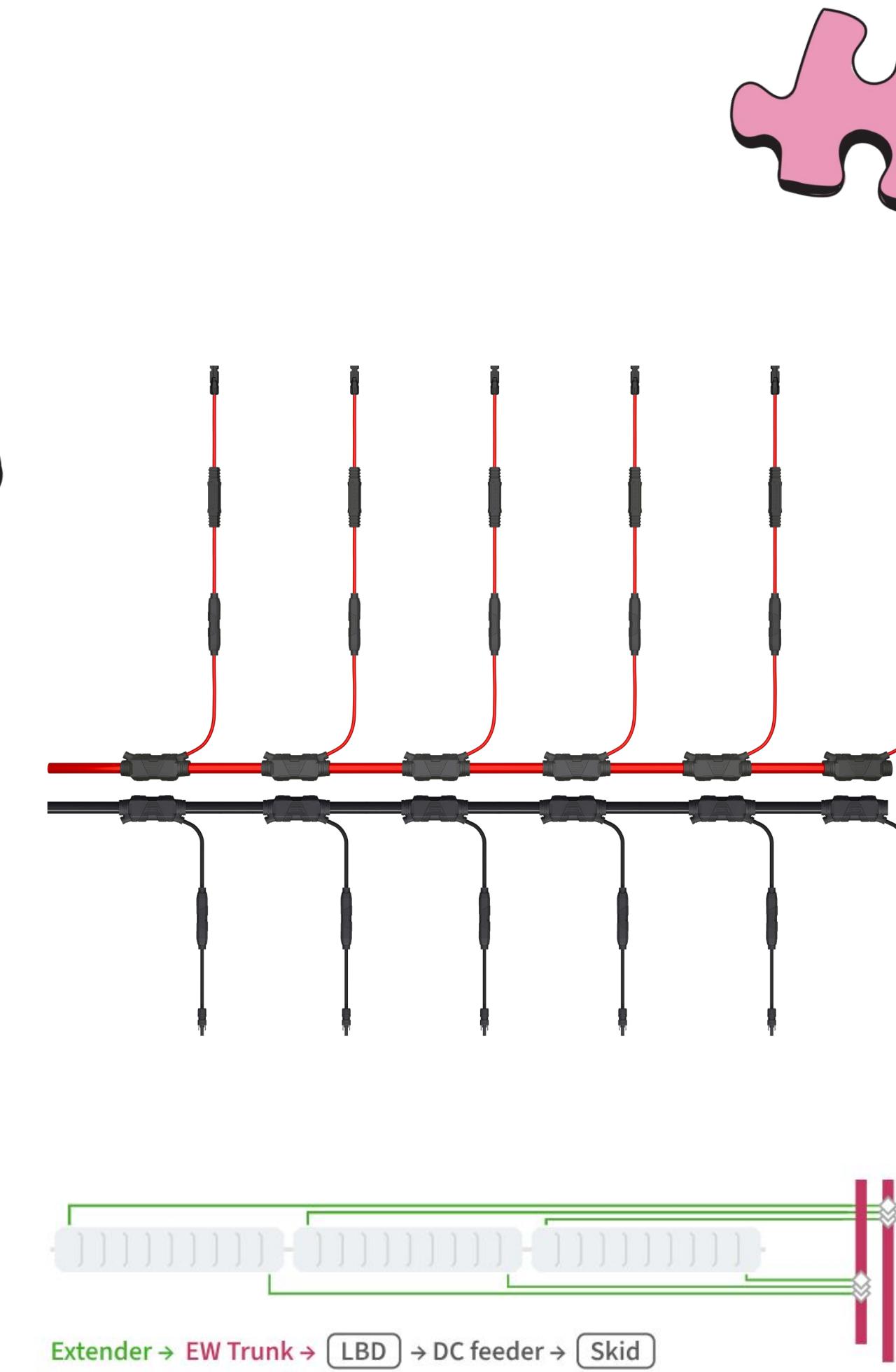
- Distributed  Clustered
- Partial  Full
- Middle of row  End of row
- Single array  Multi array

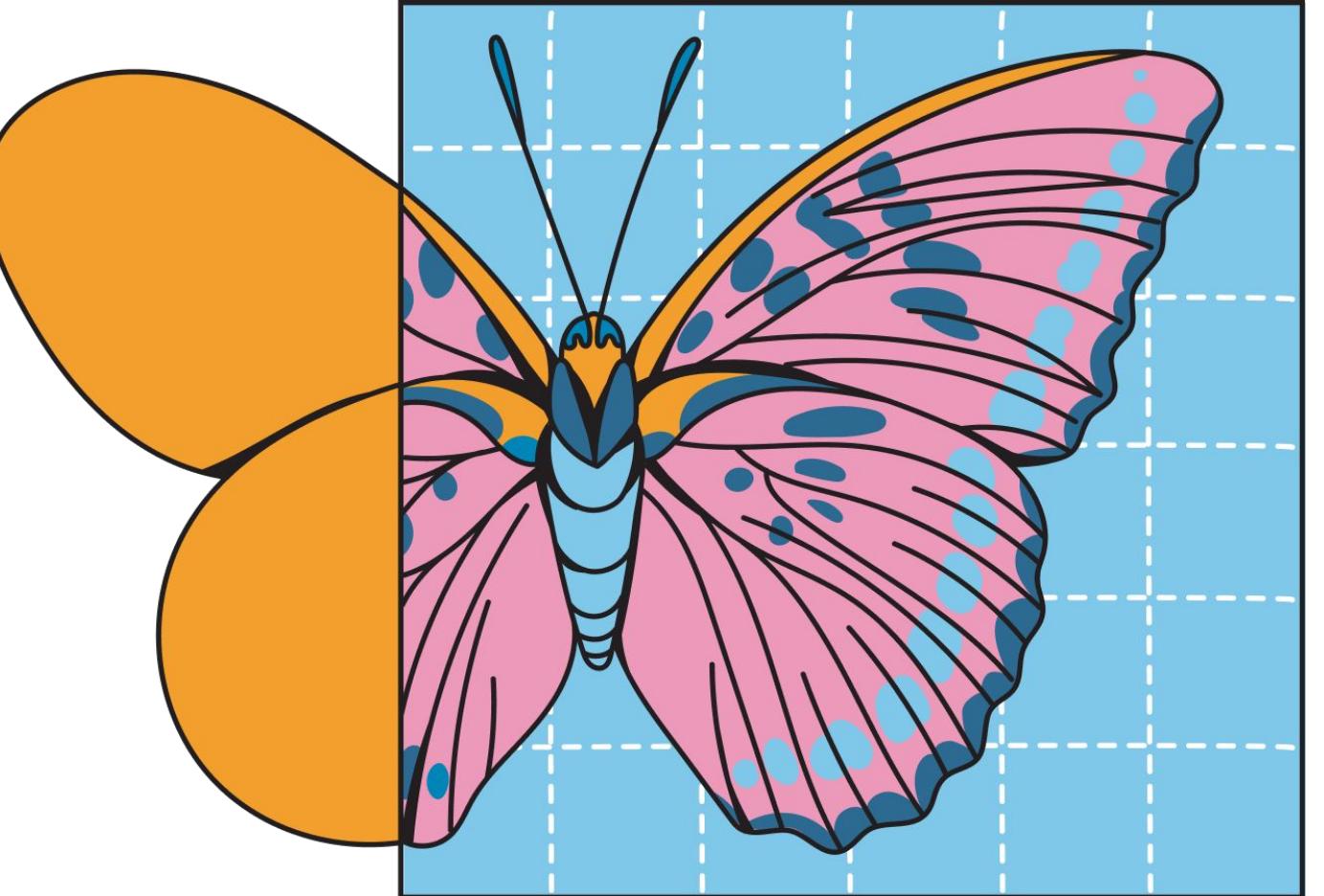


Electrical CAPEX:  
**0.1195 \$/W**

LCOE, \$/MWh:  
**49.23 \$/MWh**

- Distributed  Clustered
- Partial  Full
- Middle of row  End of row
- Single array  Multi array

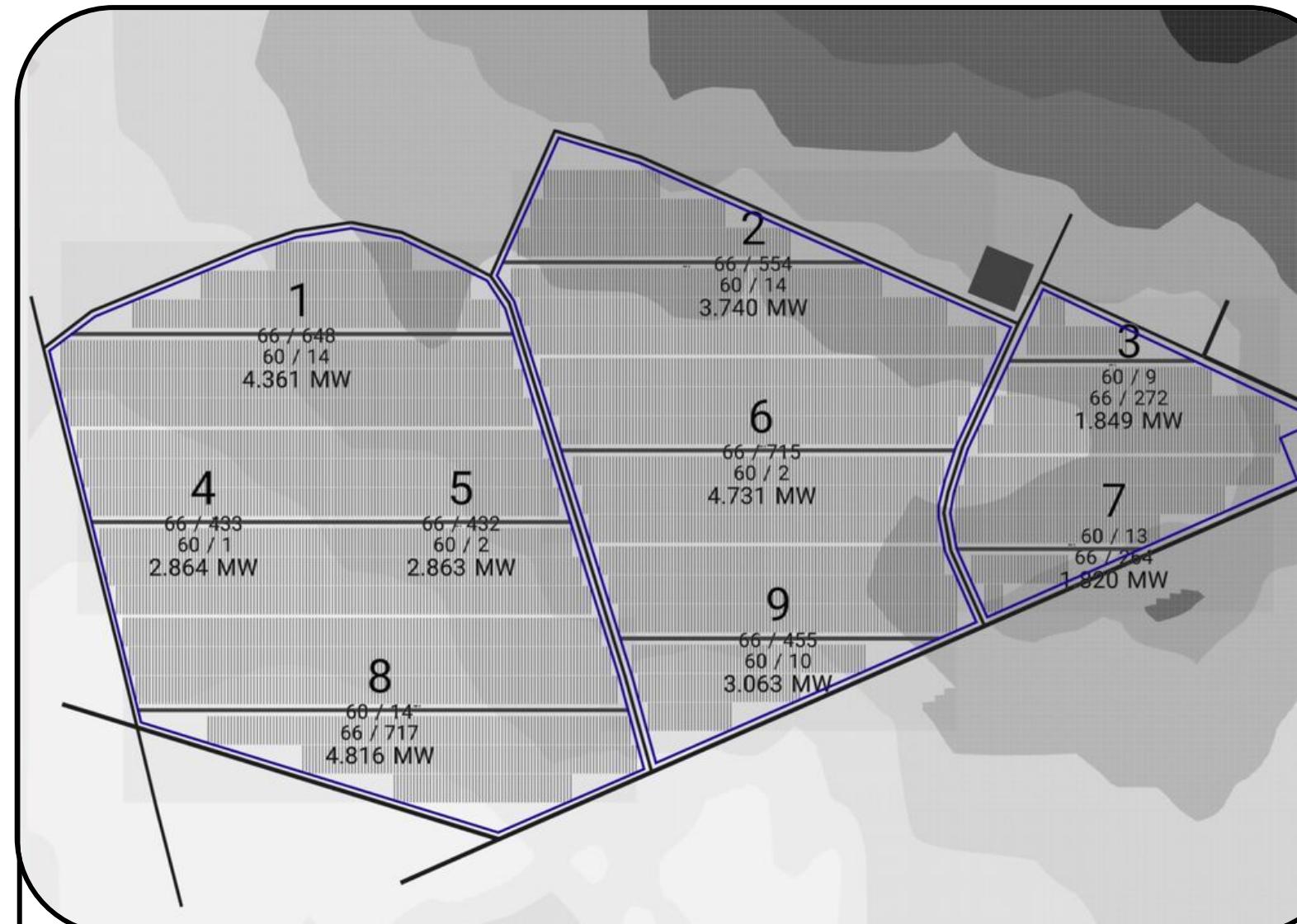




# Fast Evolving

# 10 Years Ago vs Now

## 10 Years Ago



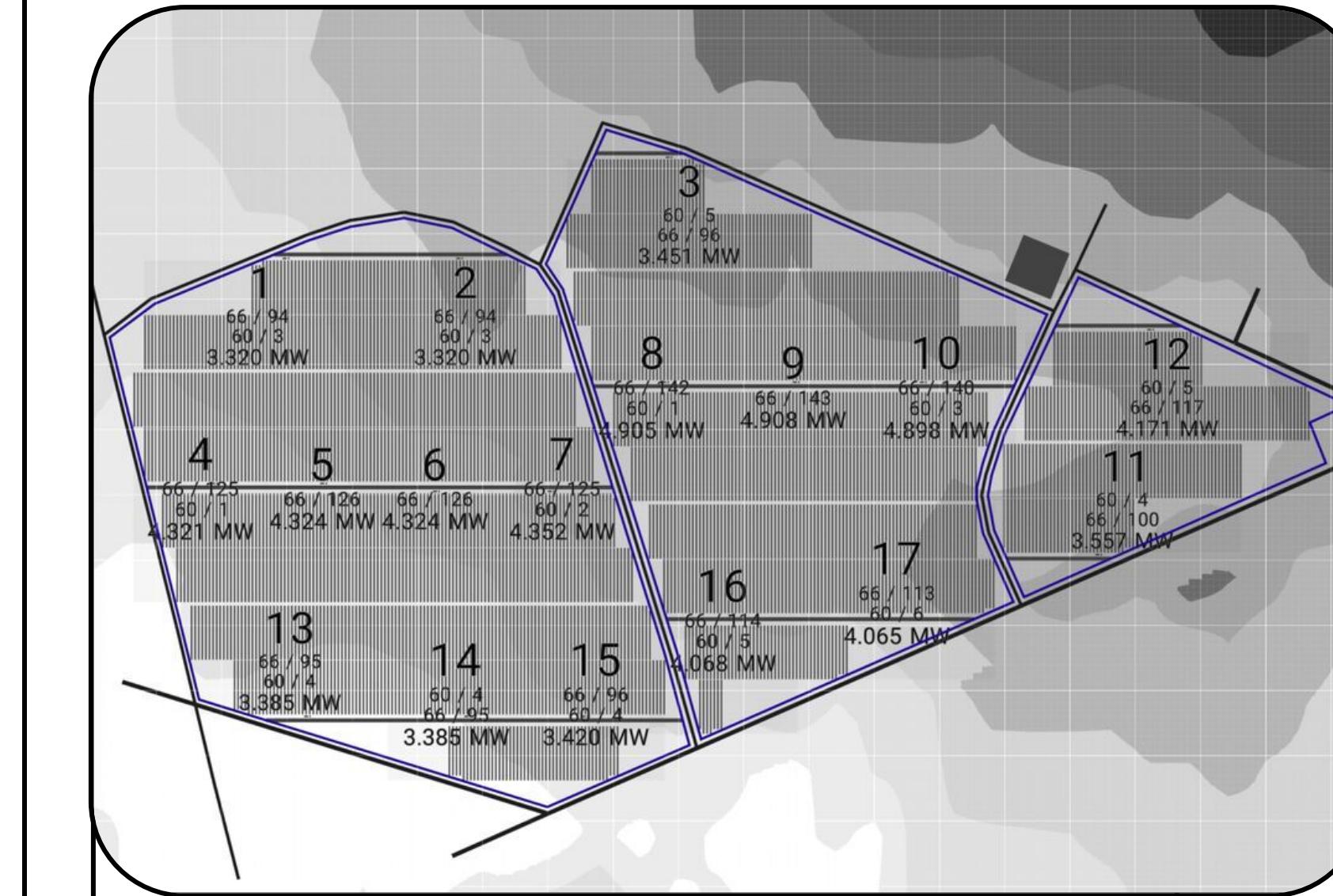
**Capacity: 30.1 MW**

**Energy: 26,816 MWh**

**Modules: 301,080**



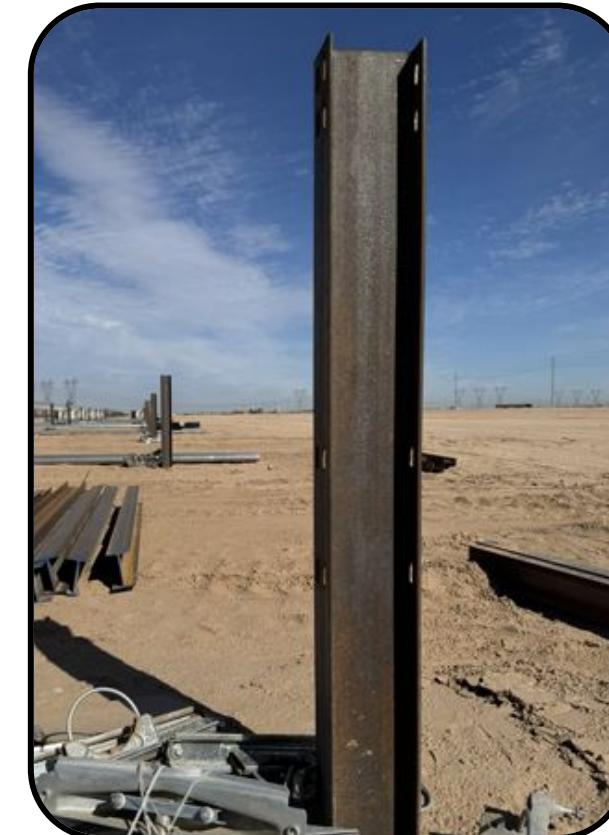
## Today

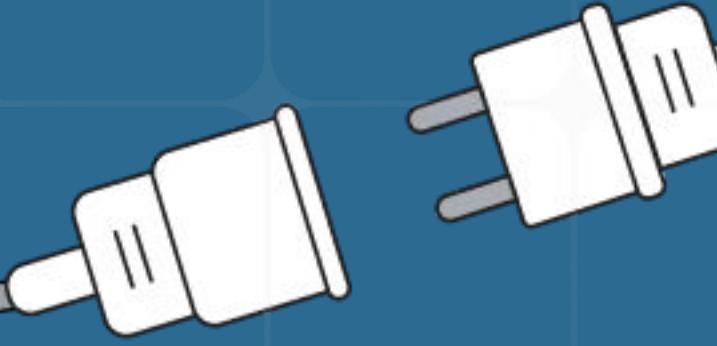


**Capacity: 68.2 MW**

**Energy: 75,857 MWh**

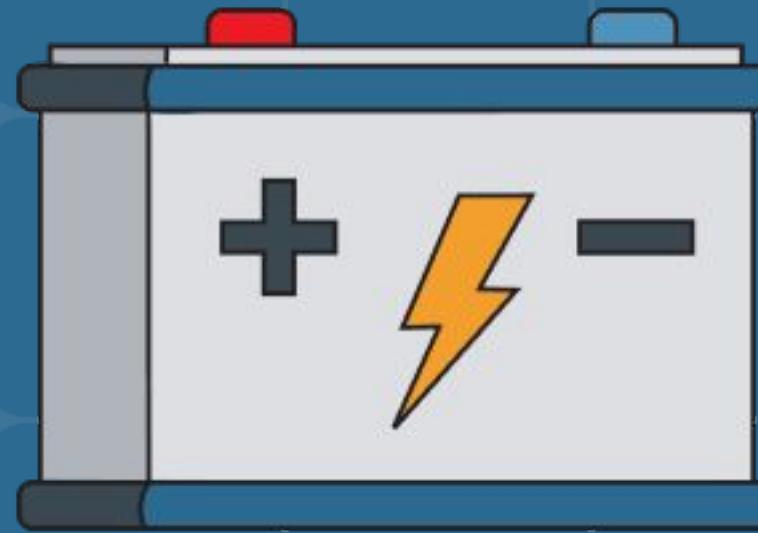
**Modules: 131,106**

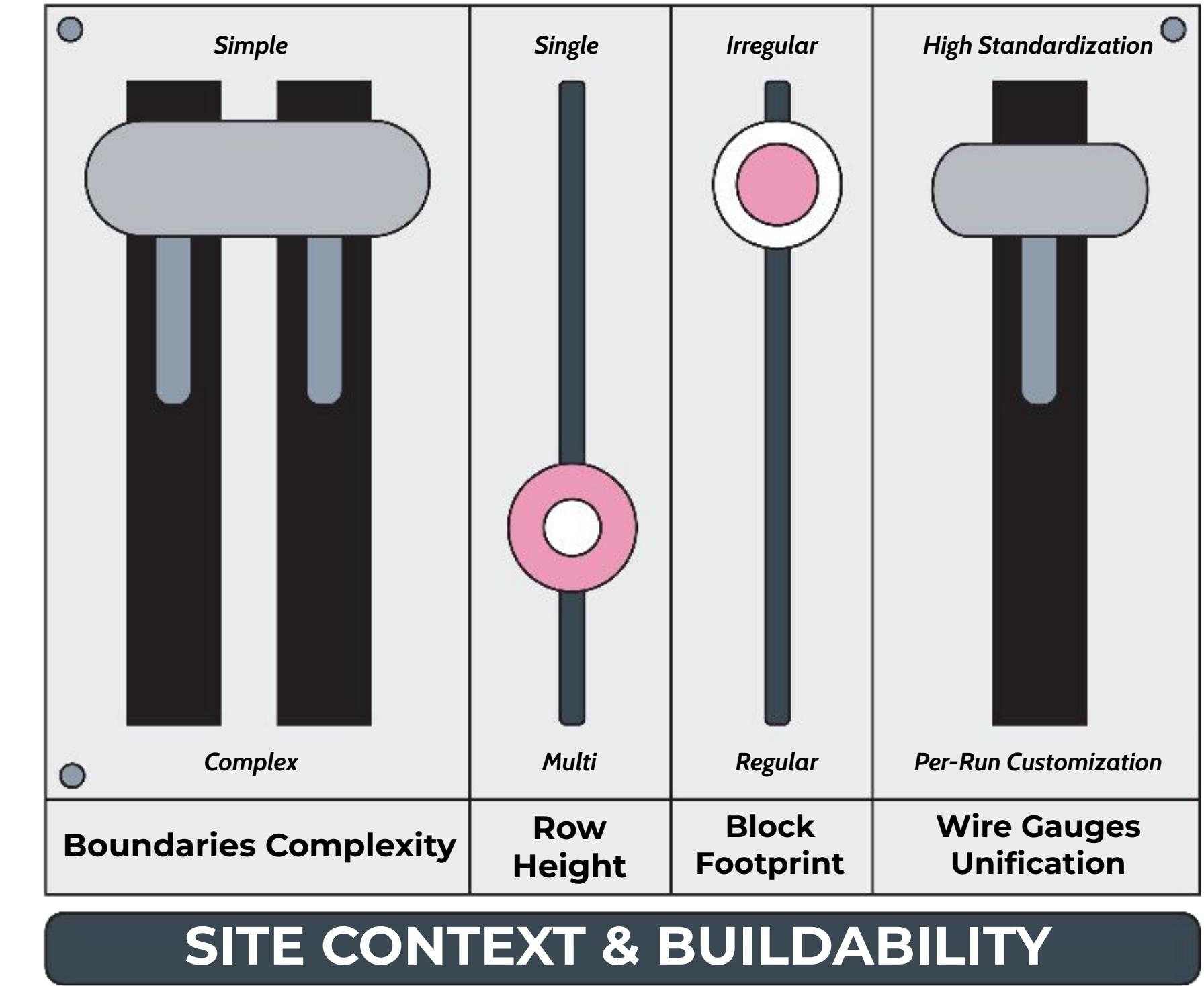
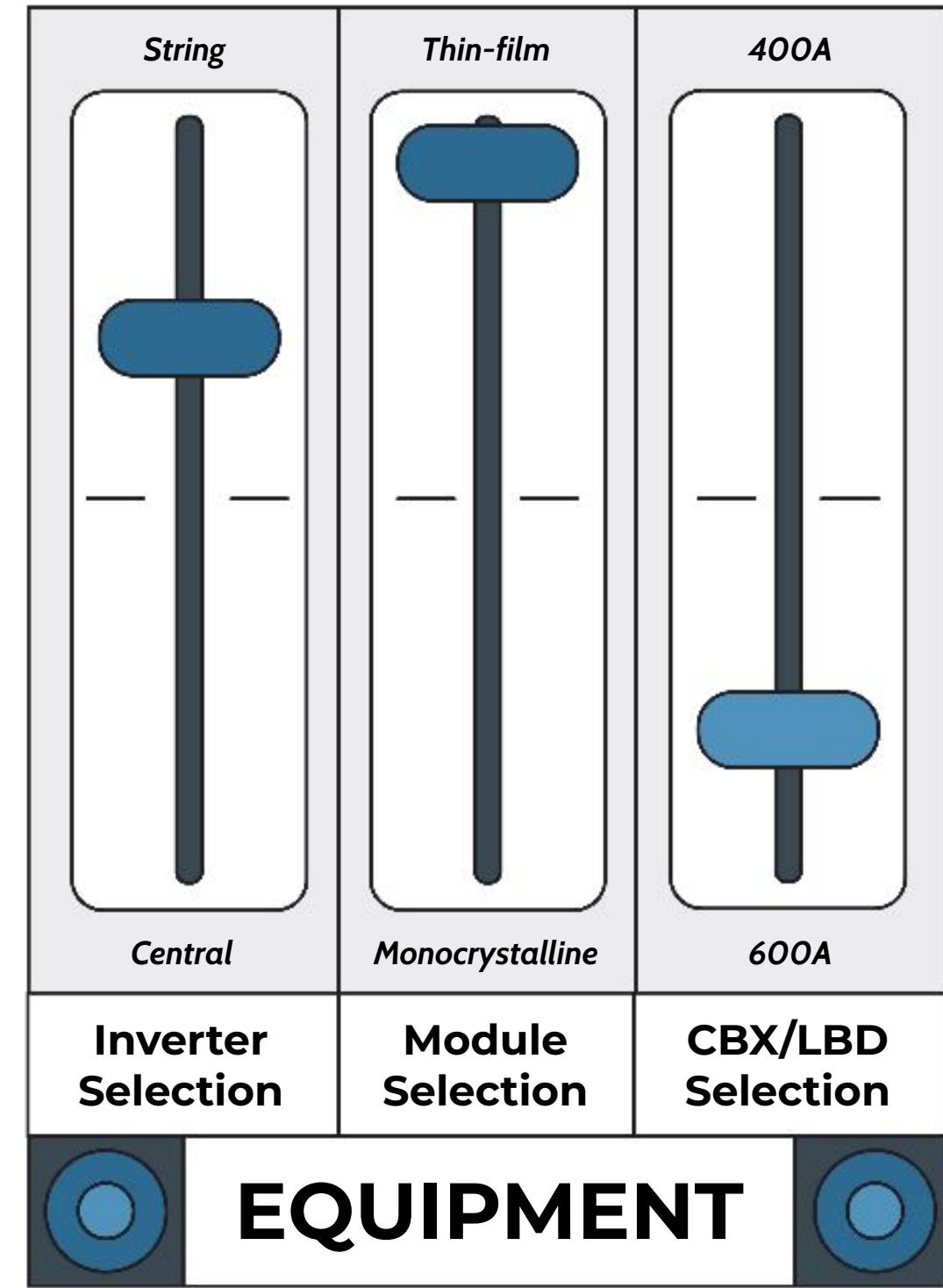
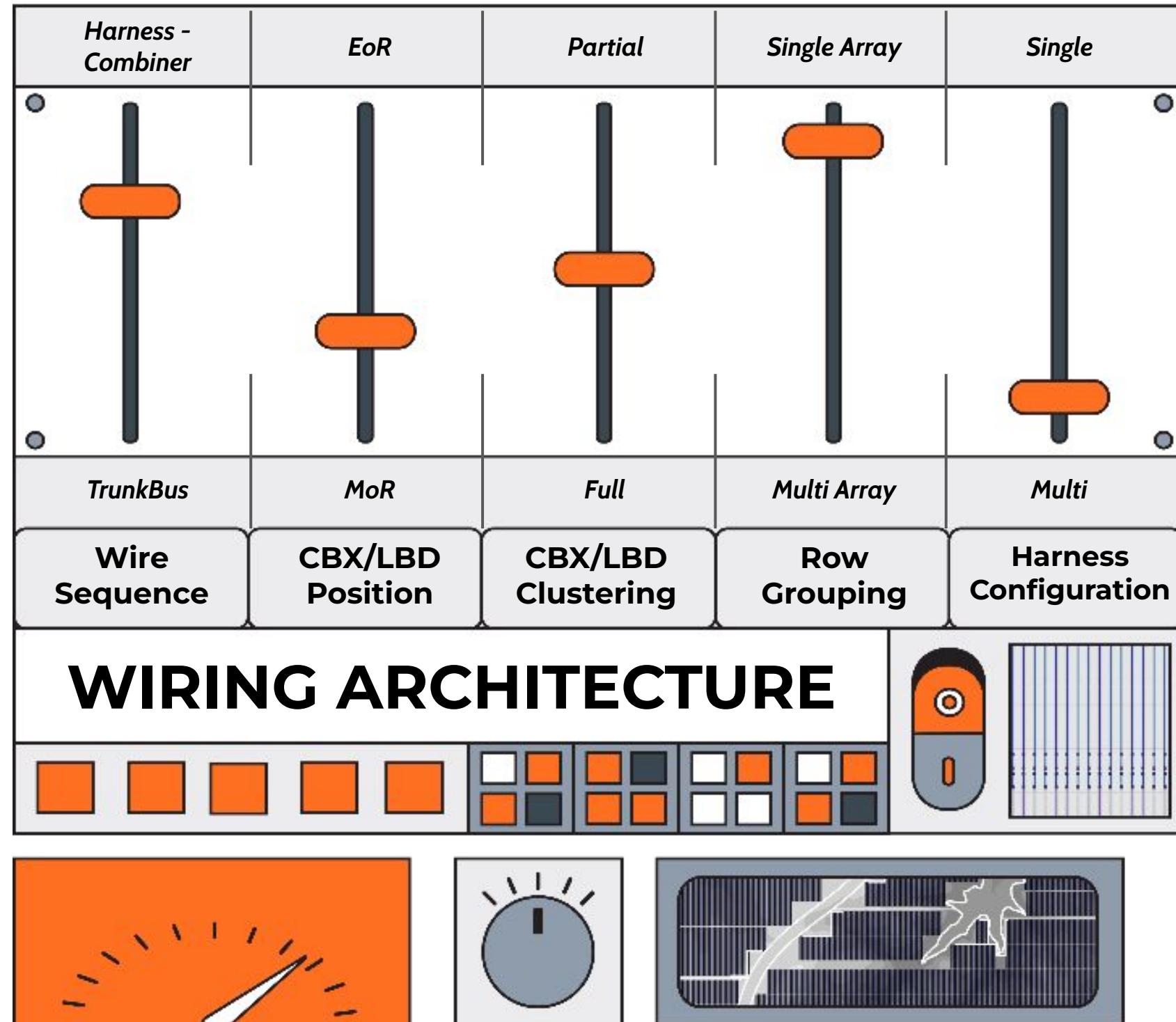
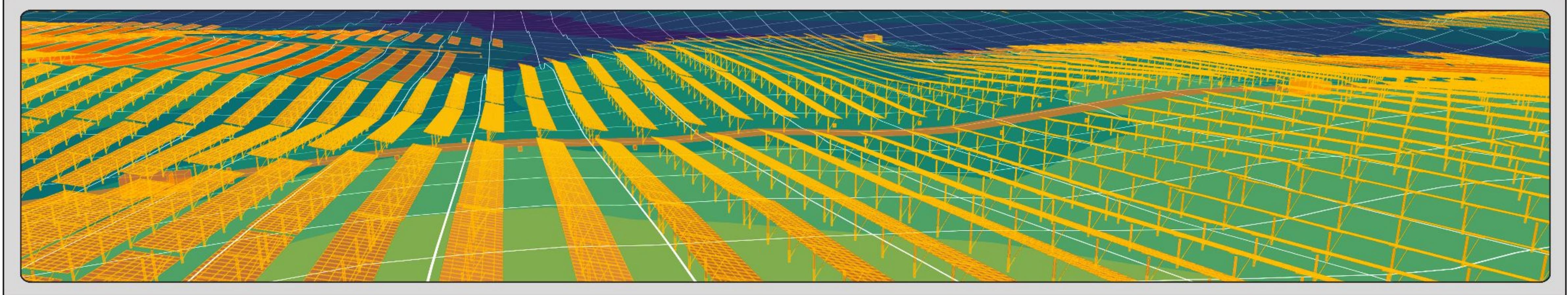




# Electrical Parameters

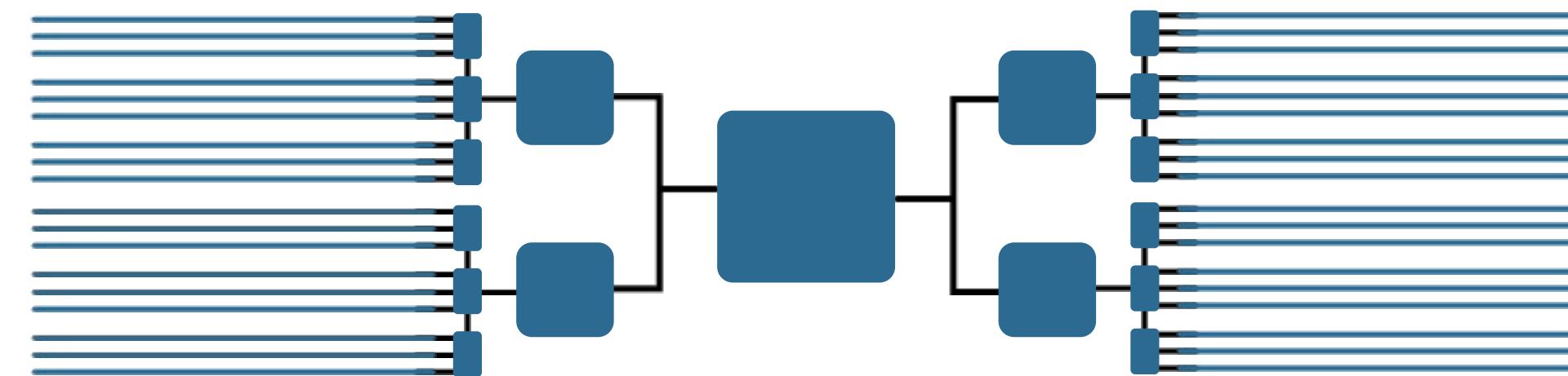
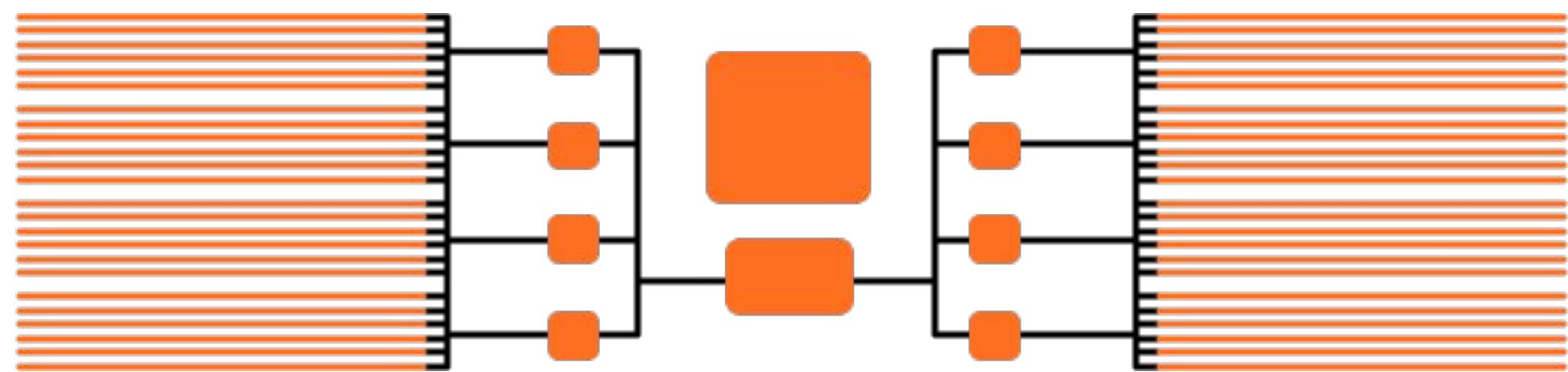
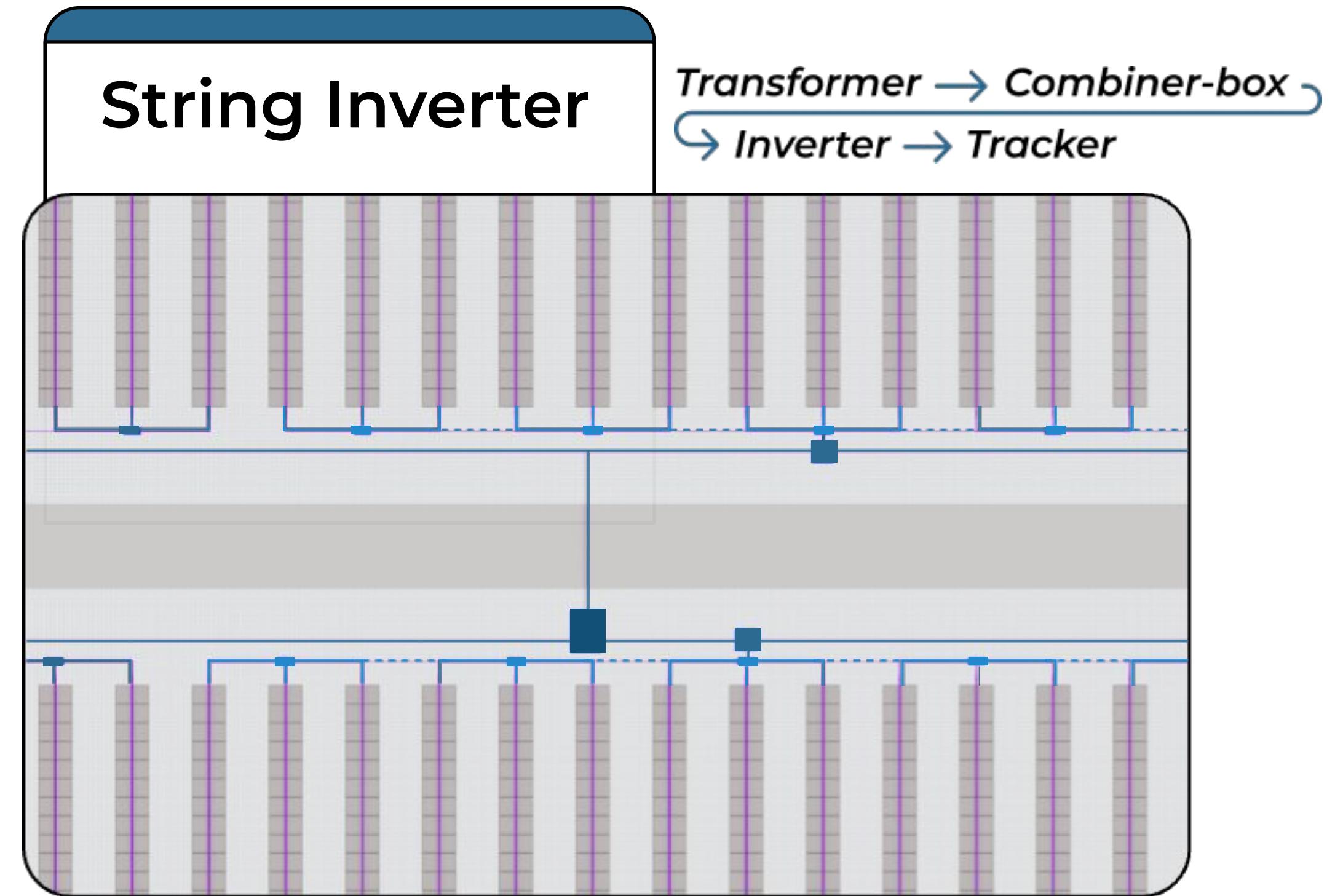
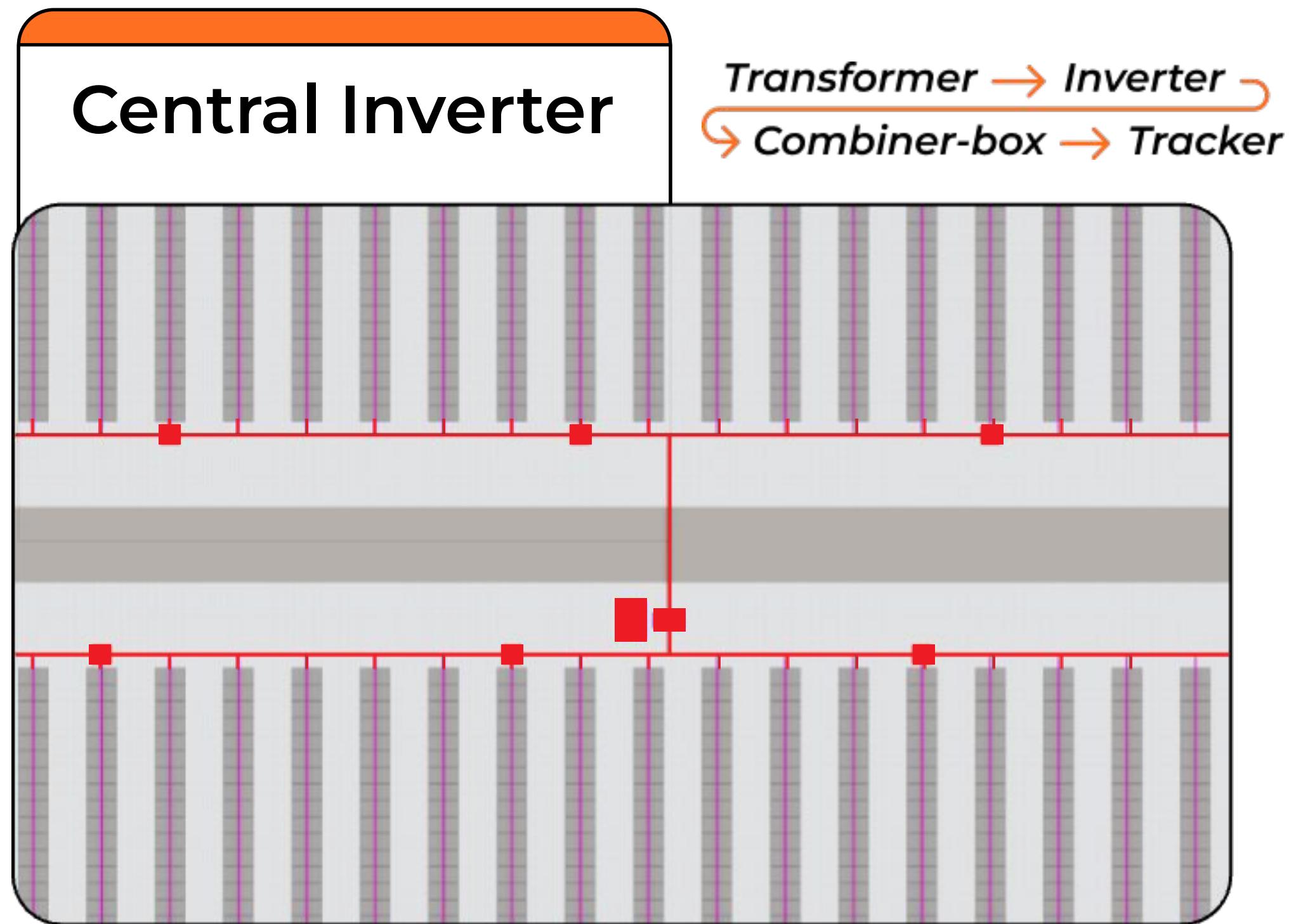
*Knobs you turn to shape outcomes - guiding energy, cost, and complexity with every choice*





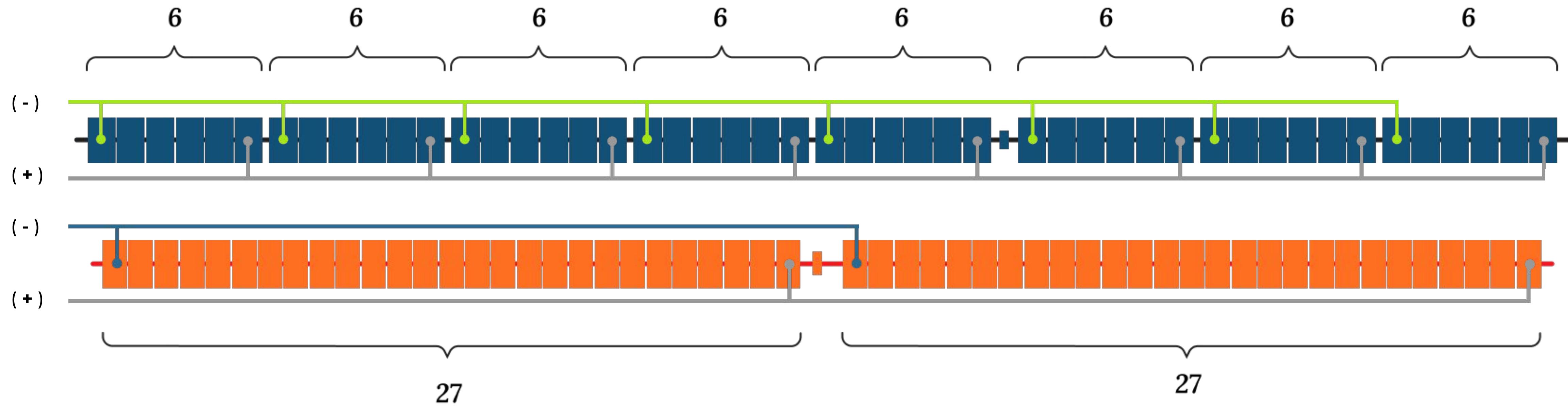
# 1 Inverter Selection

Inverter power, Inverter type



# 2 Module Selection

## Module Power, Module type



### Thin-film

**First Solar**  
String size: *6 mods*  
Total: *54 mods*  
23.2kW tracker power

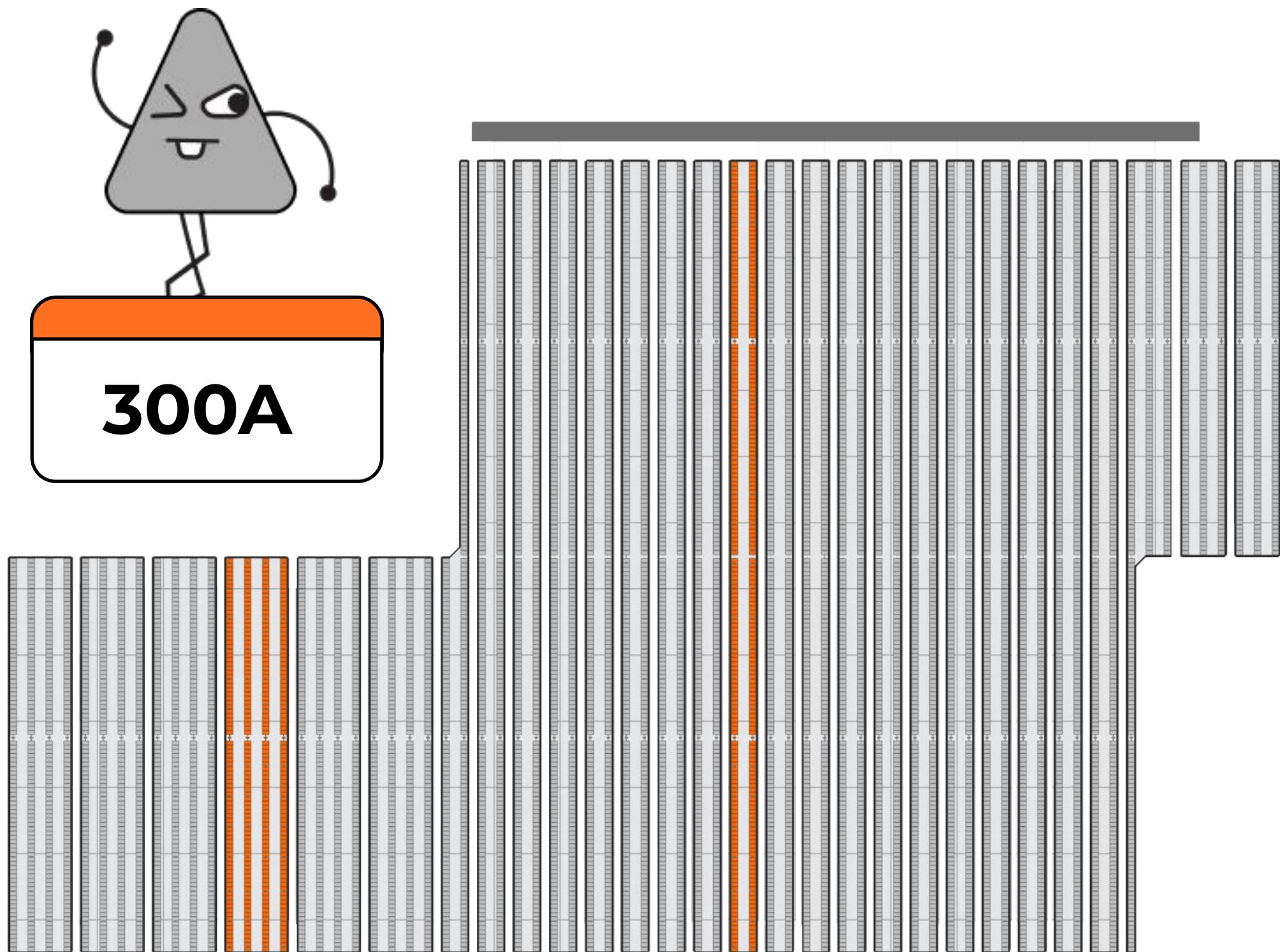
### Monocrystalline

**Canadian Solar**  
String size: *27 mods*  
Total: *54 mods*  
21.9kW tracker power

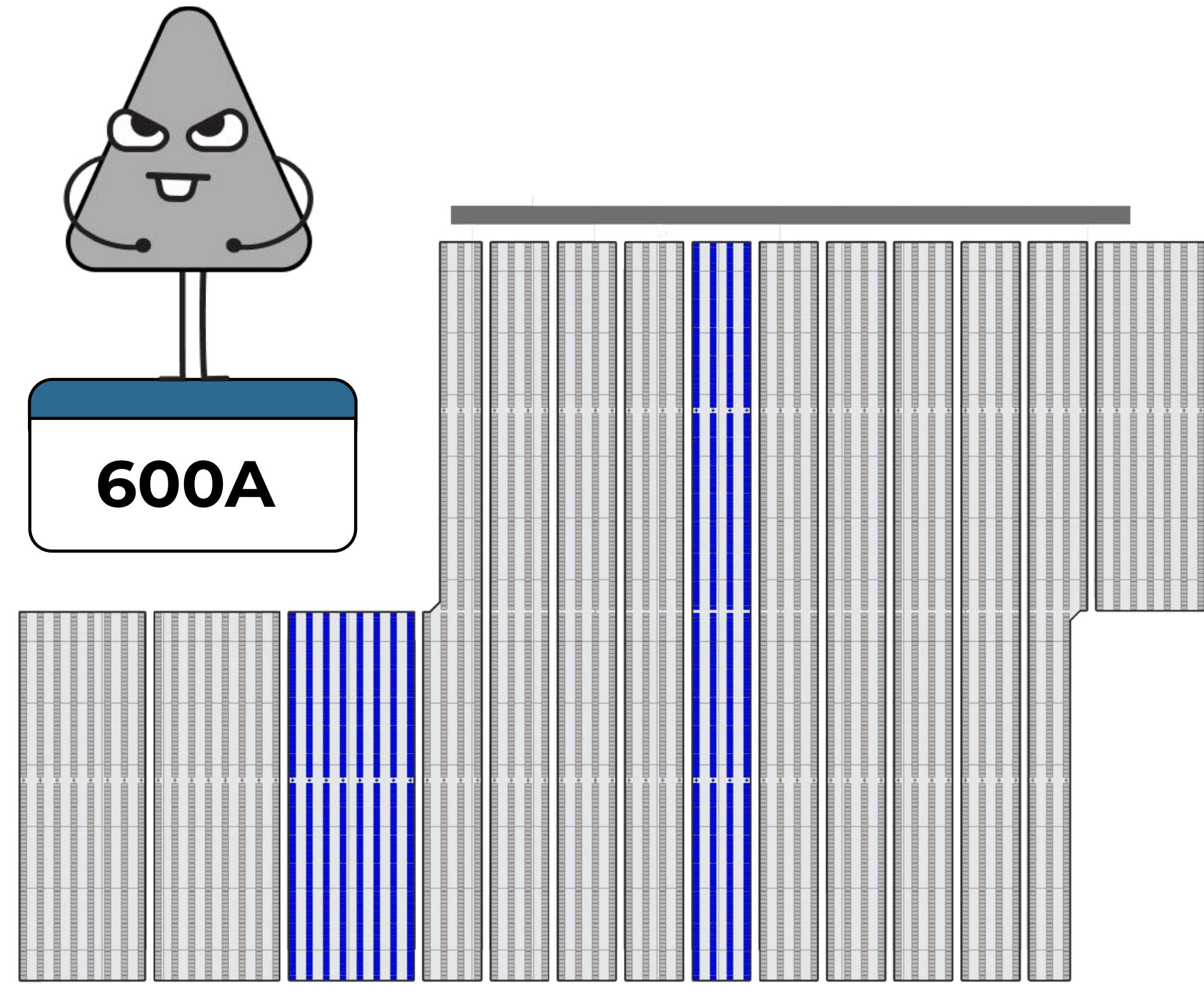
In the Harness scheme, electrical costs are higher with thin-film modules due to the greater labor intensity of installing the Harness (more strings need to be connected). In the Trunk Bus scheme, electrical costs are higher with thin-film modules due to the greater number of Extenders.

# 3 CBX/LBD Selection

*Ampacity*



VS



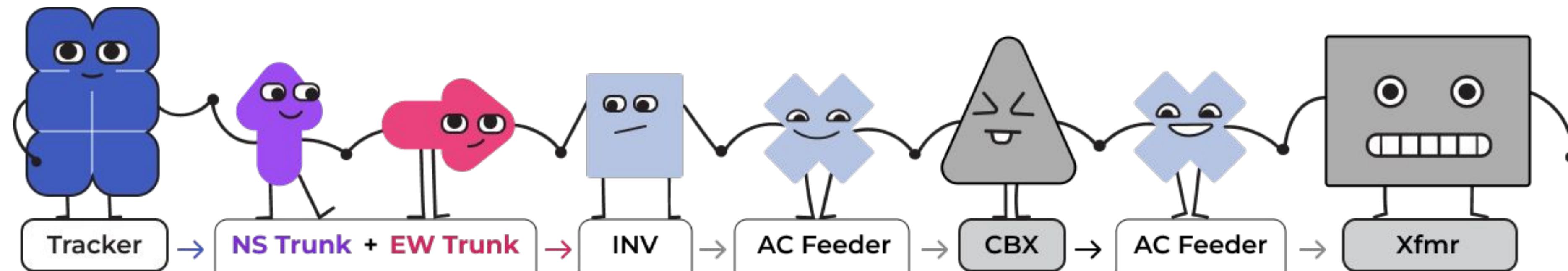
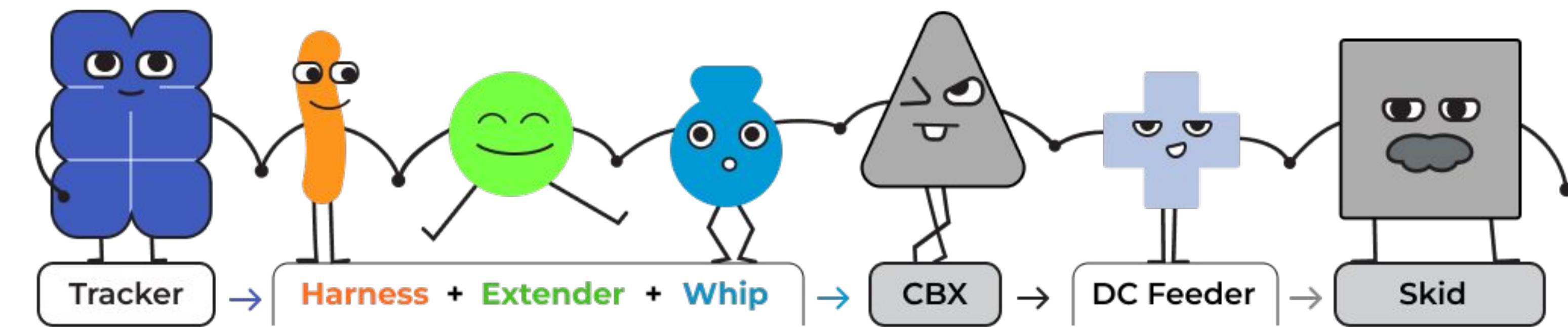
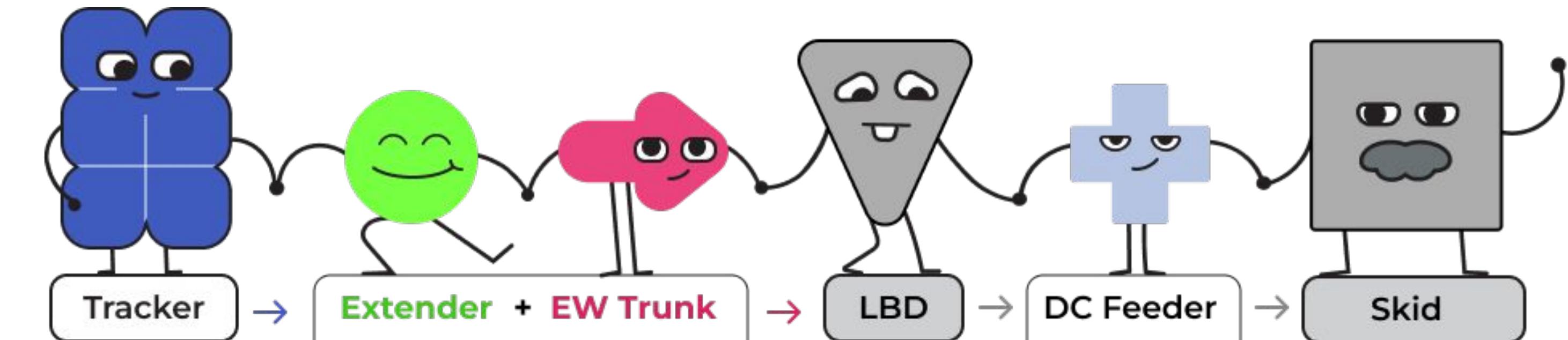
# 4 Wire Sequence

## Order of connection

Shorter length → less loss.

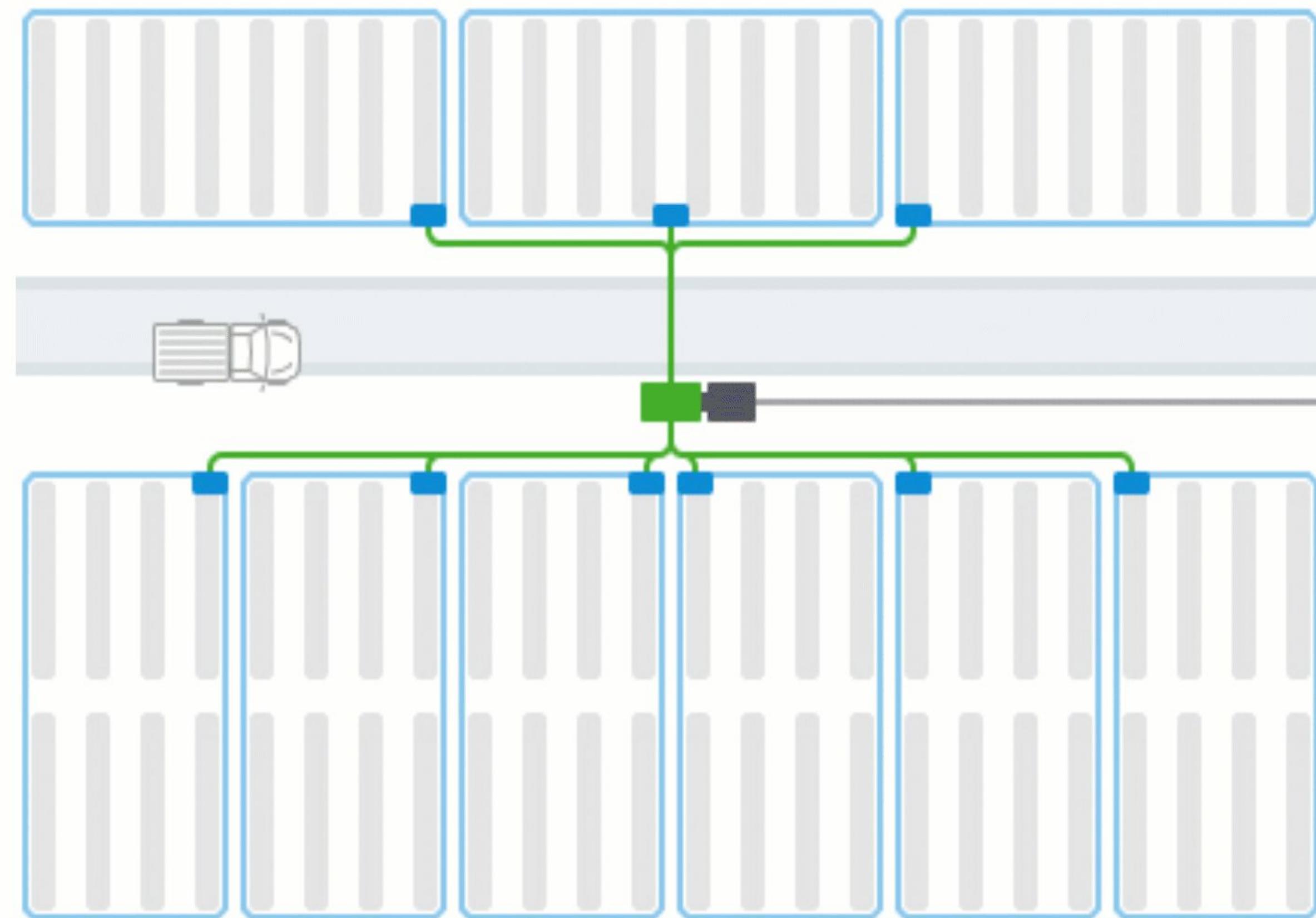
Less loss → smaller cross-section.

Smaller cross-section → lower cost.



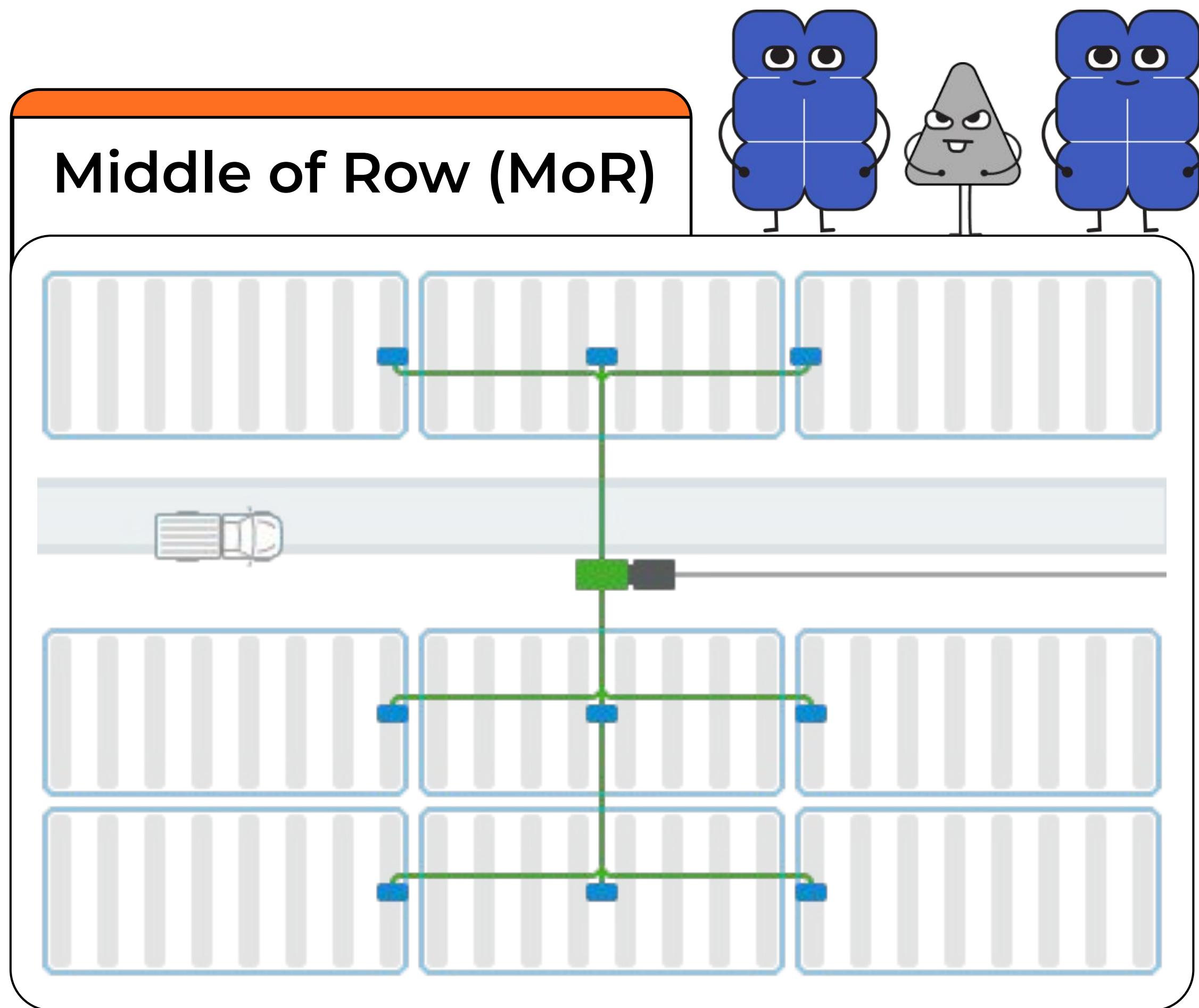
# CBX/LBD Features

Distributed	<input type="checkbox"/>	Clustered
Partial	<input checked="" type="checkbox"/>	Full
Middle of row	<input type="checkbox"/>	<b>End of row</b>
Single array	<input type="checkbox"/>	<b>Multi array</b>

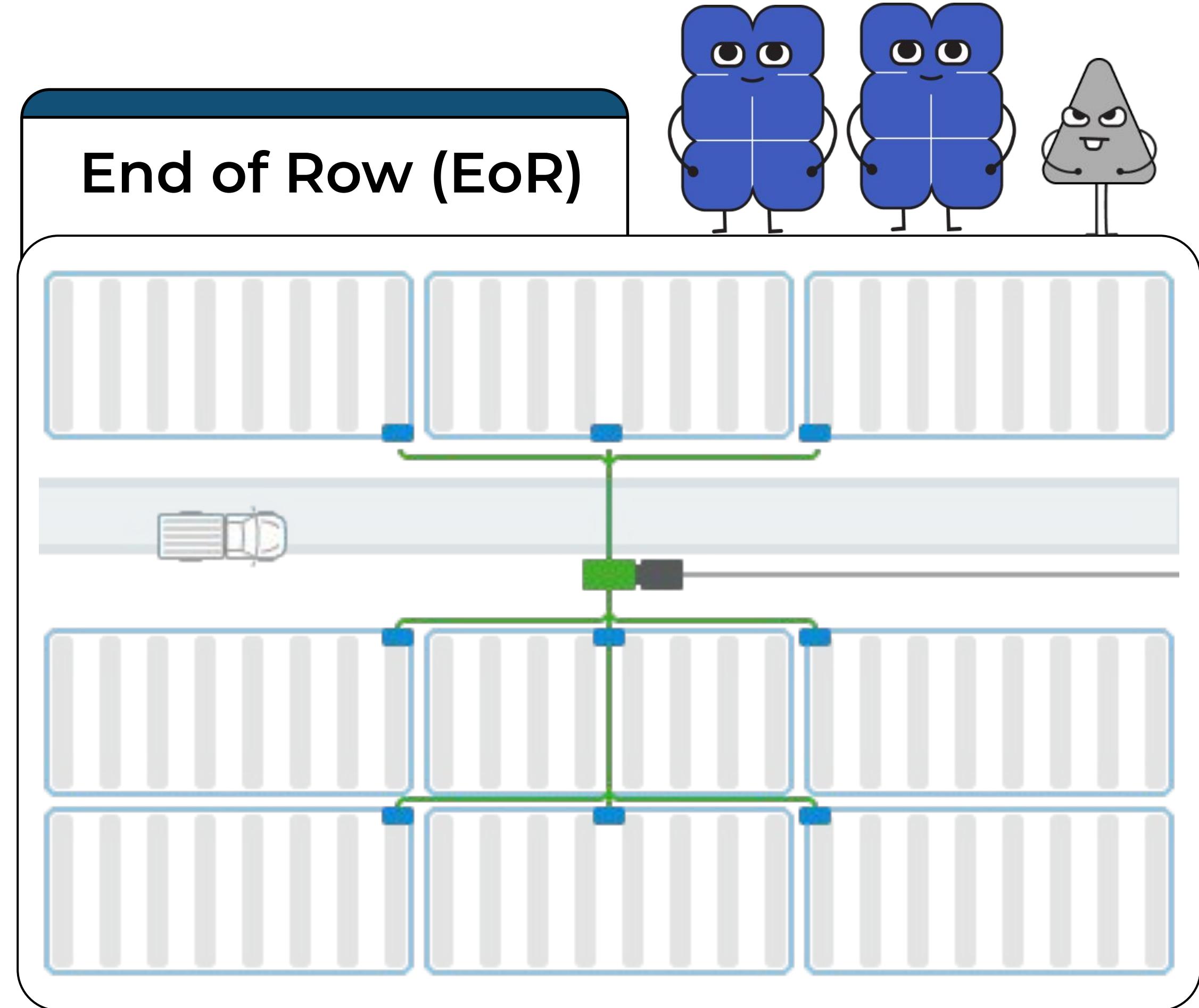


# 5 CBX/LBD Position

*Affects trench lengths, voltage drop, and block connectivity*

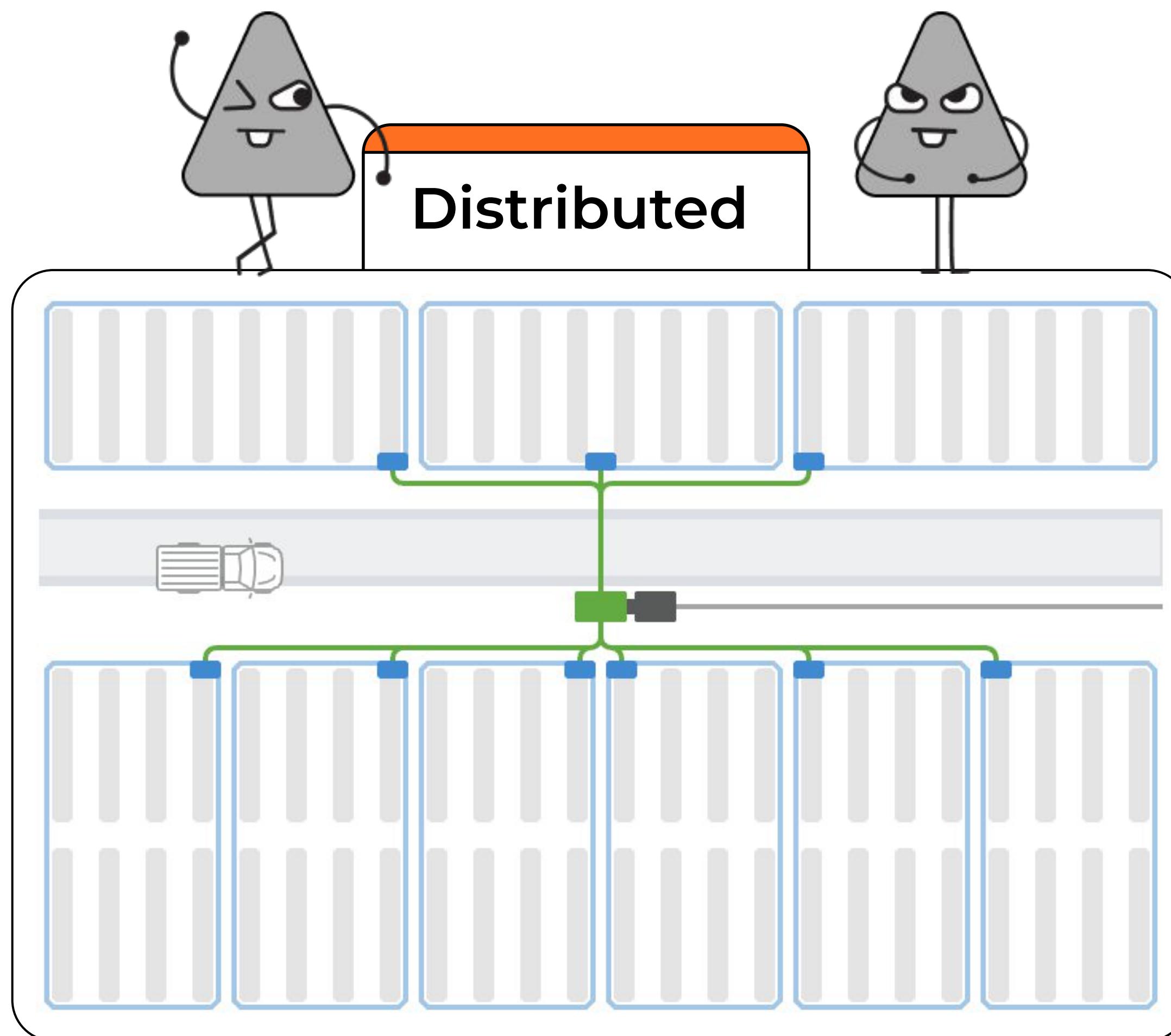


VS

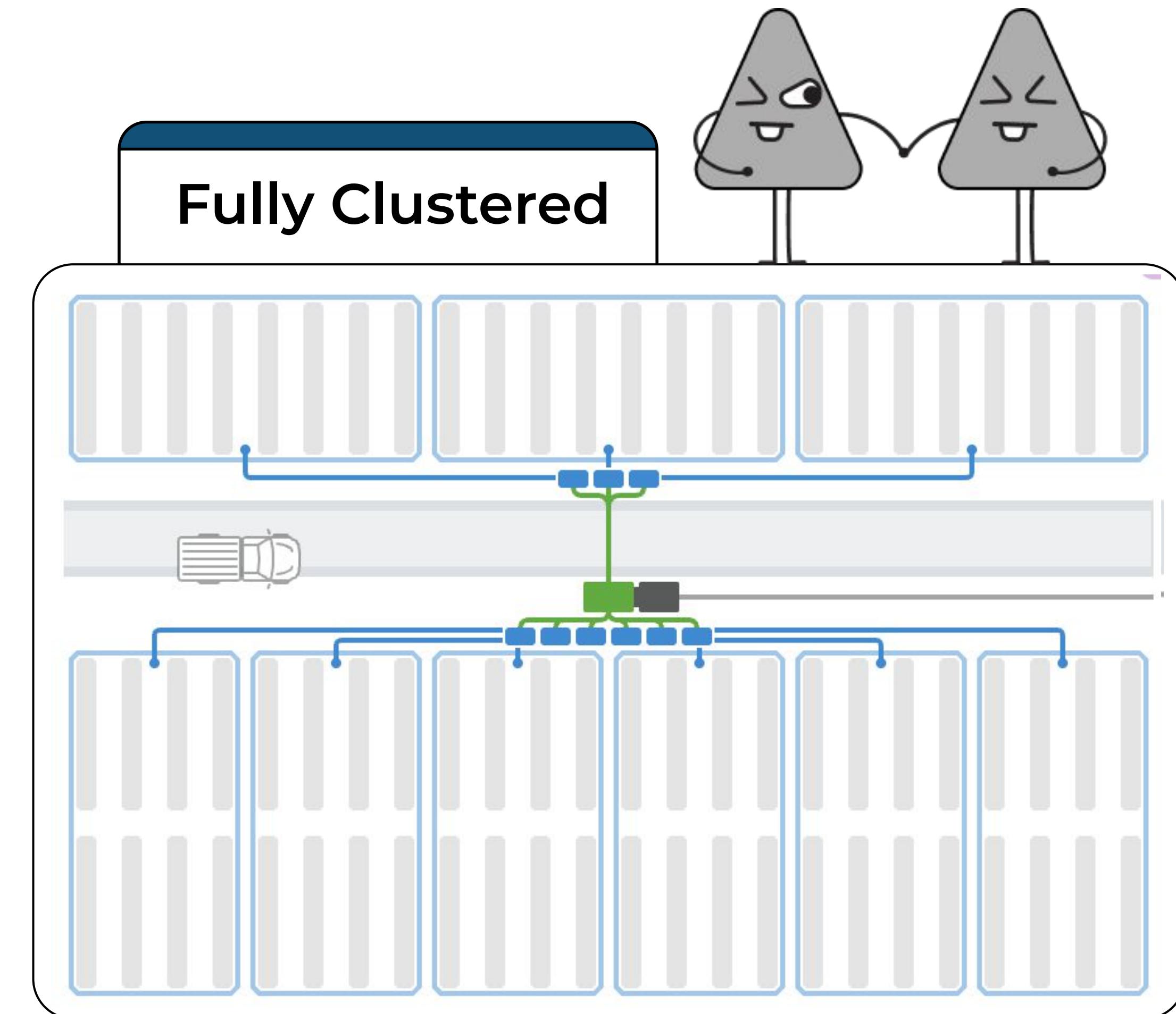


# 6 CBX/LBD Clustering

Drives concentration of terminations  
and trench convergence points

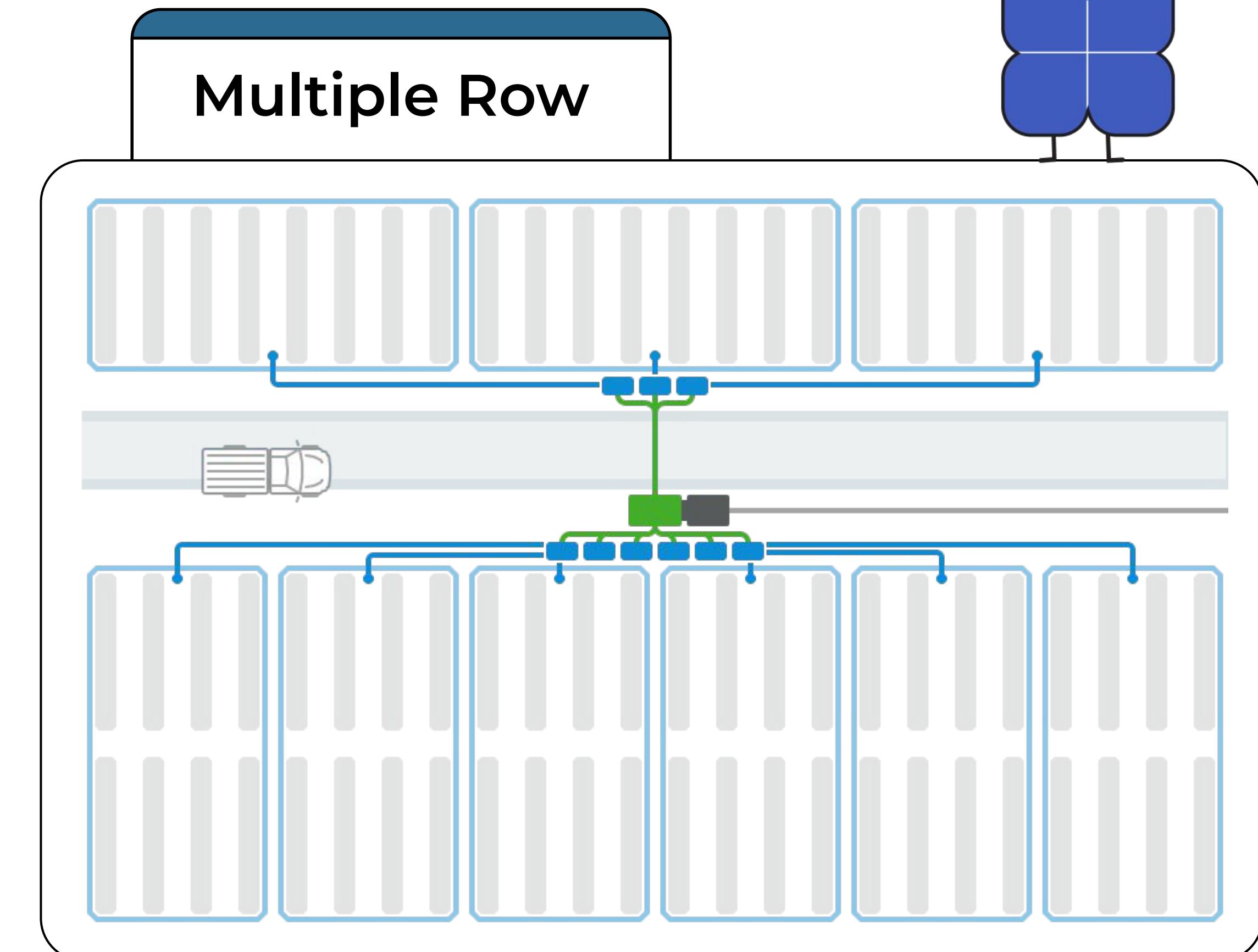
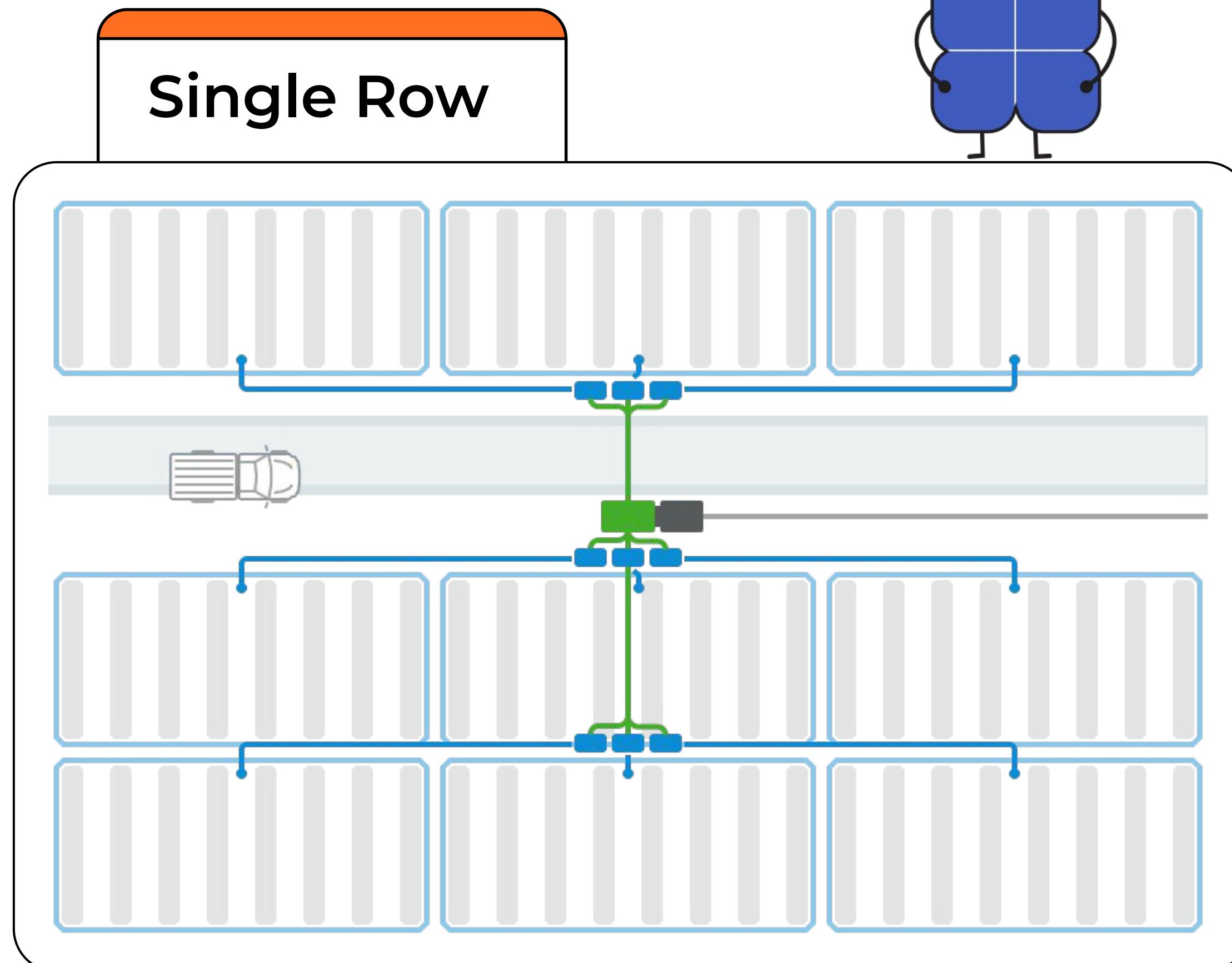


VS

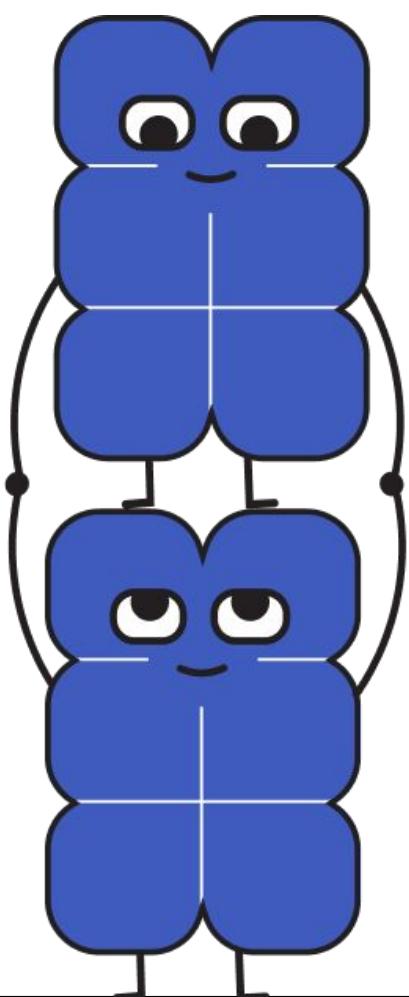


# 7 Row Grouping

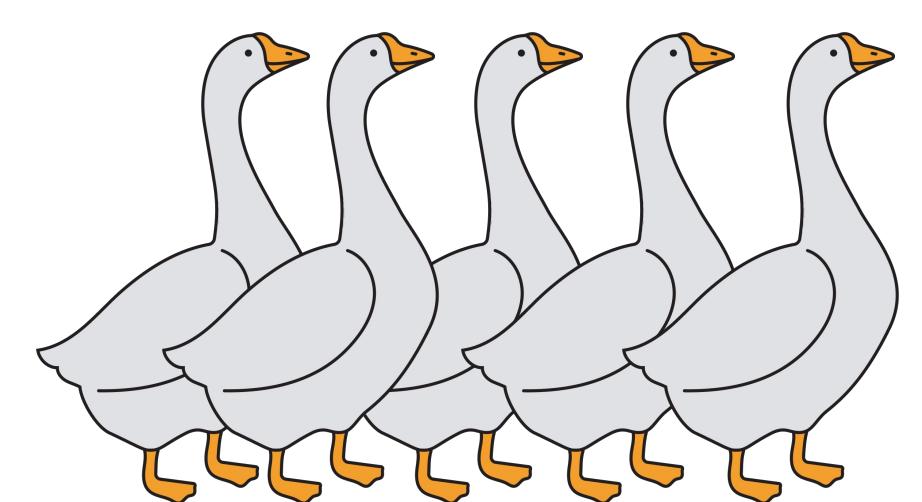
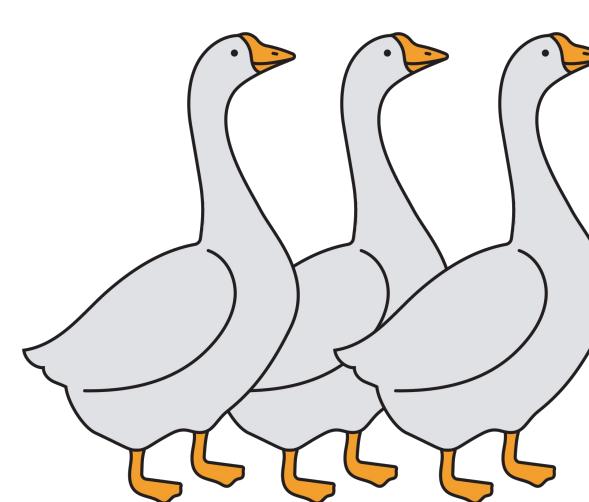
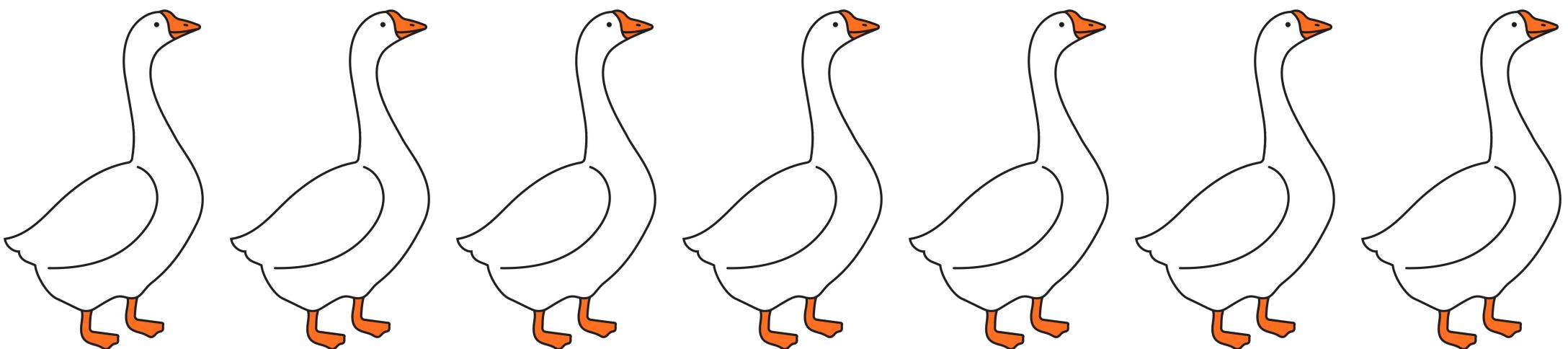
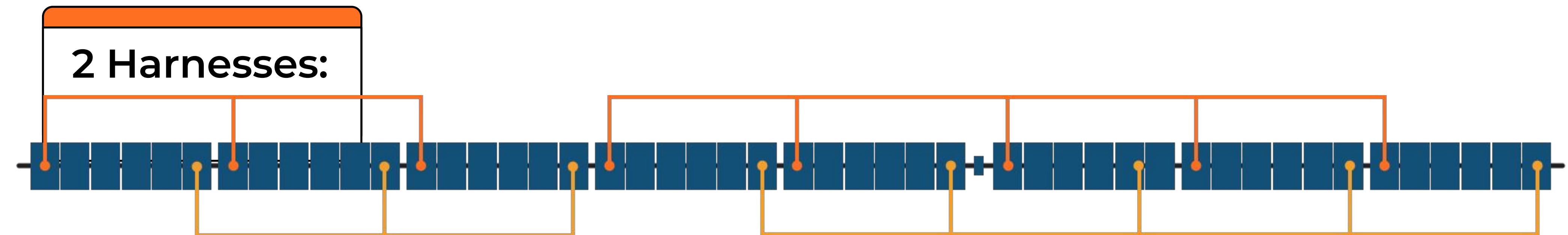
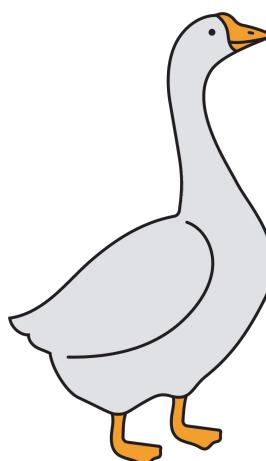
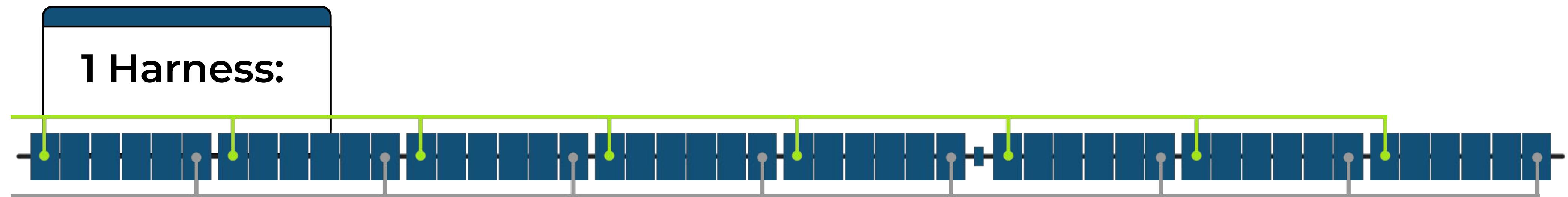
*Determines wiring symmetry  
and layout predictability  
across the field*



VS



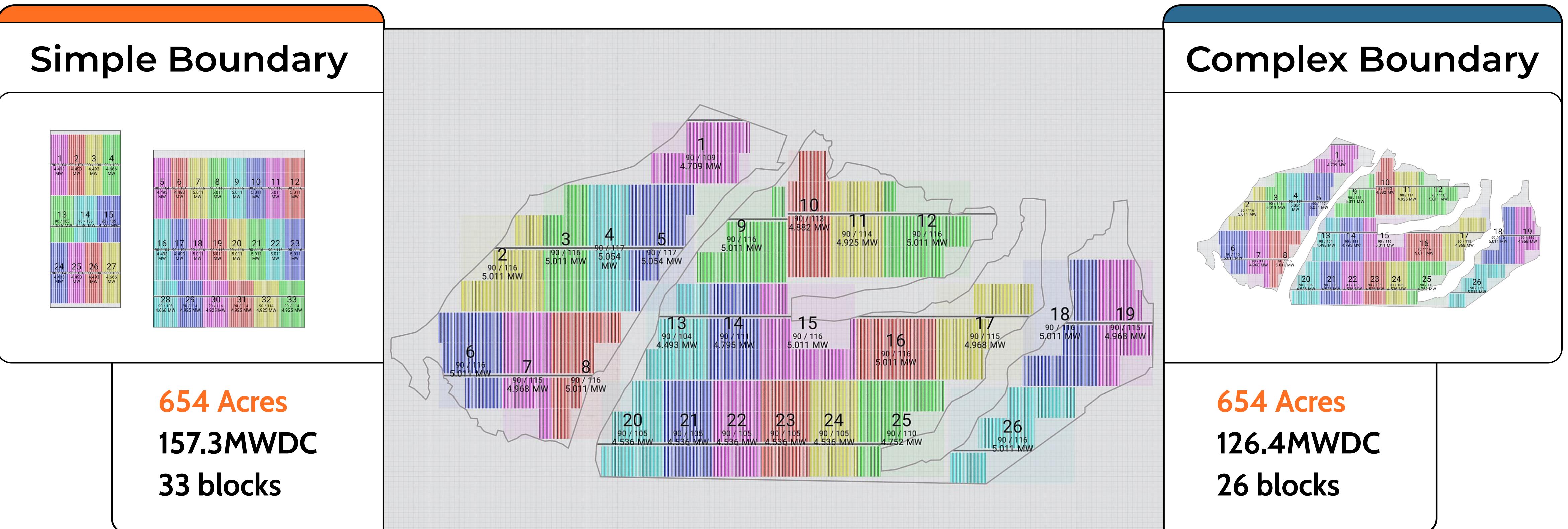
# 8 Harness Configuration



9

# Boundaries Complexity

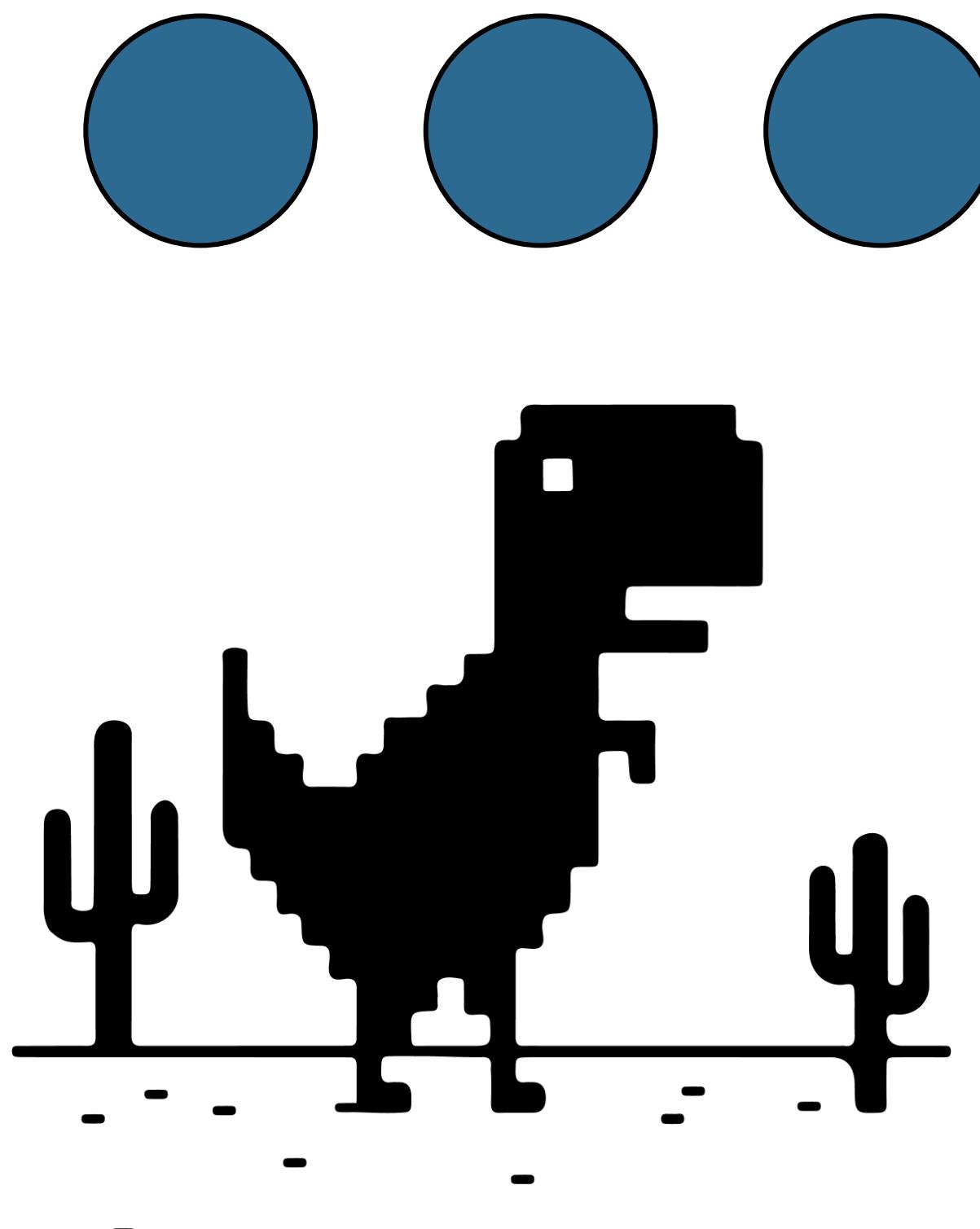
*Complex boundaries reduce block uniformity*



# 10 Wire Gauges Unification

*Sometimes, simplicity is the most valuable optimization*

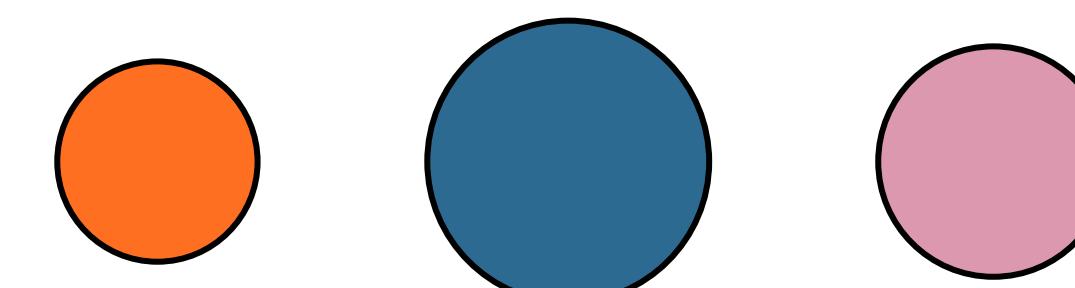
Optimized for construction



VS



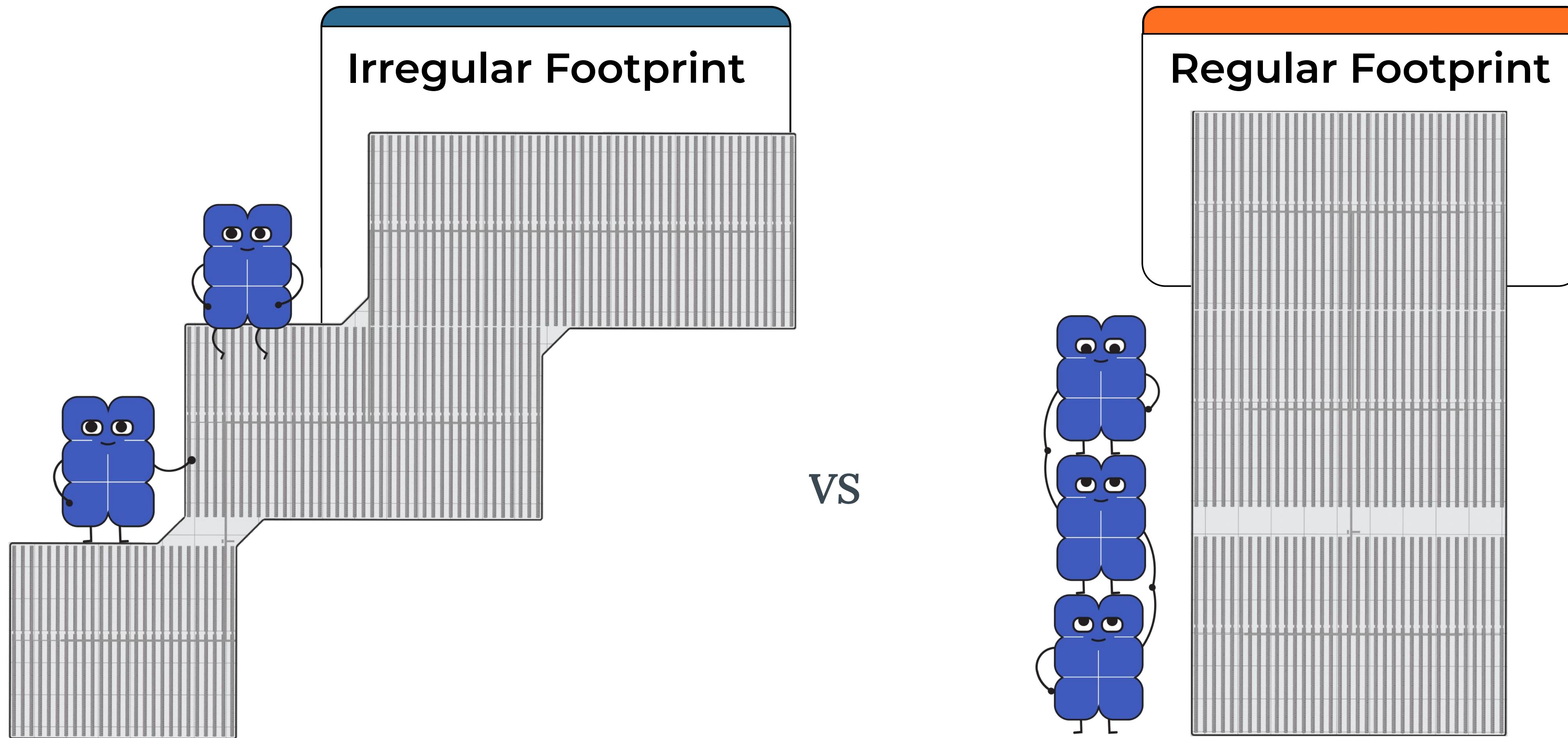
Optimized for materials



11

# Block Footprint

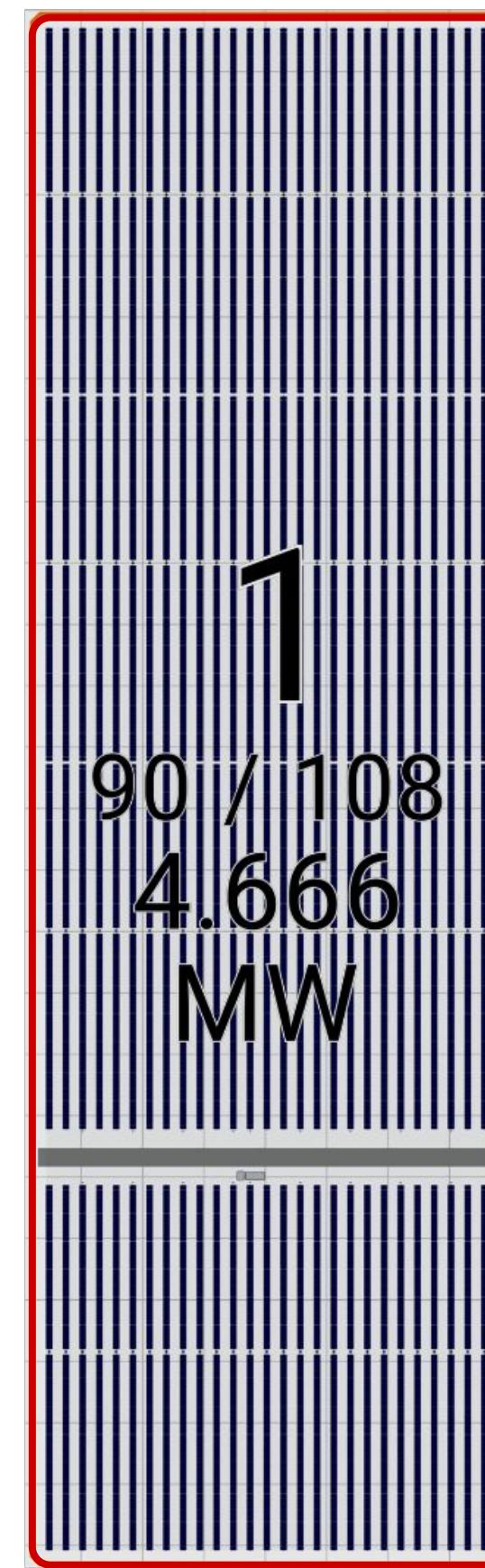
*The fancier the block footprint, the more variation in wire lengths, gauges, and routing*



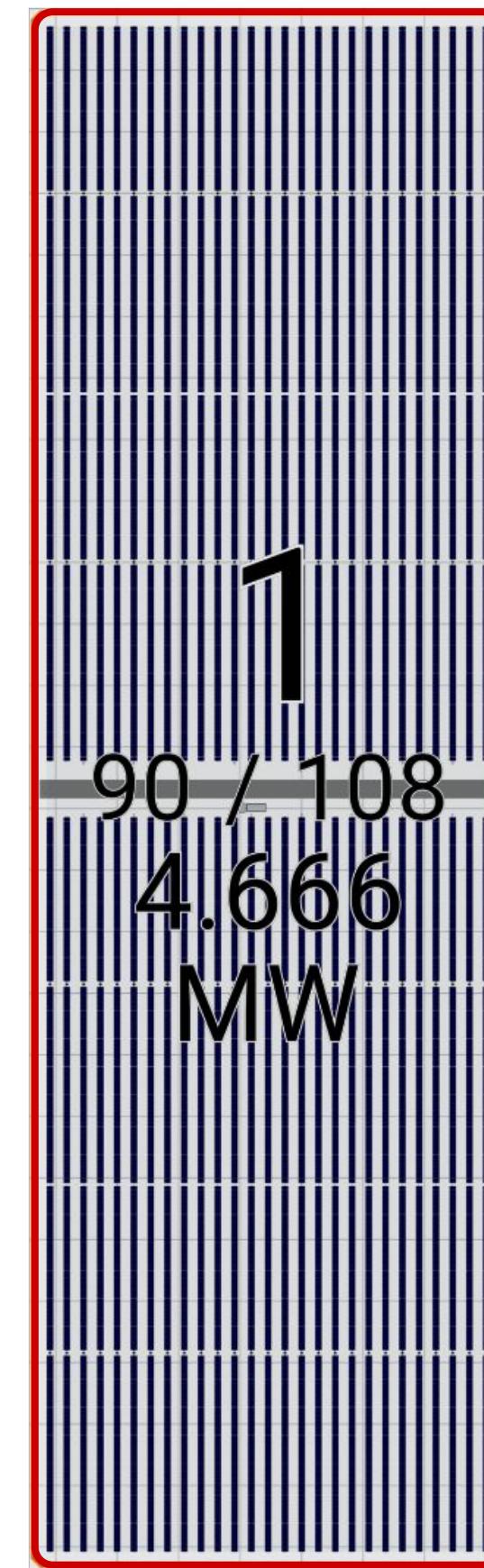
# 12 Row Height

*As a proxy for low voltage drop*

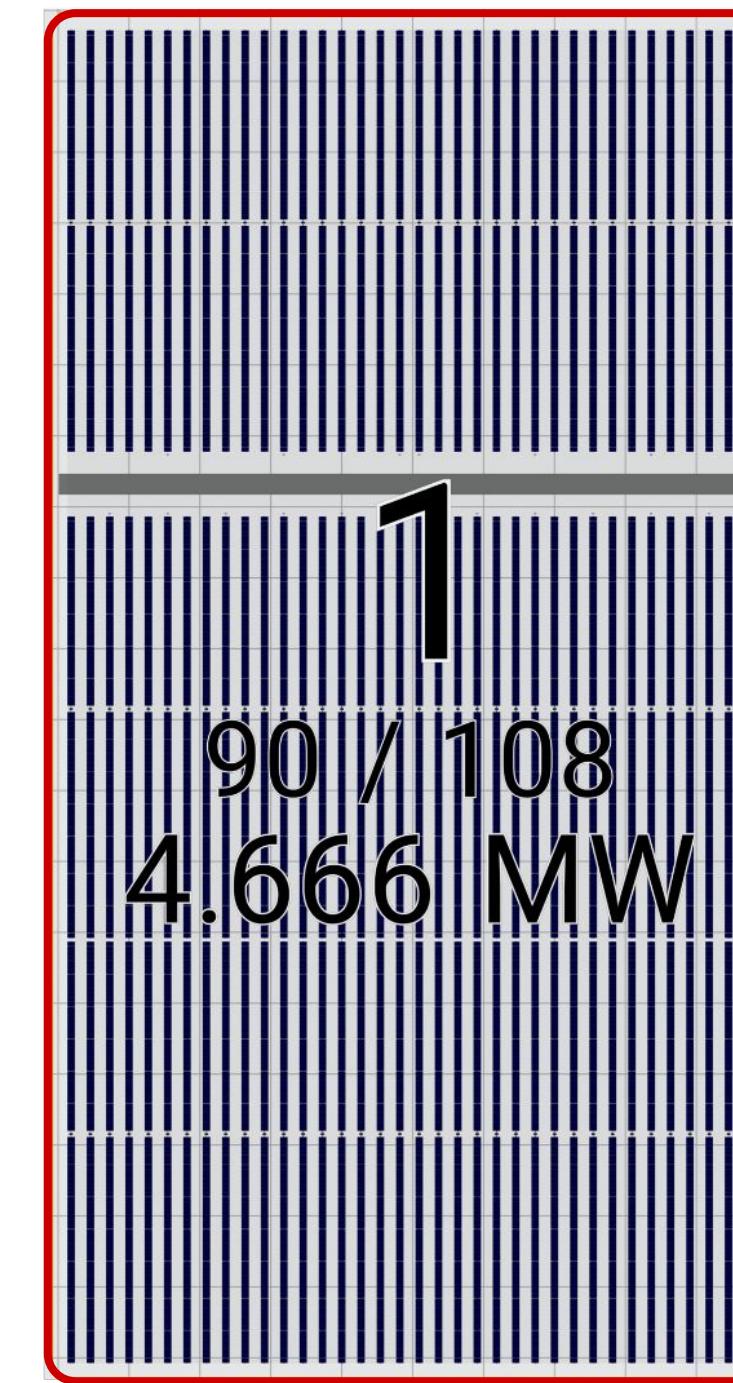
Max RH = 3



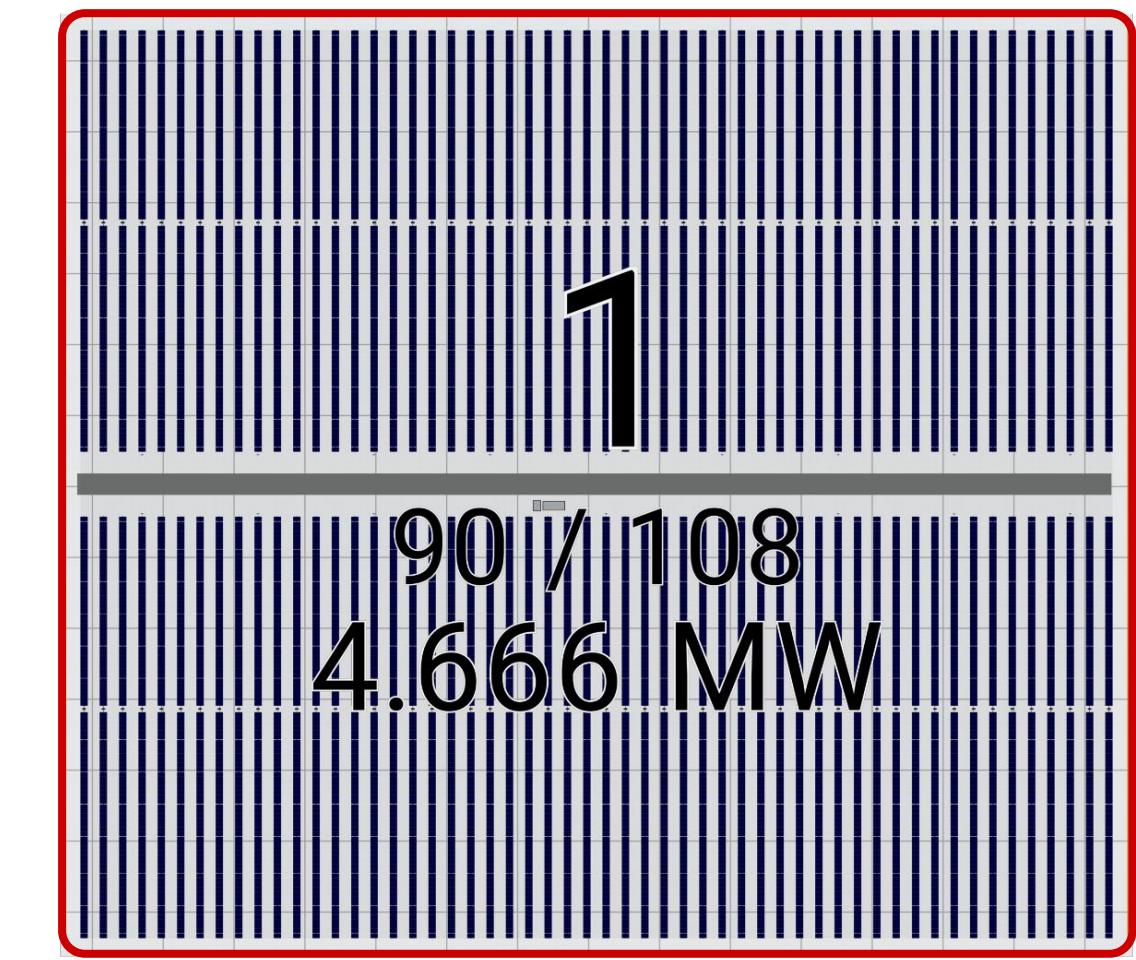
Max RH = 2



Max RH = 2

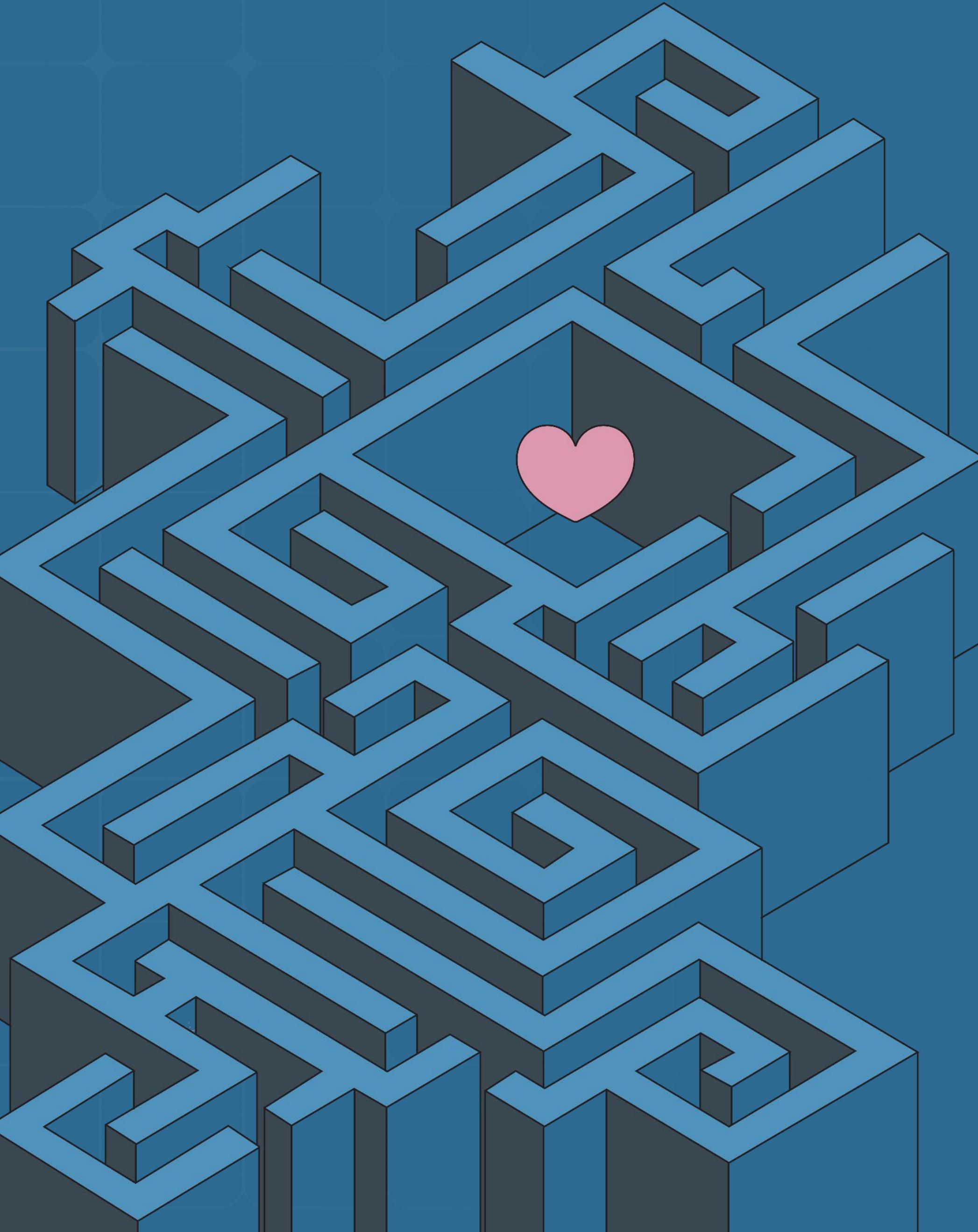


Max RH = 1



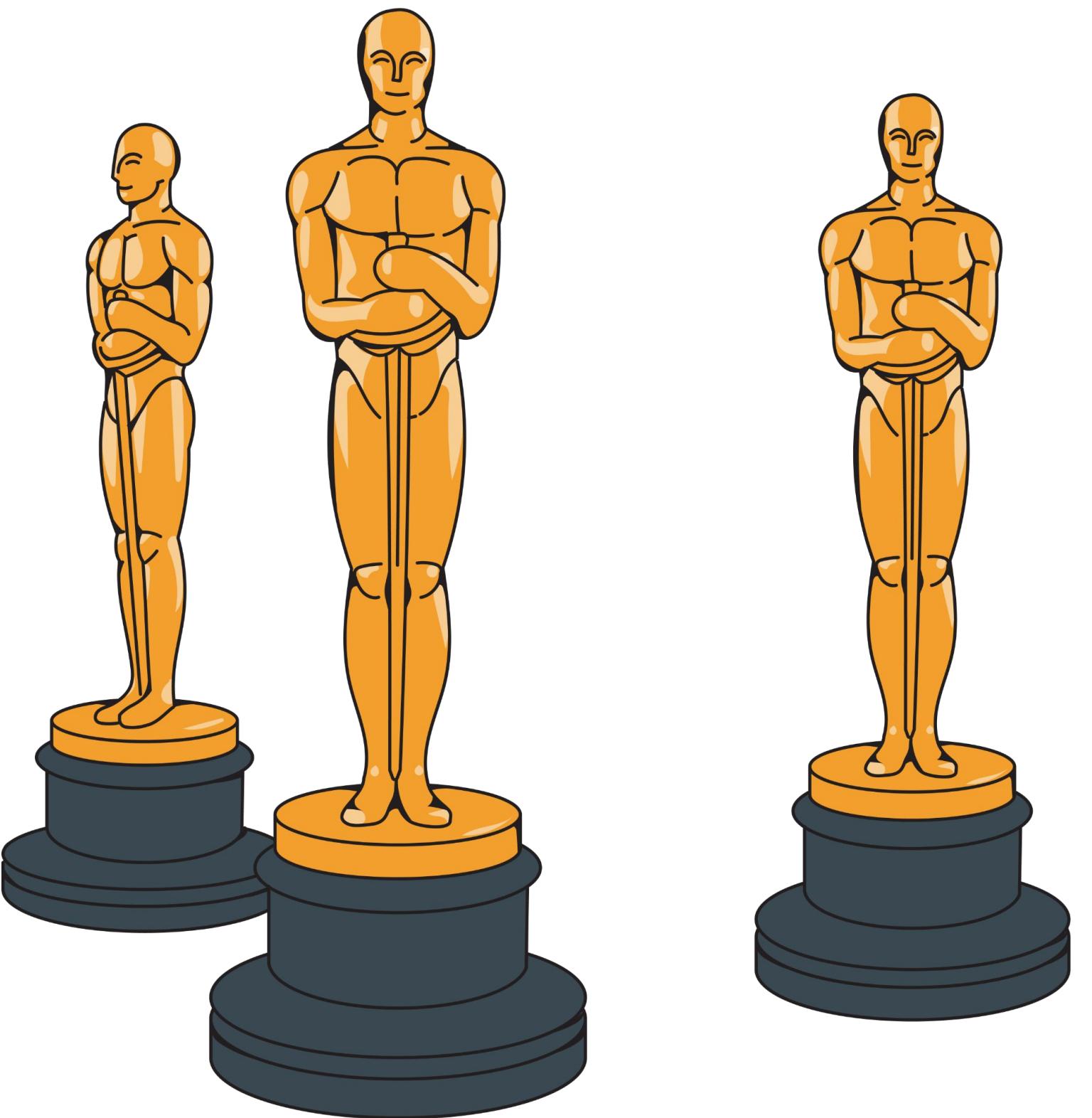
# Prioritising Parameters

*The essence of strategy is  
choosing what NOT TO DO  
at the early design phase*



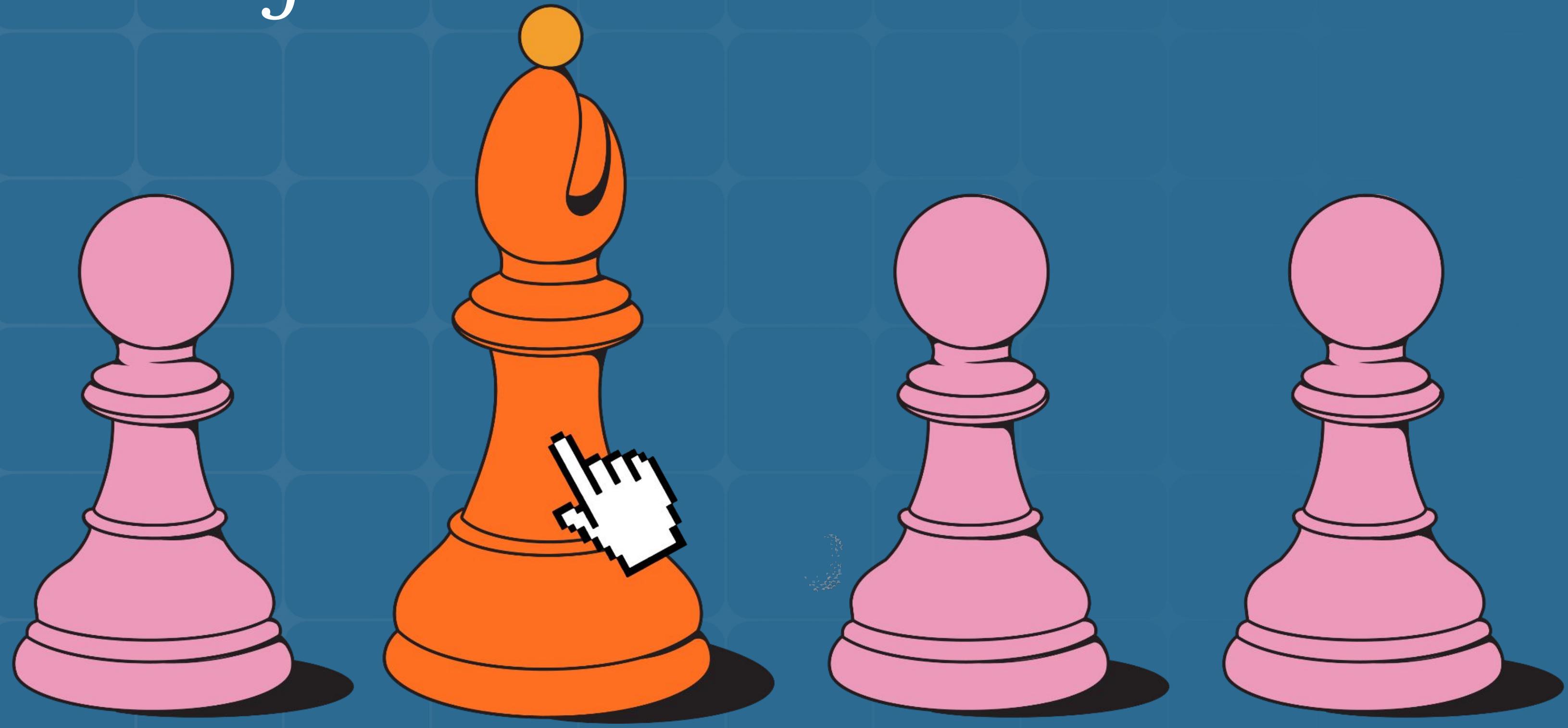
# Grand Prix Evaluation Rules

<b><i>Dimension</i></b>	<b><i>Definition</i></b>
<b>Cost Impact</b>	How much this parameter influences total electrical system cost
<b>Design Lock-In Risk</b>	How hard it is to change this decision later in the design/construction
<b>System Interaction</b>	How many other design elements this parameter affects or constrains



# Defining Cost Impact

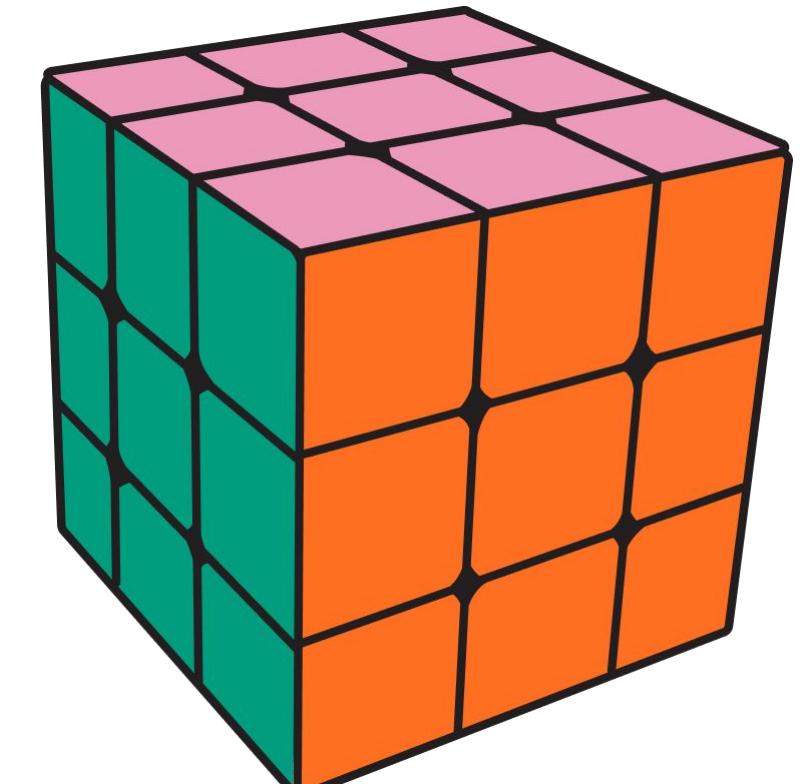
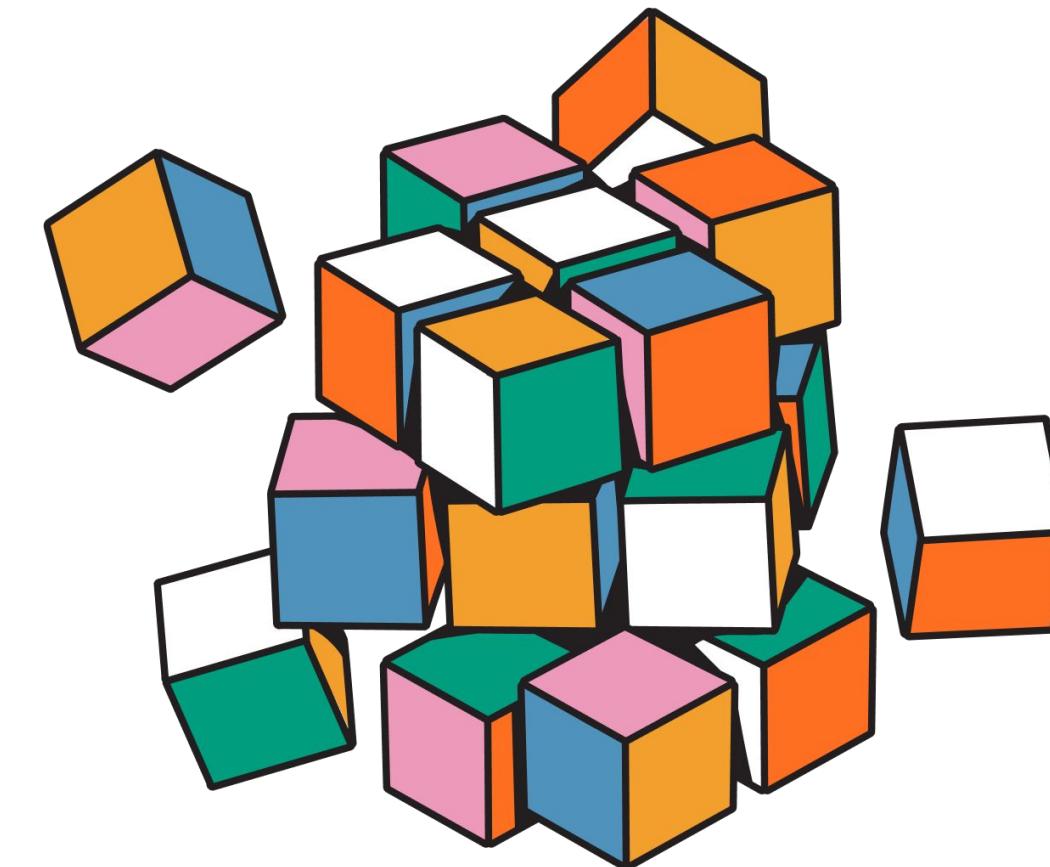
*Two tests walk into a lab...  
only the variable changed!*



# Controlled Comparison Method

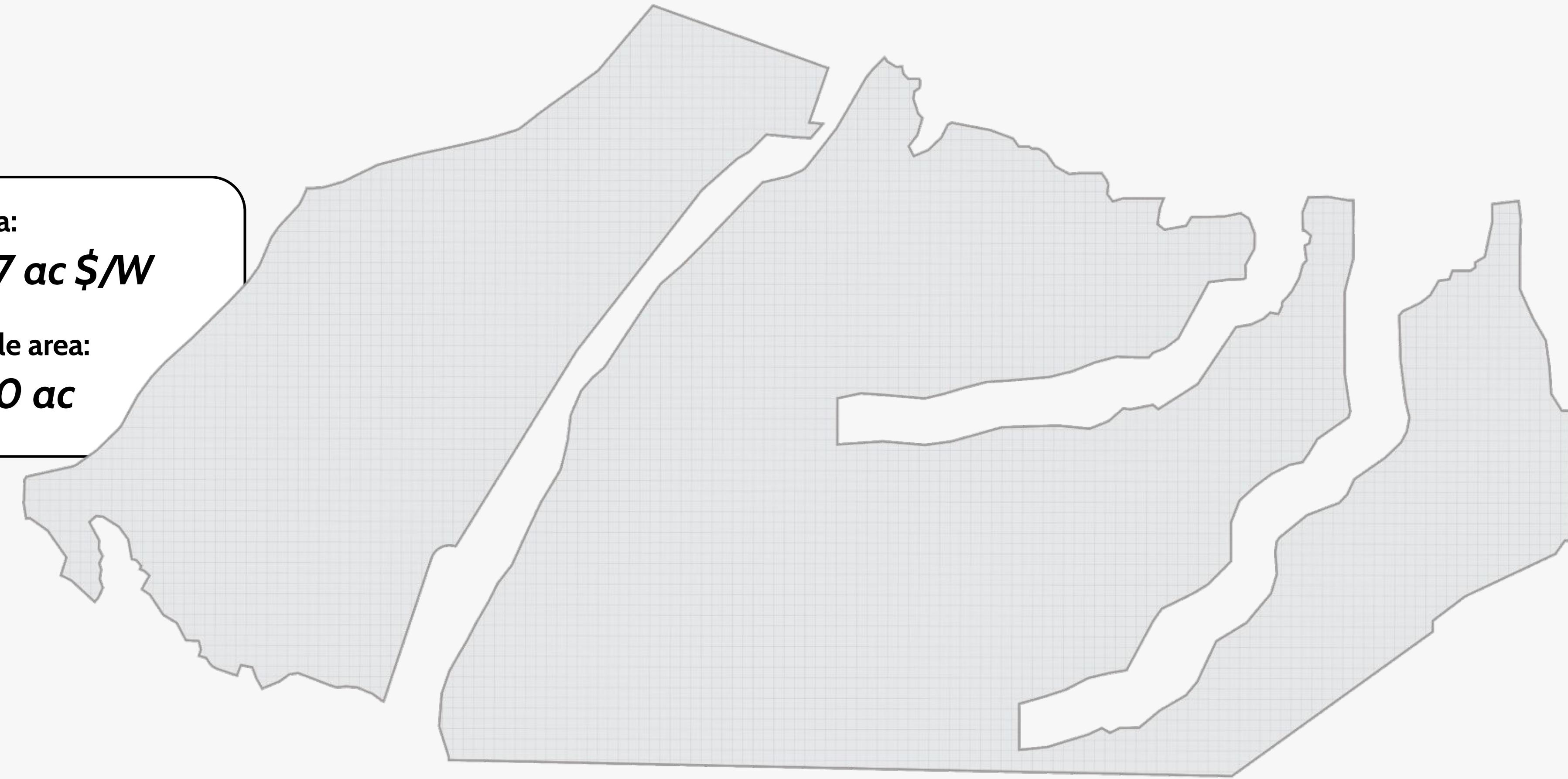
*Strengthens decision-making by avoiding misleading correlations*

1. Lock all other variables and run best vs worst  
(or A vs B) scenarios for that parameter
2. Normalize the result to \$/W or \$ impact per MW
3. Measure impact



Site area:  
**657.57 ac \$/W**

Buildable area:  
**653.50 ac**



**Tracker 84 mods**  
\_\_\_\_\_  
**PV Module 540W**



**CBX 300A**  
\_\_\_\_\_  
**CBX 600A**

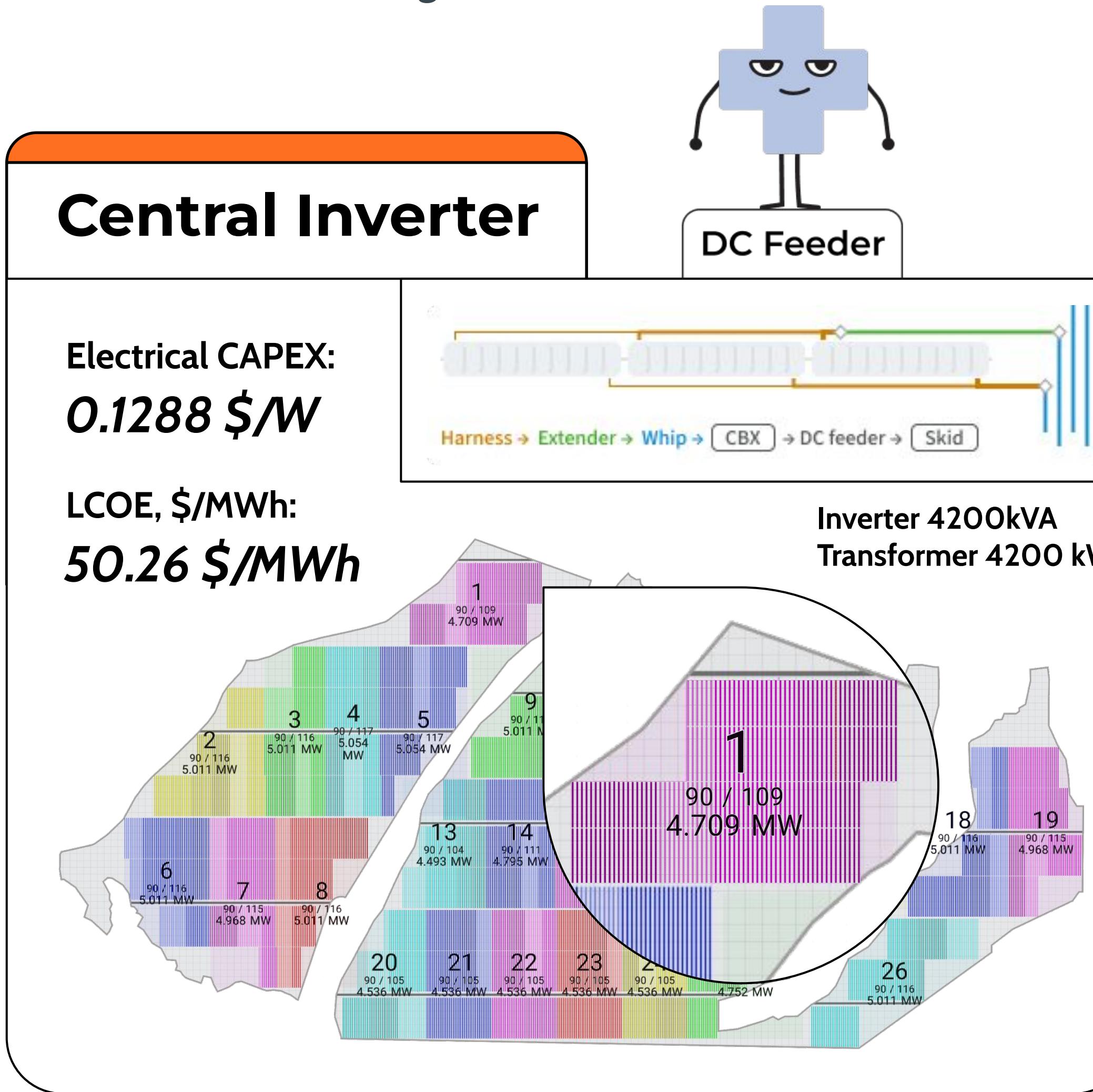


**Inverter**  
**840; 4200**

# Inverter Type

## Central vs String

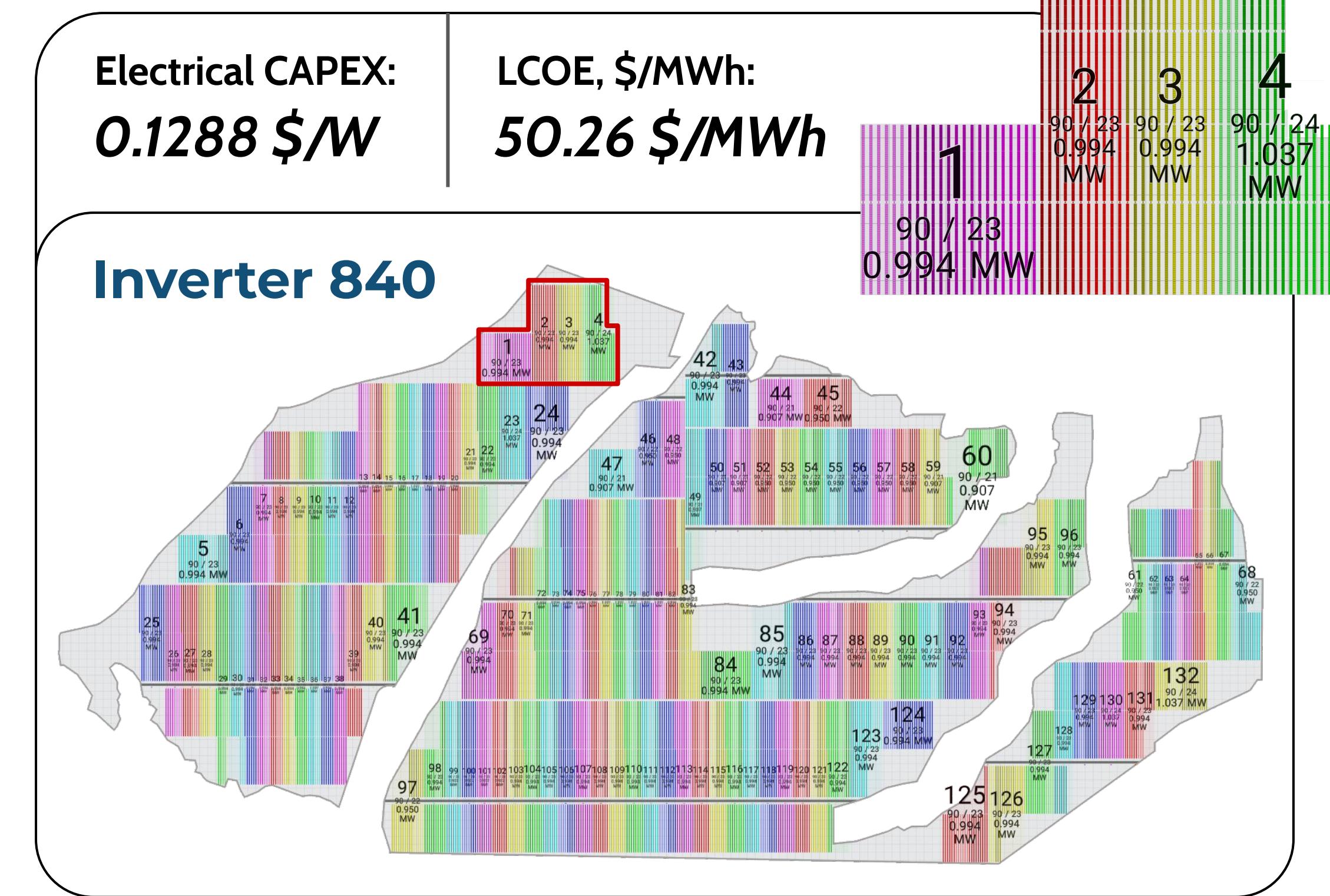
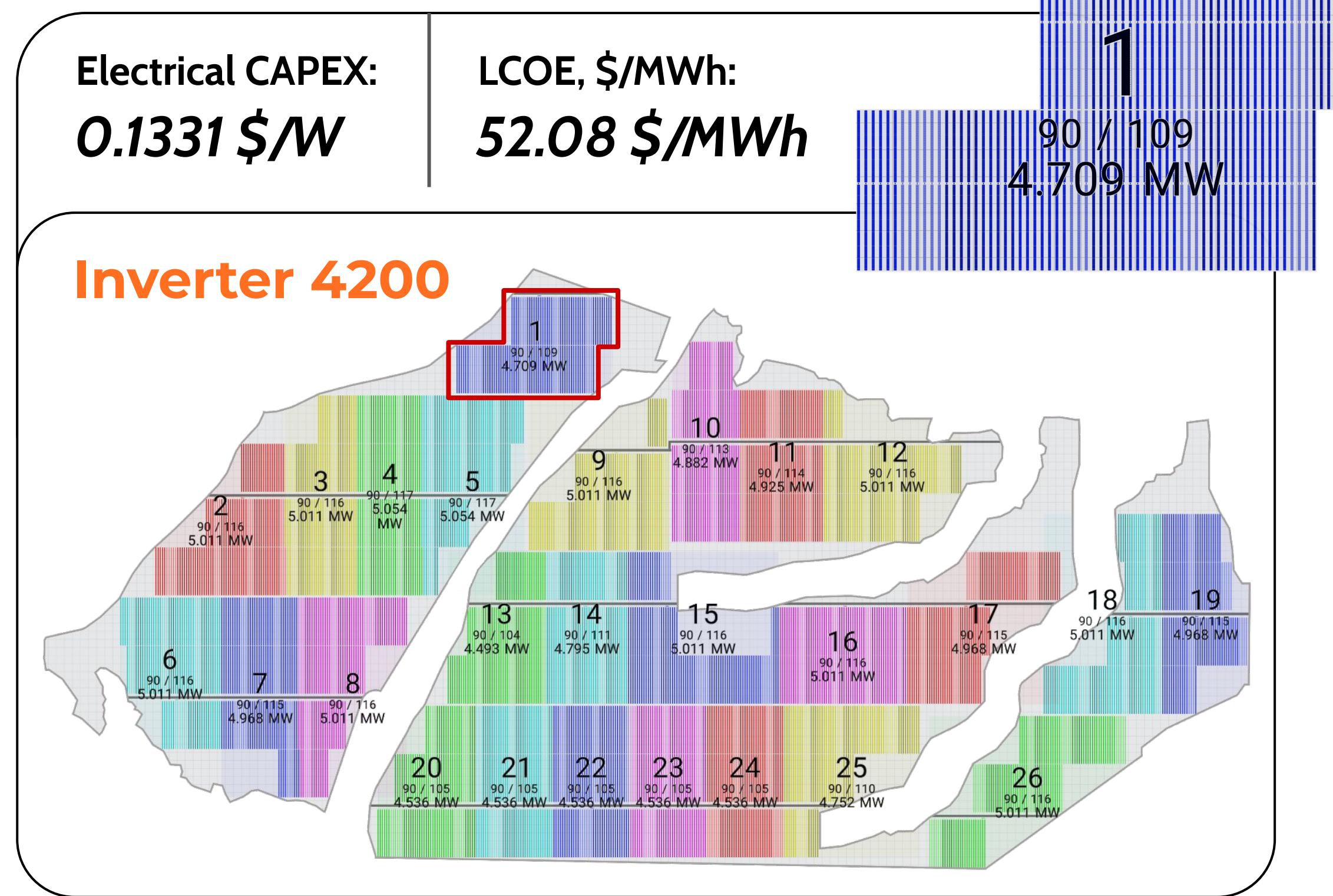
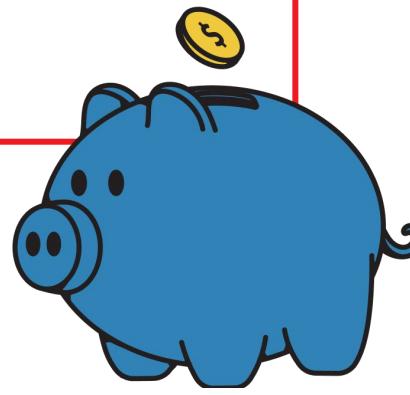
Using String Inverter lowers EBOS by **1.25%**  
**\$0.2M saved**



# Inverter Power

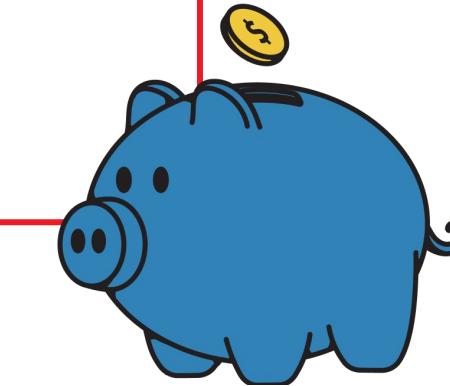
With a lower inverter power rating, electrical costs are reduced because the total length of DC feeders is shorter. In large blocks, some combiner boxes are located far from the central inverter, which requires long DC feeder runs. At the same time, the lengths of the Harness, Extender, and Whip do not depend on the inverter capacity, since the combiner group size is the same in both cases.

Using Inverter 840 lowers EBOS by **3.2%**  
**\$0.54M saved**



# PV Module Type

Using Monocrystalline lowers  
EBOS by **28.4%**  
**\$6.08M** saved



## Monocrystalline

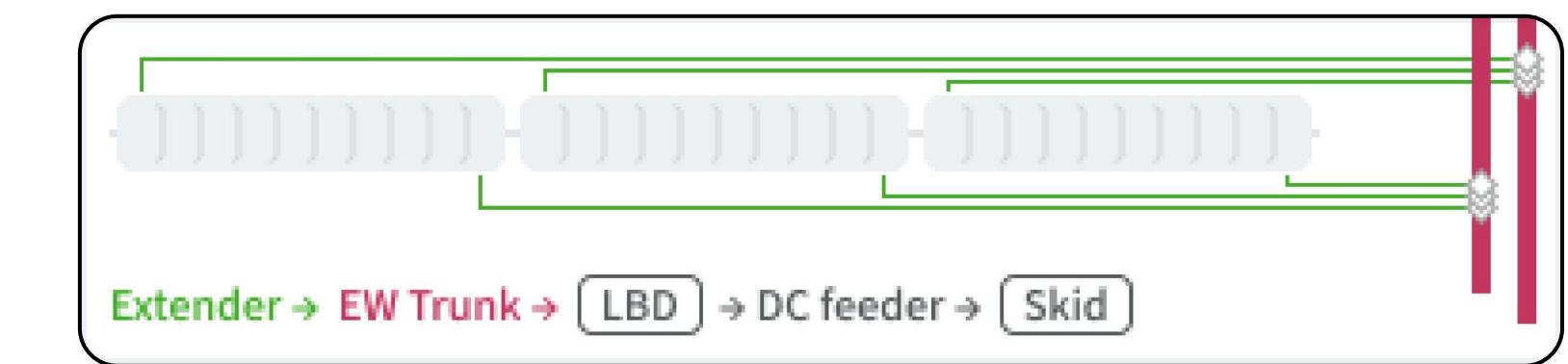
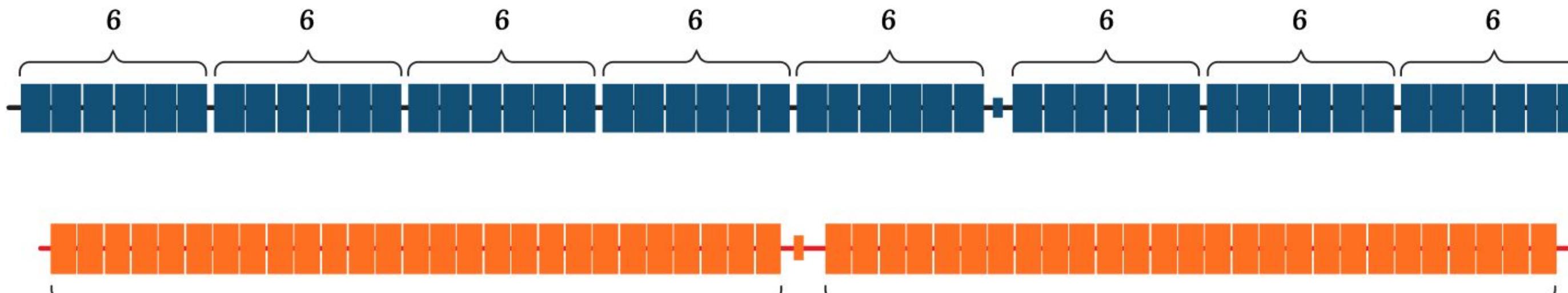
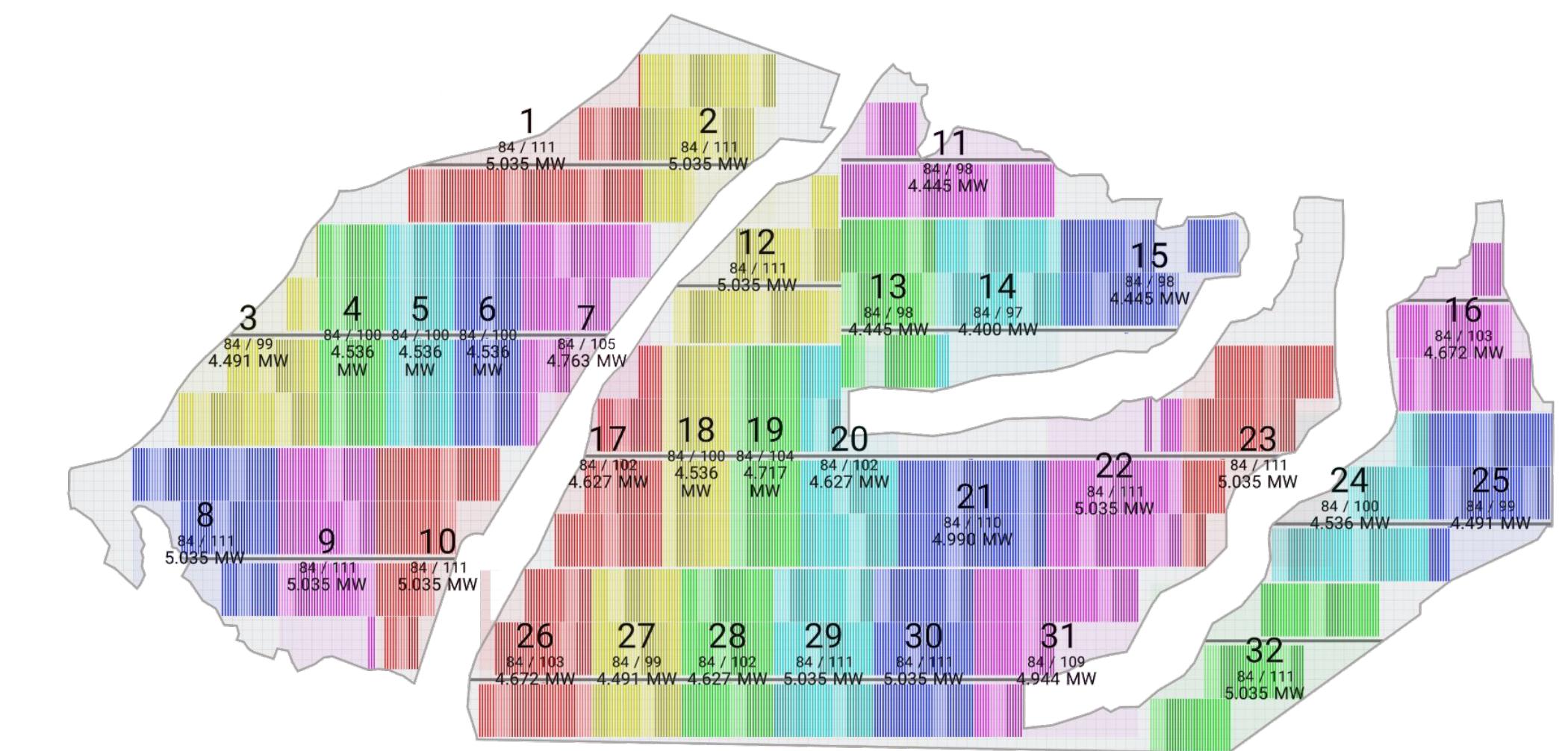
Canadian Solar  
String size: 27 mods  
Total: 54 mods  
21.9kW tracker power

Electrical CAPEX:  
**0.1213 \$/W**  
LCOE, \$/MWh:  
**48.52 \$/MWh**

## Thin-Film

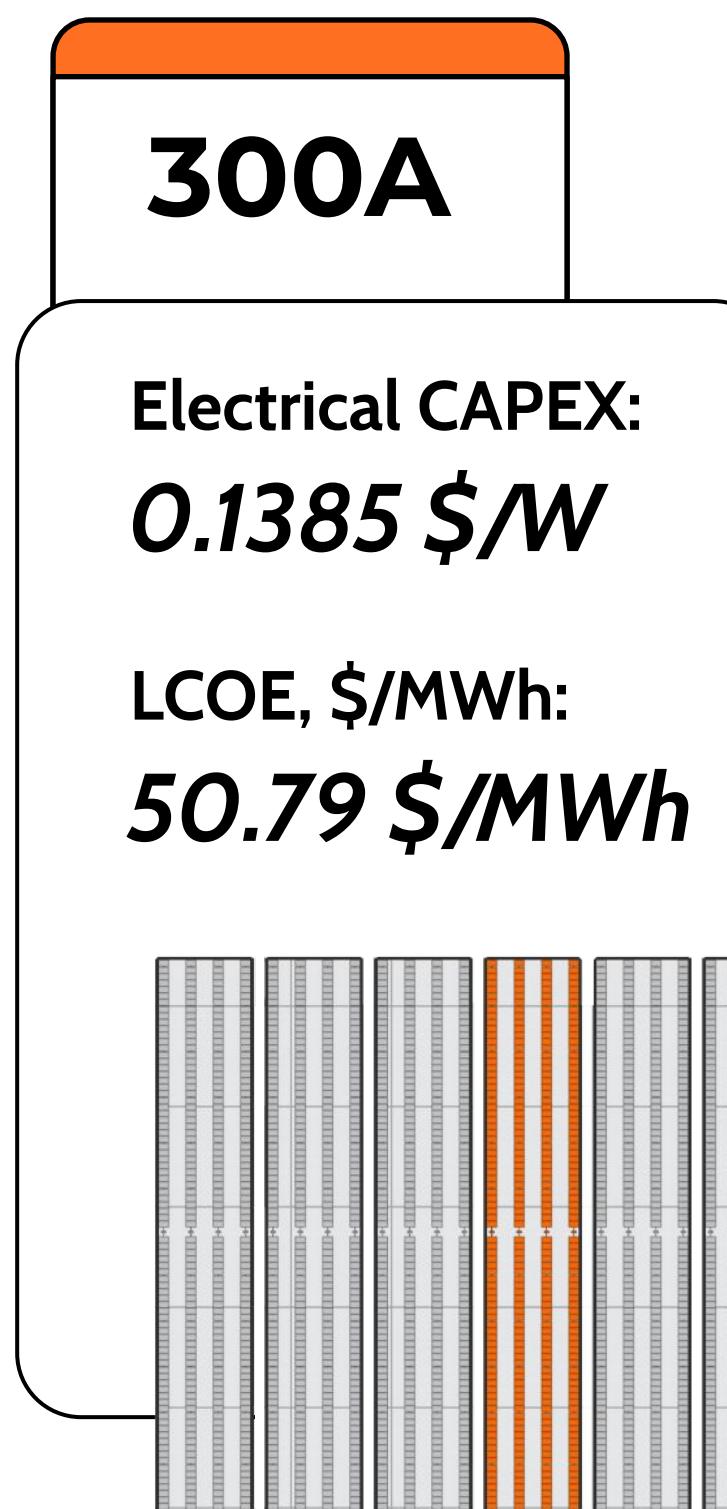
First Solar  
String size: 6 mods  
Total: 54 mods  
23.2kW tracker power

Electrical CAPEX:  
**0.1694 \$/W**  
LCOE, \$/MWh:  
**50.74 \$/MWh**

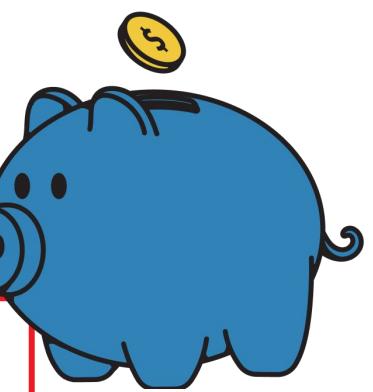


# CBX/LBD Ampacity

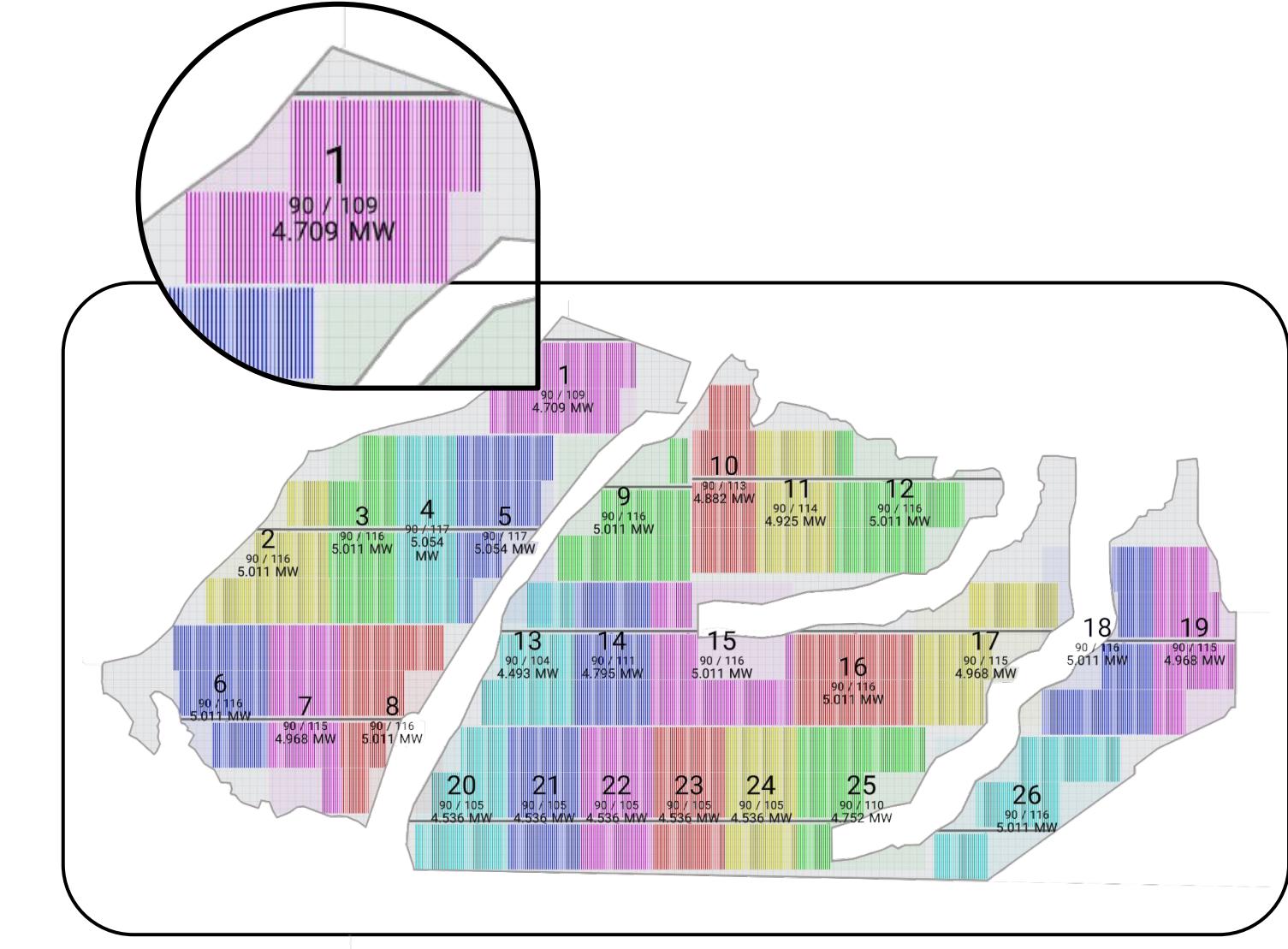
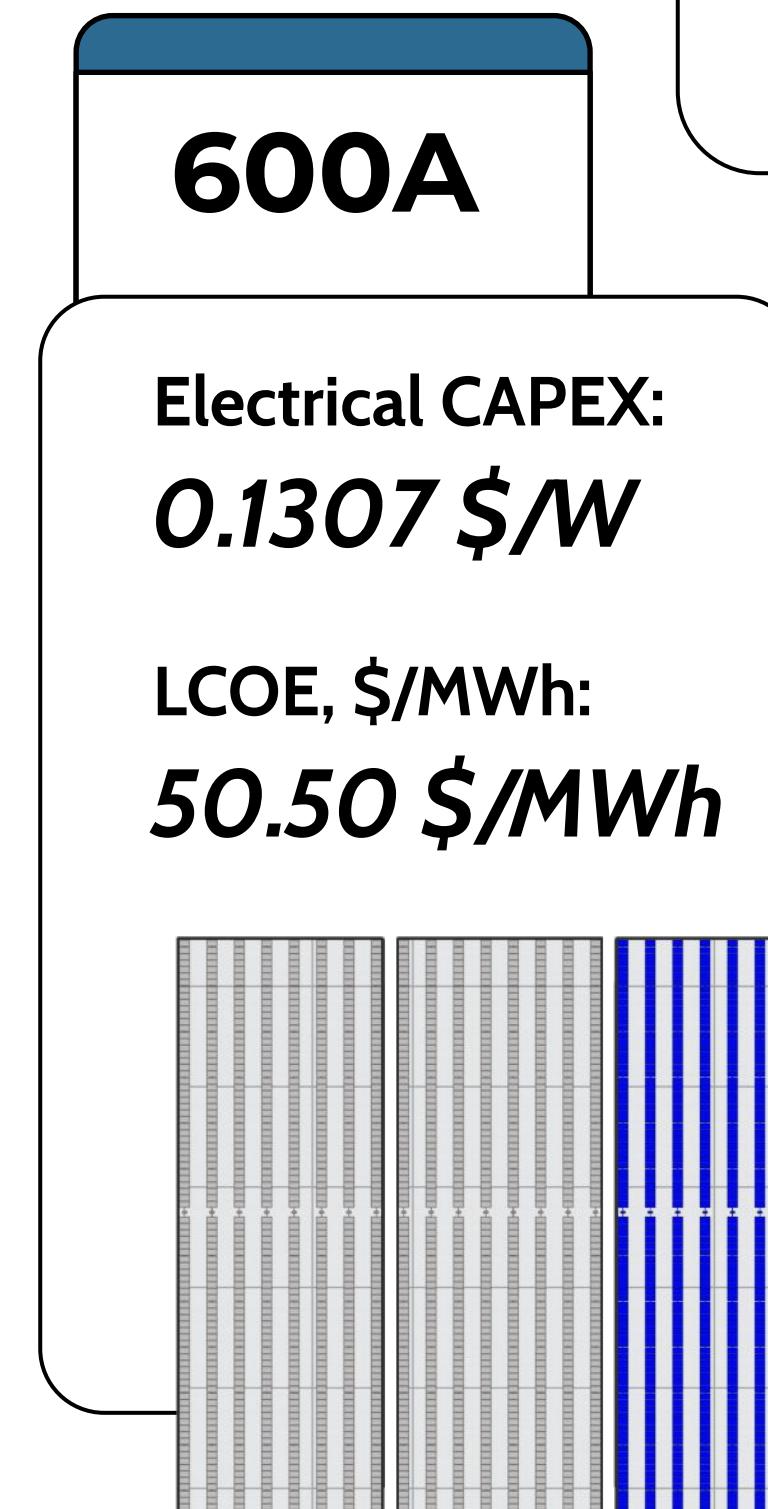
*Affects conductor type/size, termination complexity, and layout adaptability*



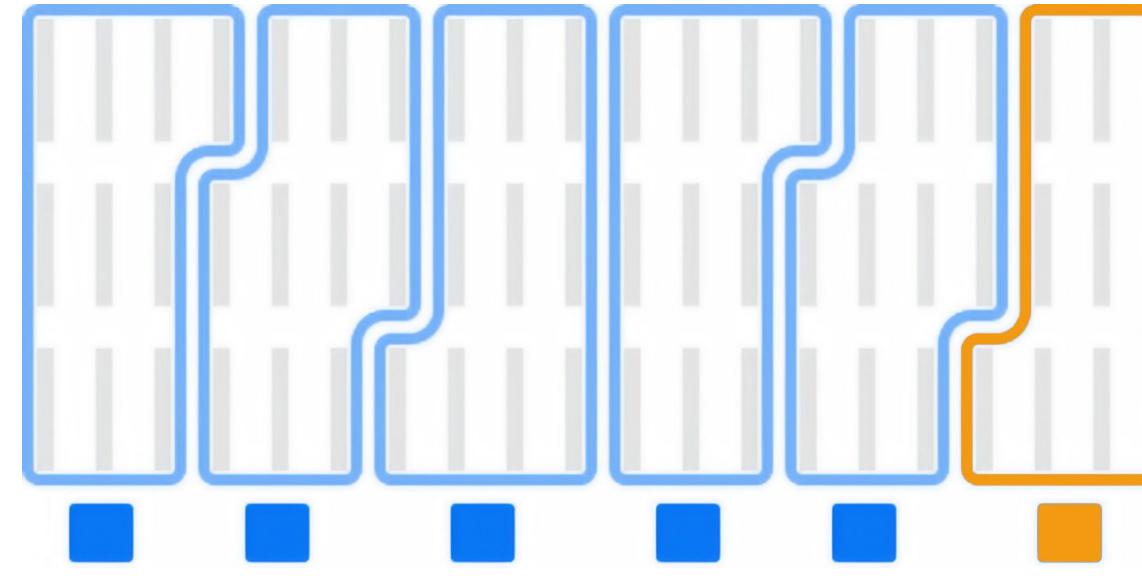
Using 600A lowers EBOS by **5.6%**  
**\$0.98M saved**



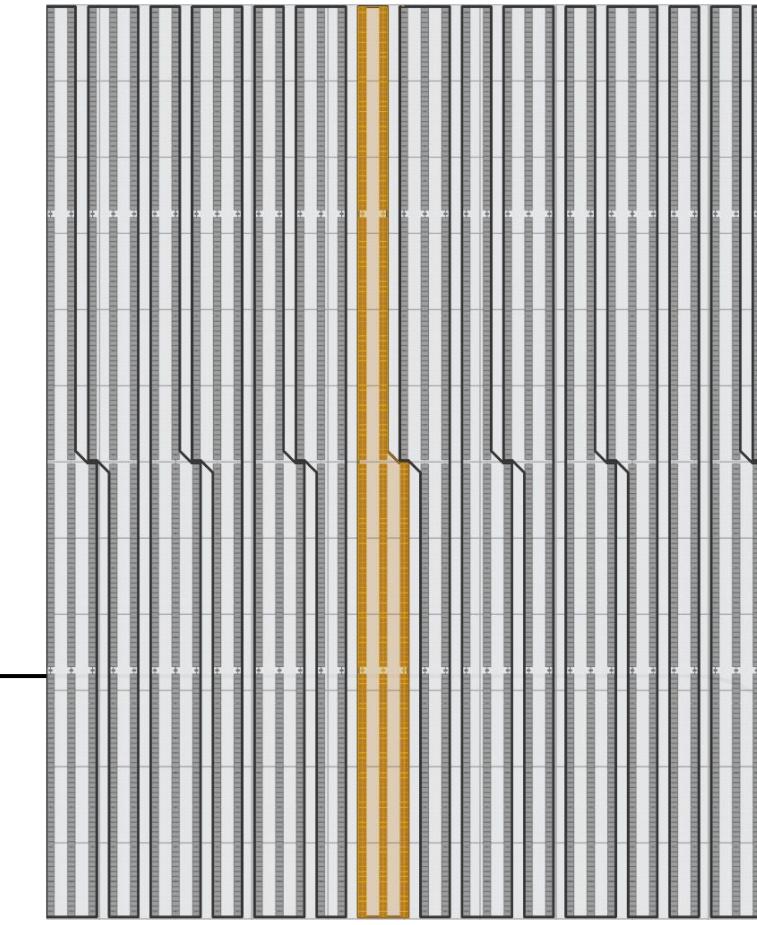
VS



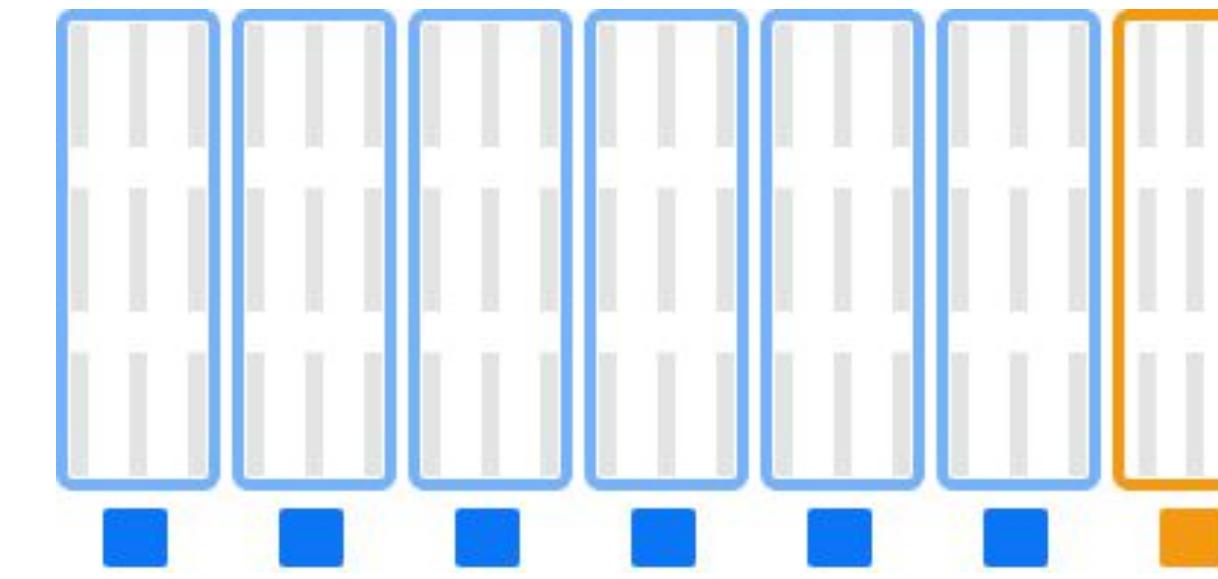
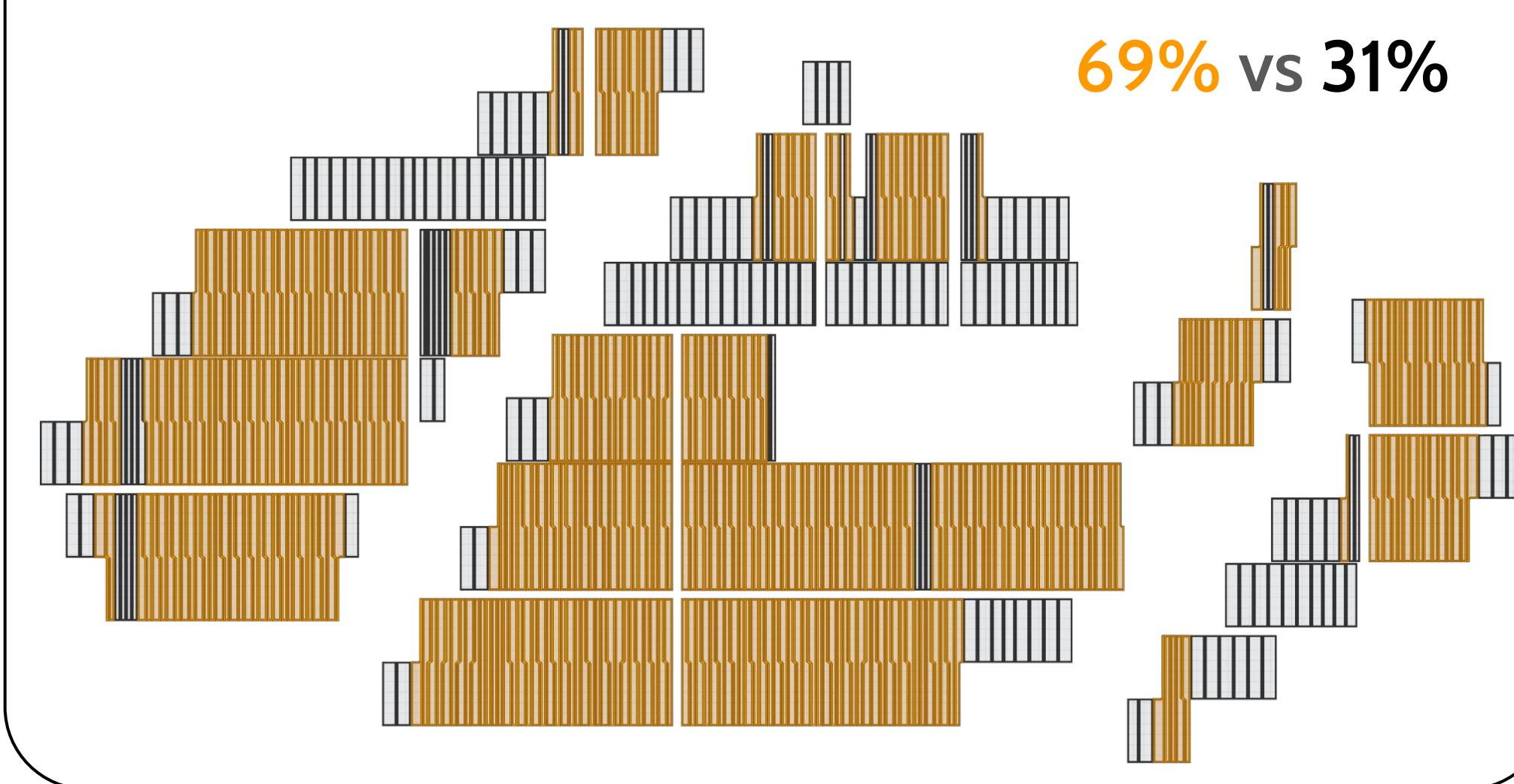
# Optimised for Load vs Buildability



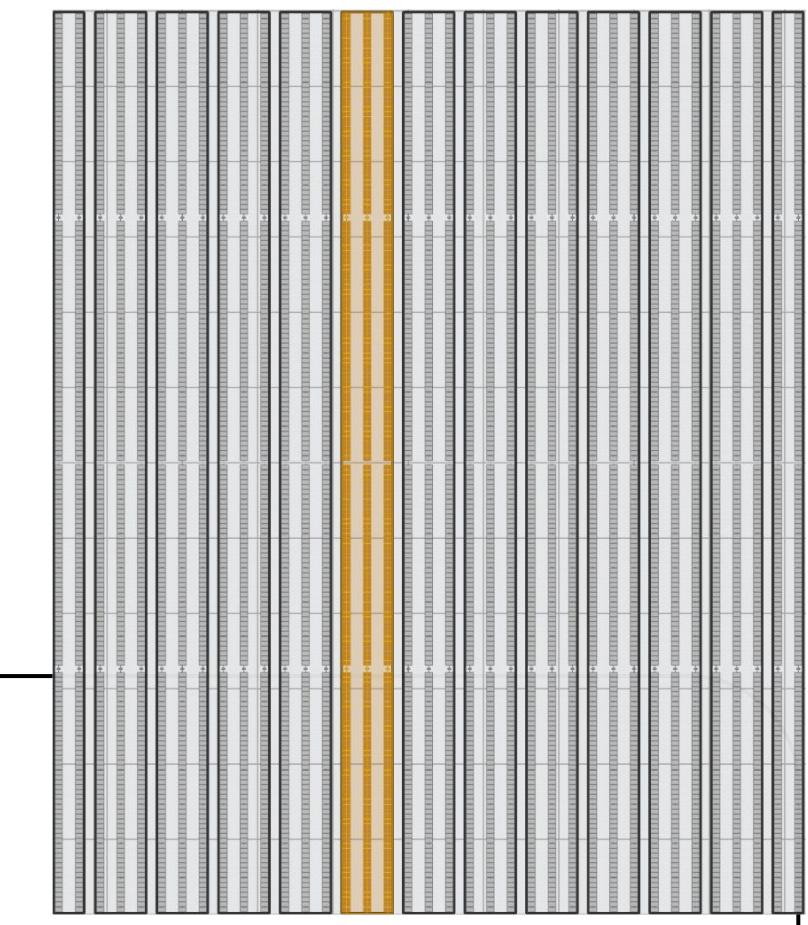
**CBX 320A - 5 SAT**



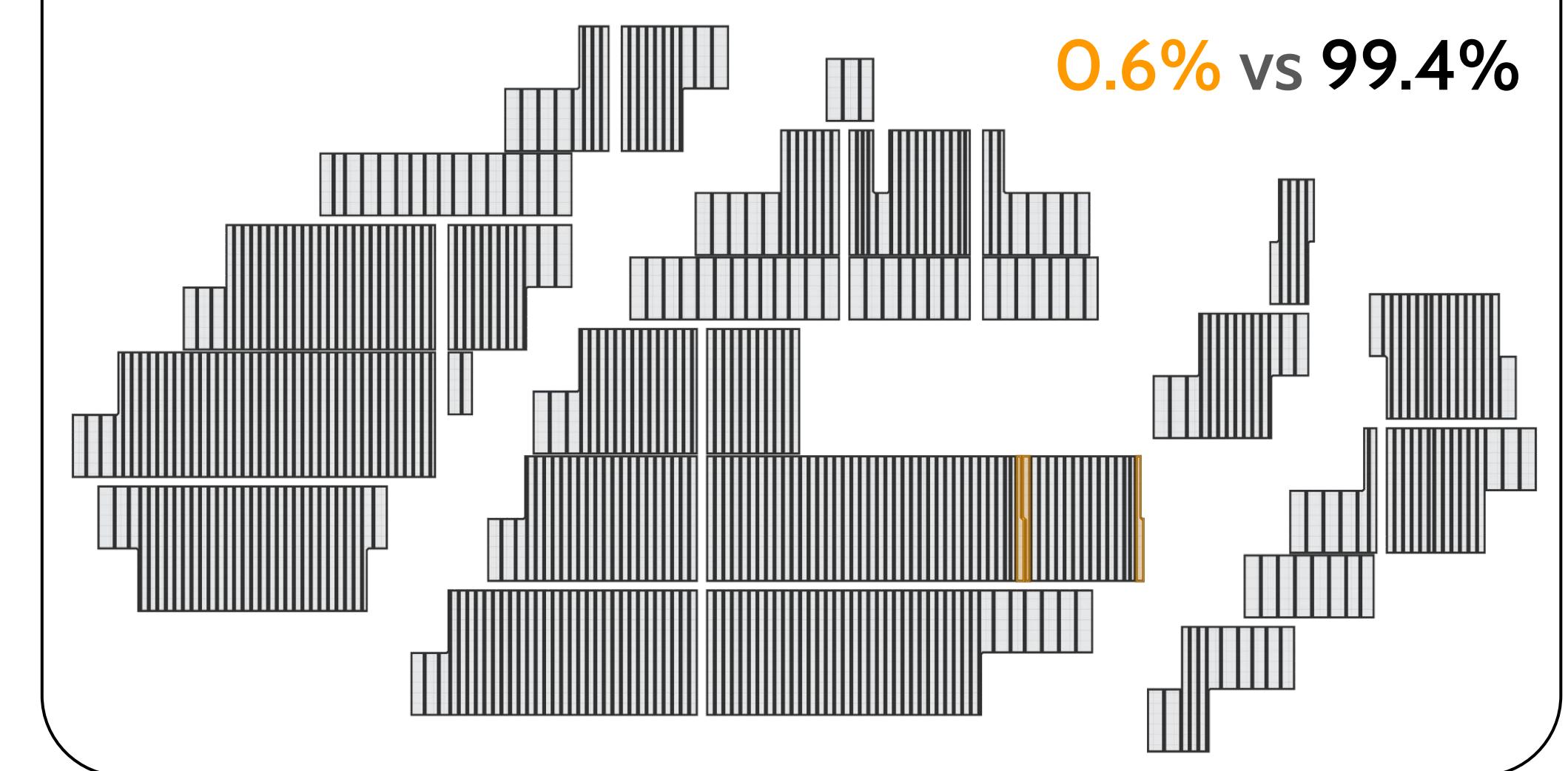
69% vs 31%



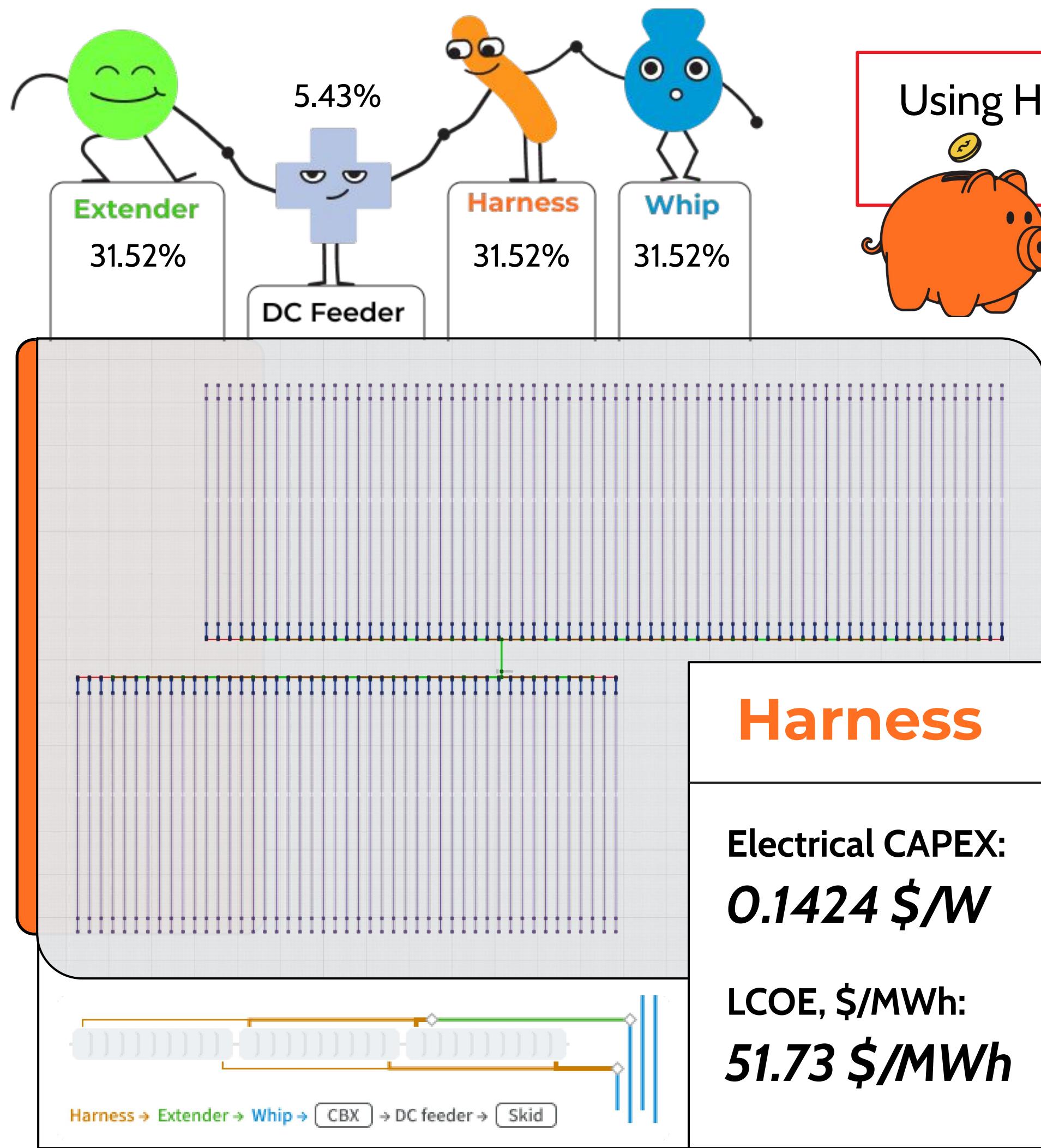
**CBX 400A - 6 SAT**



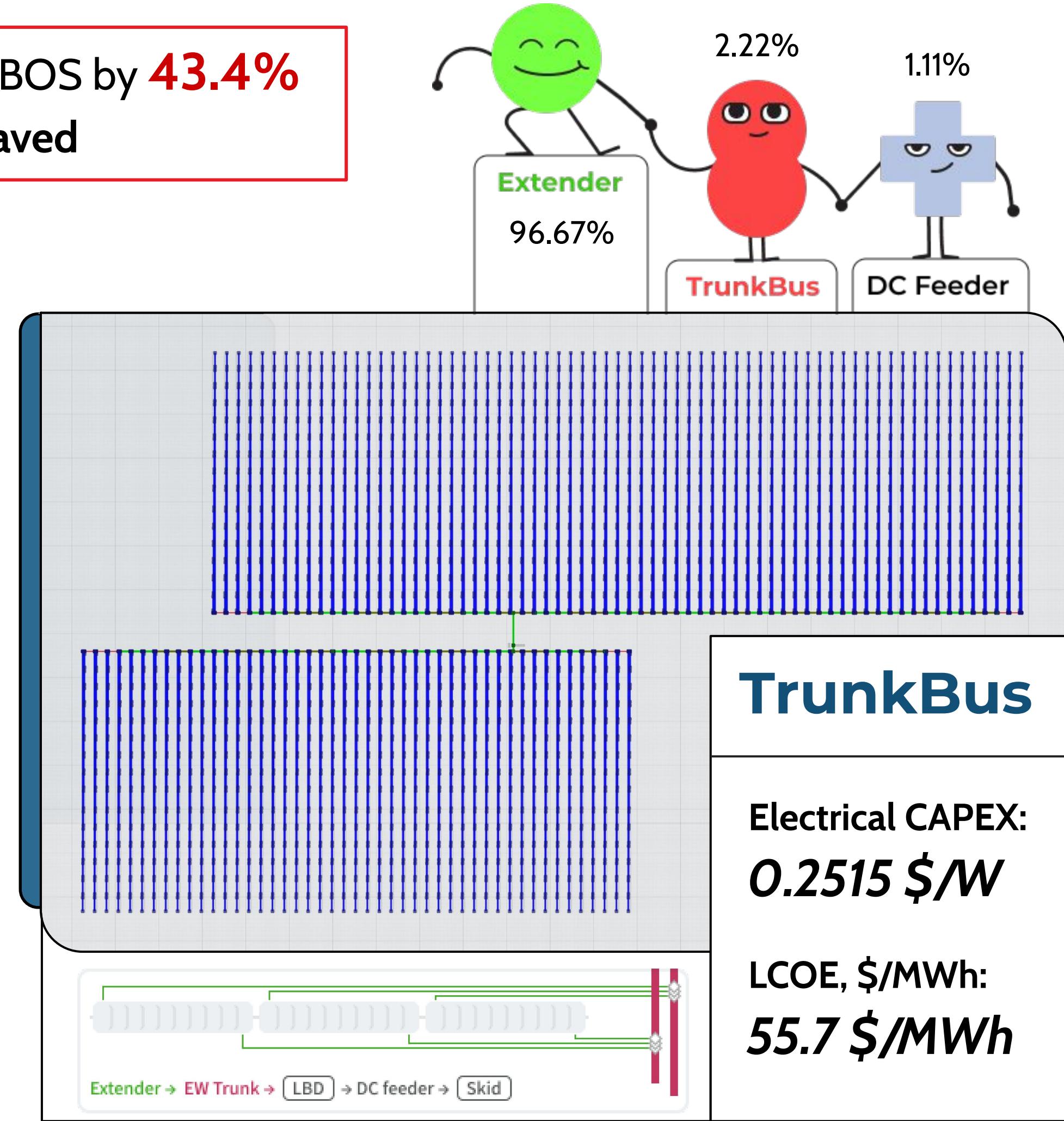
0.6% vs 99.4%



# Wire Sequence

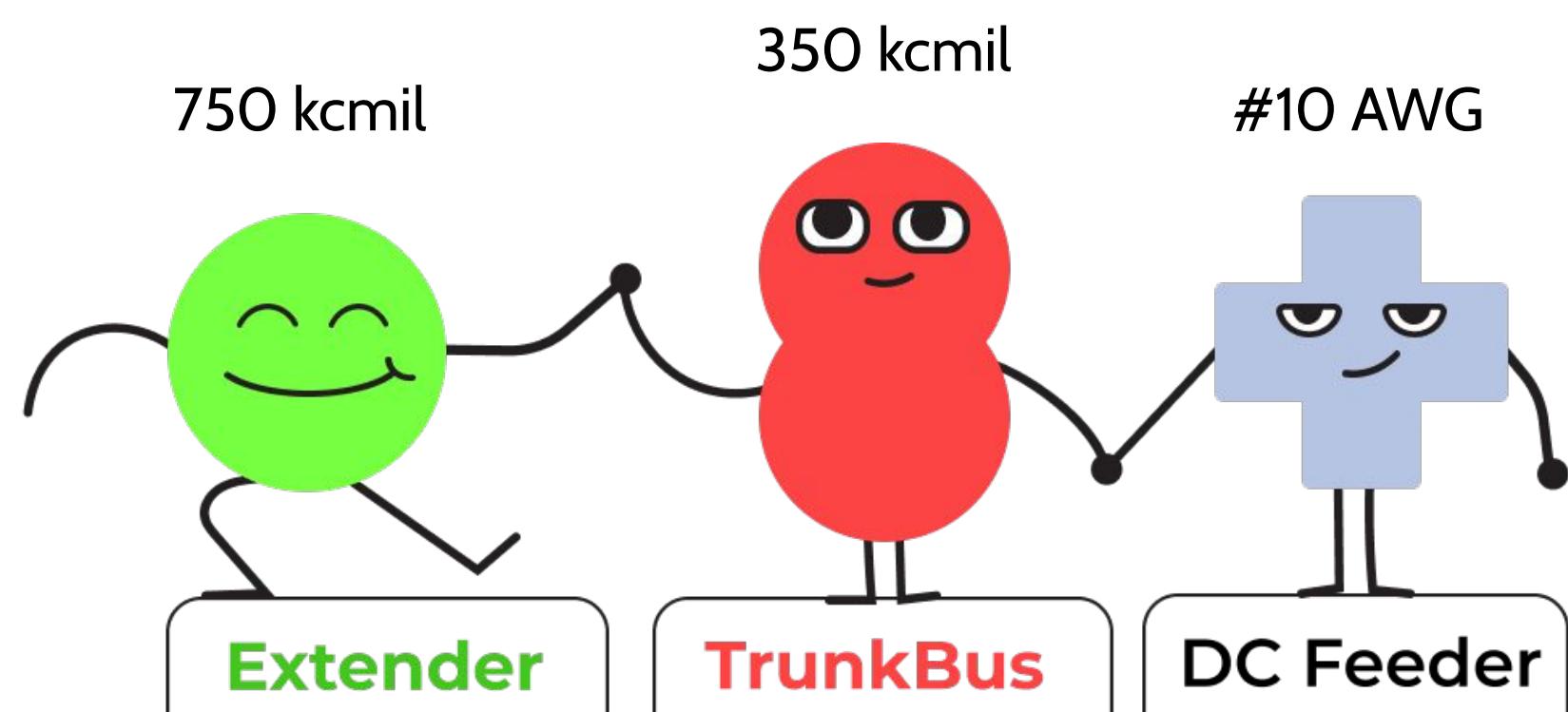
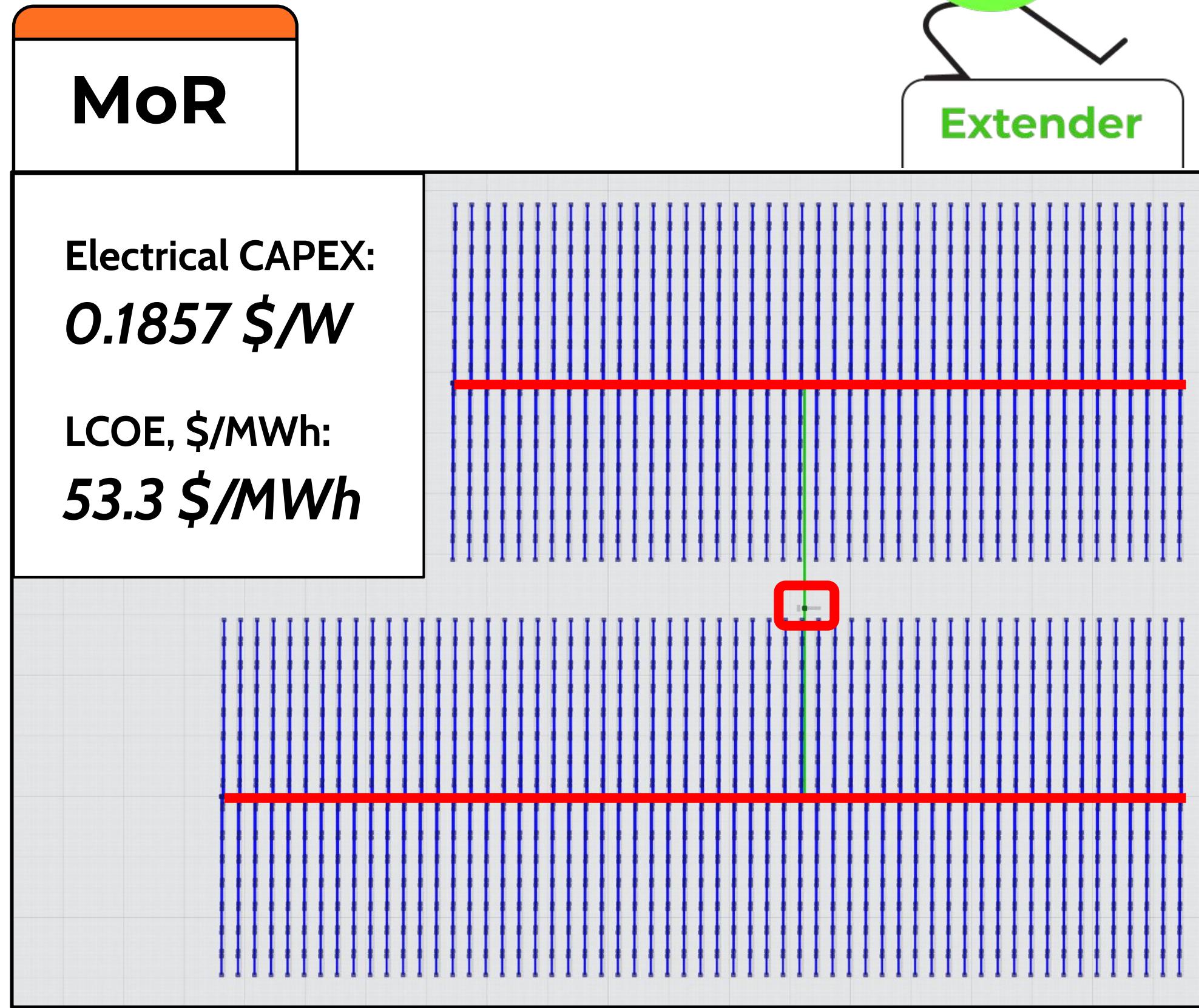


VS

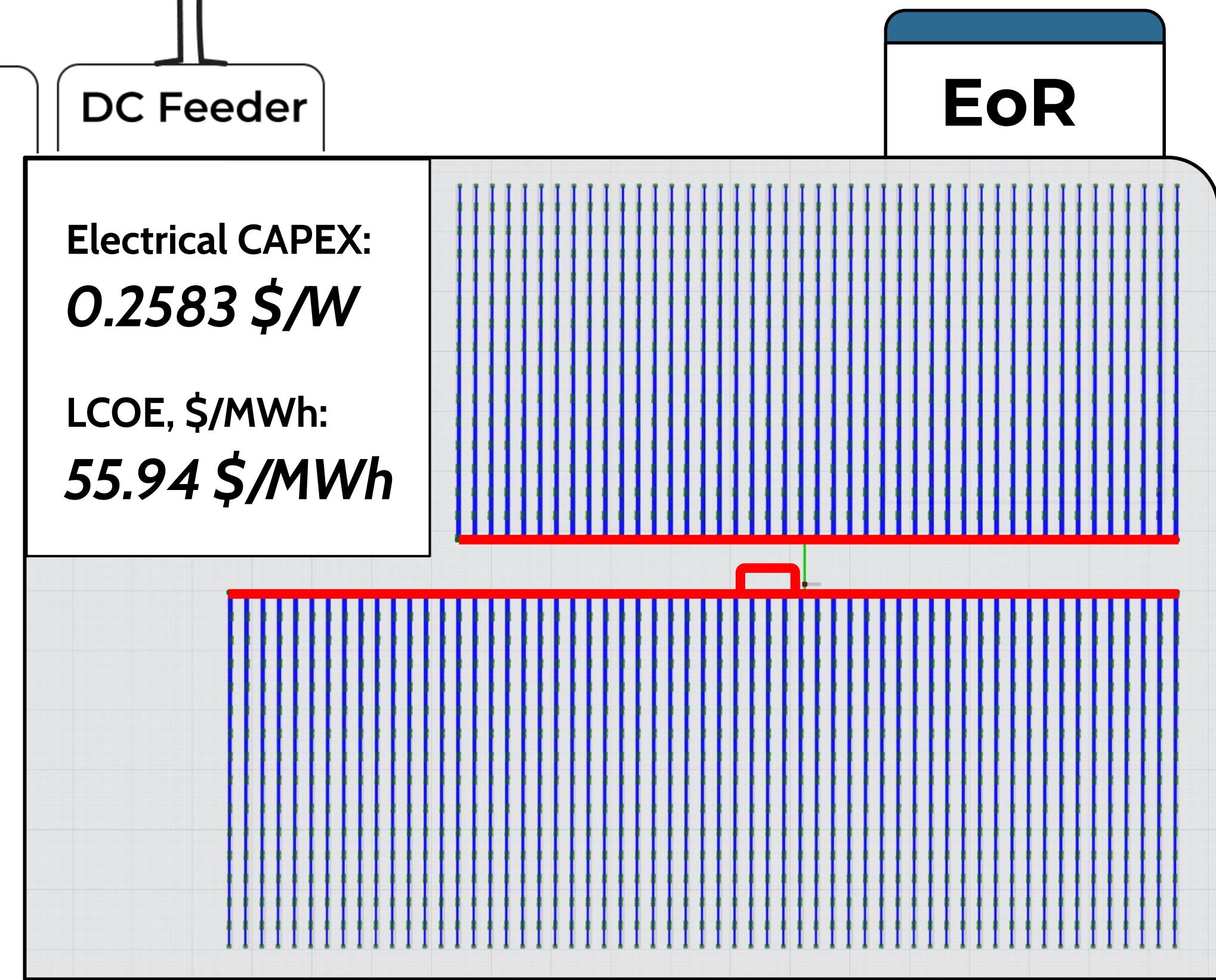


# CBX/LBD Position

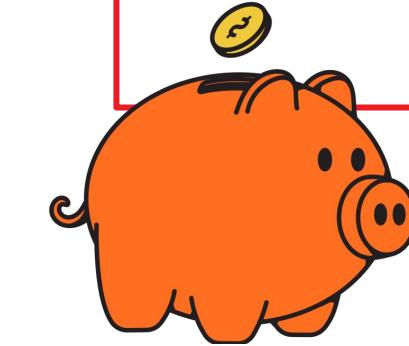
Middle of Row (MoR)  
vs End of row (EoR)



VS

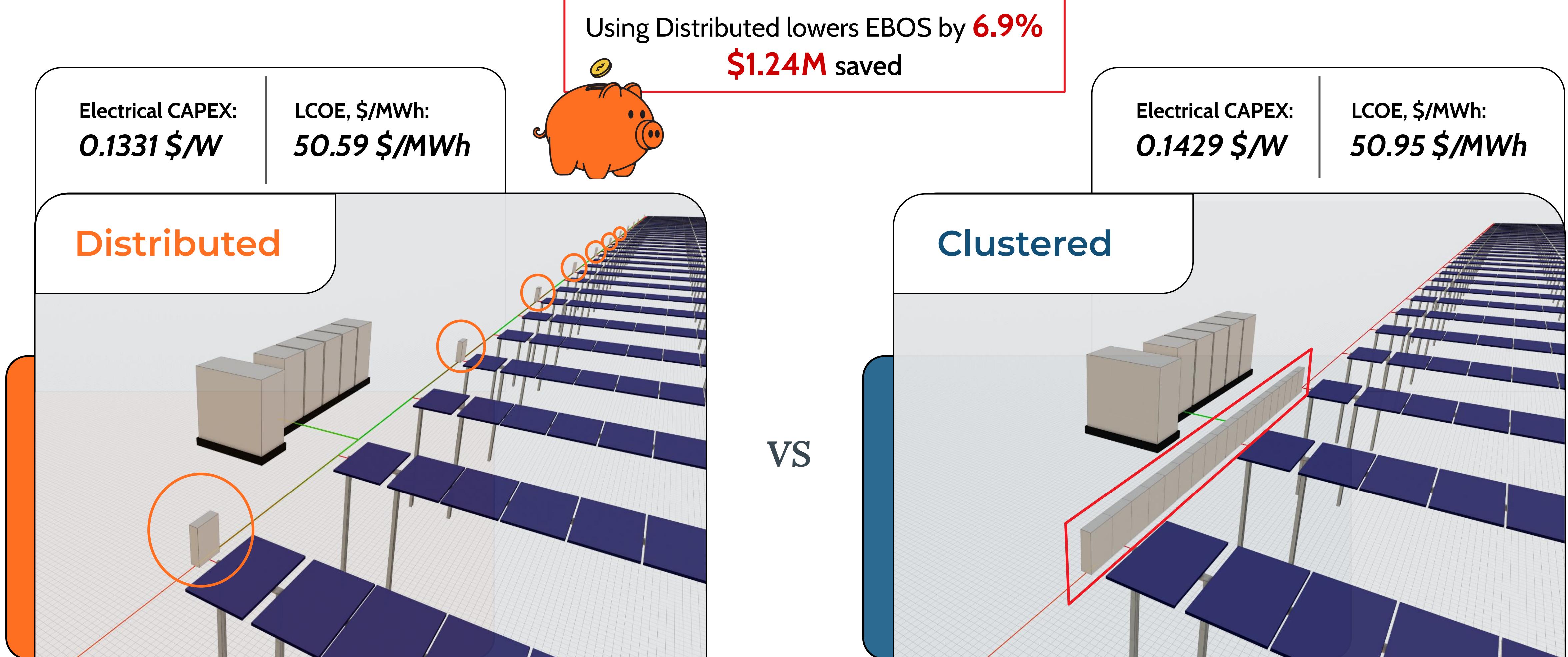


Using MoR lowers EBOS by **28.1%**  
**\$9.18M saved**

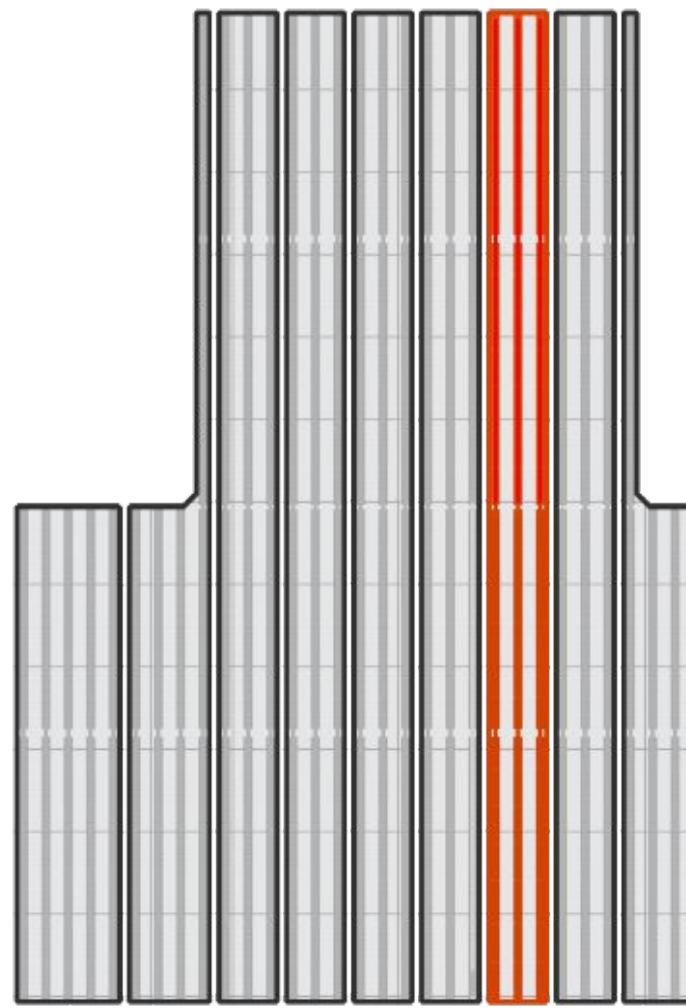


# CBX/LBD Clustering

## Distributed vs Clustered



# Row Grouping

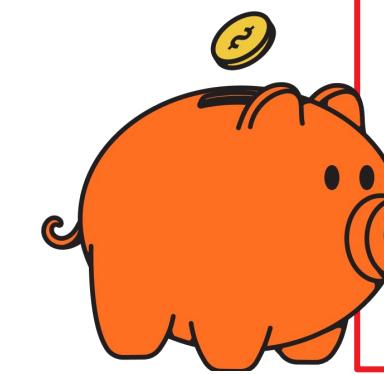
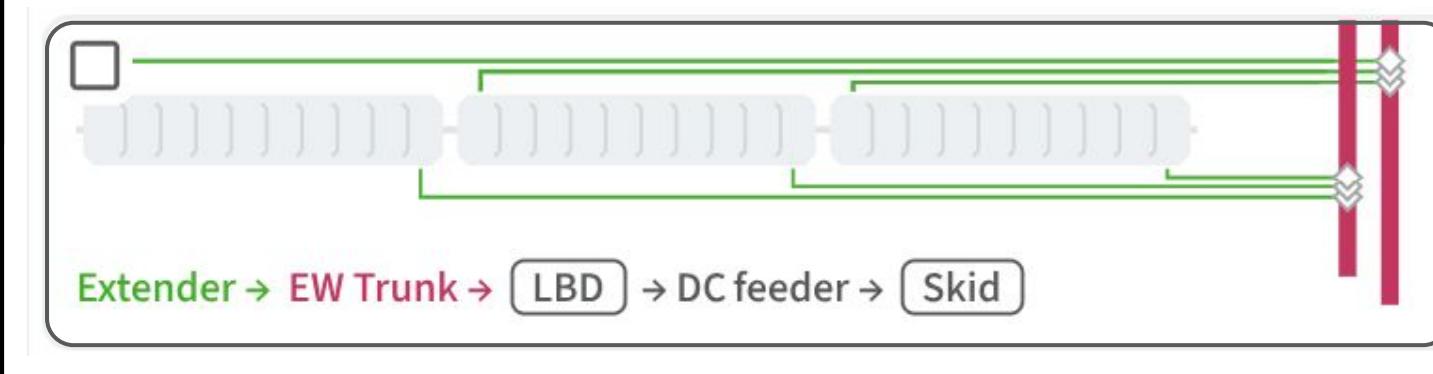
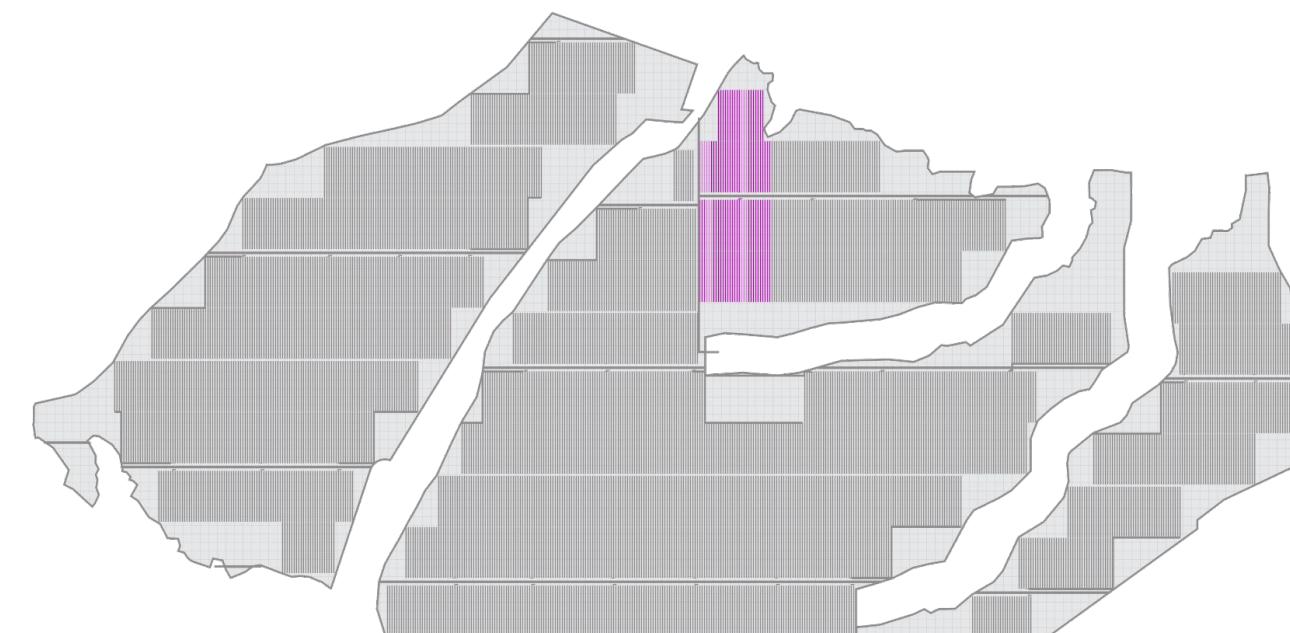


## Multiple

Electrical CAPEX:  
**0.2488 \$/W**

LCOE, \$/MWh:  
**53.88 \$/MWh**

- Distributed  Clustered
- Partial  Full
- Middle of row  End of row
- Single array  Multi array



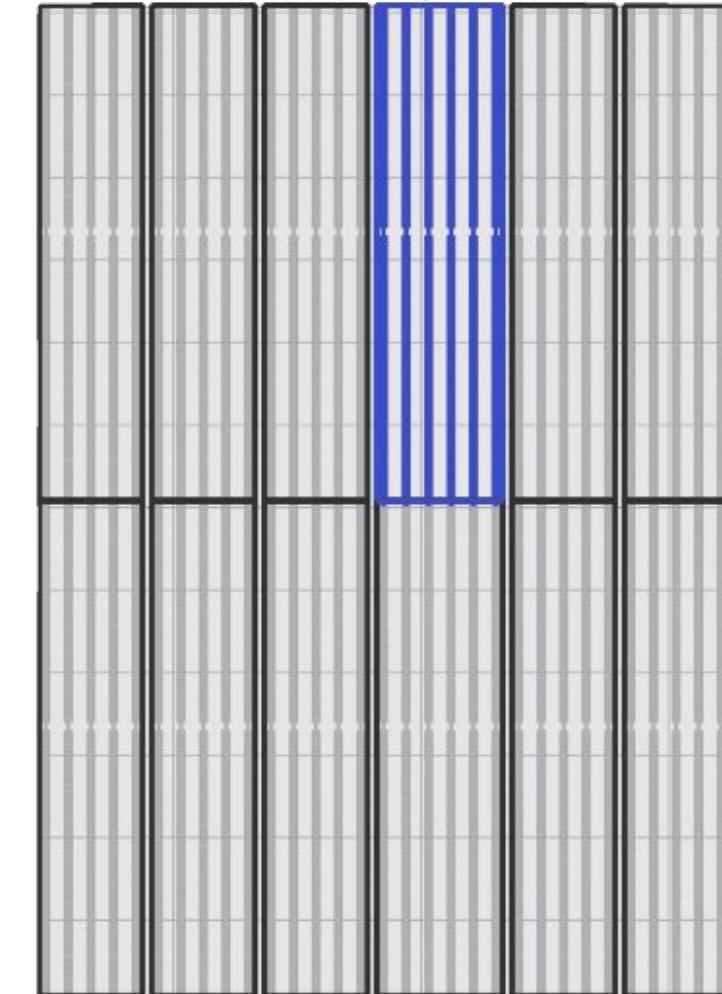
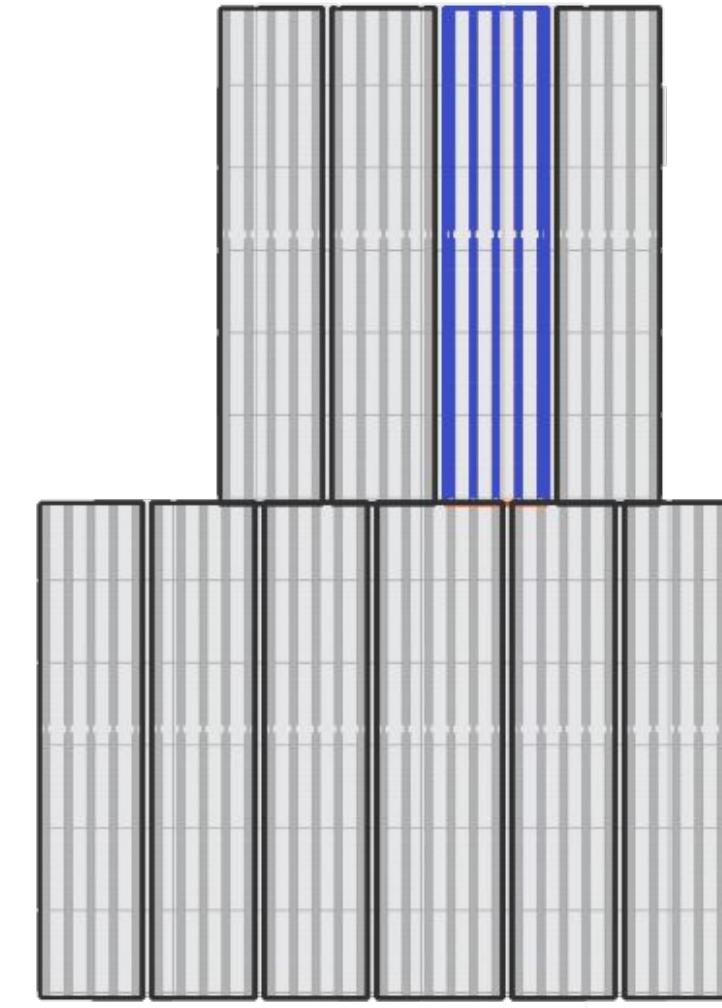
Using Multiple row grouping  
lowers EBOS by **24.22%**  
**\$7.6M saved**

## Single

Electrical CAPEX:  
**0.1886 \$/W**

LCOE, \$/MWh:  
**54.13 \$/MWh**

- Distributed  Clustered
- Partial  Full
- Middle of row  End of row
- Single array  Multi array

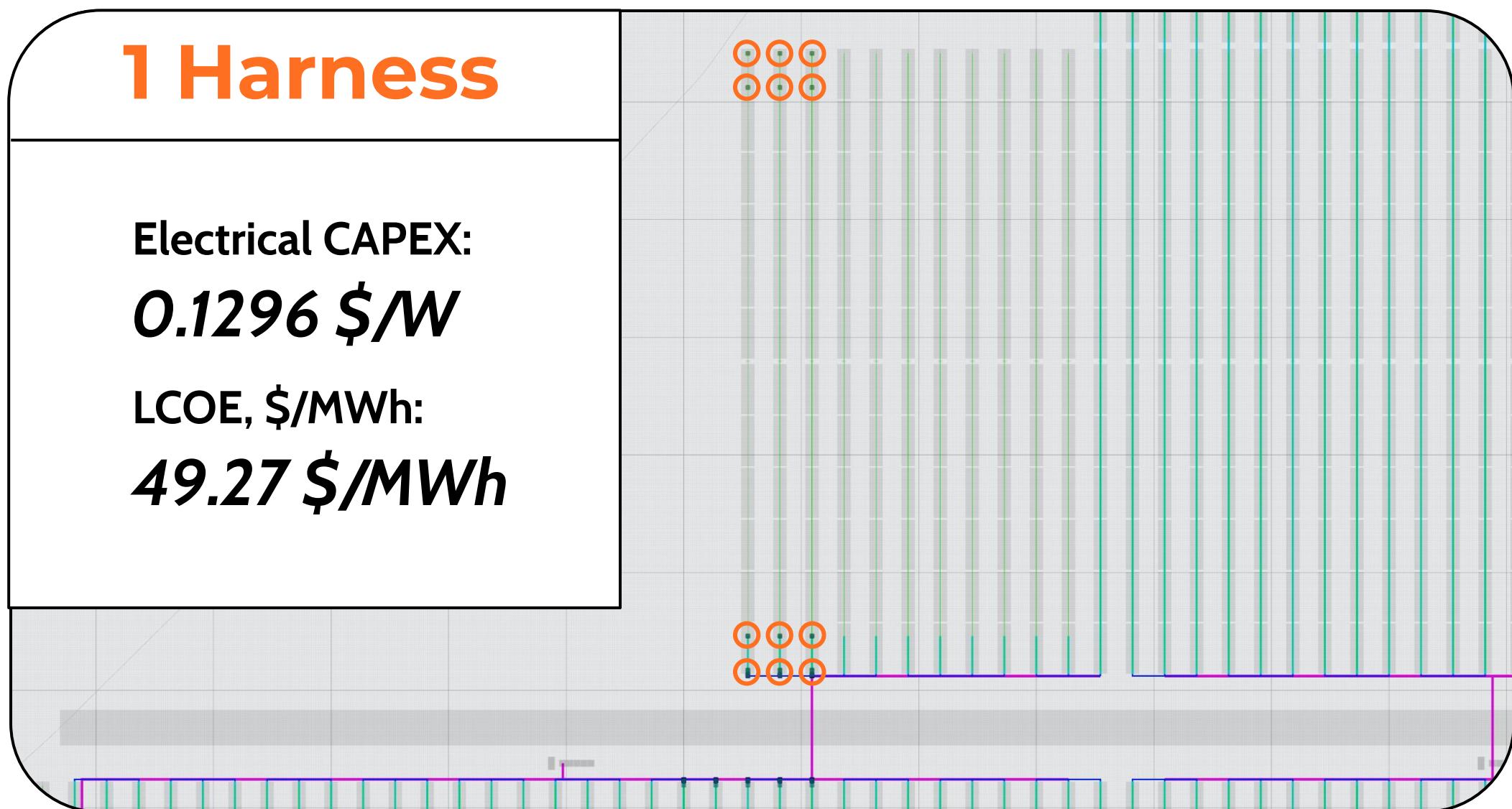
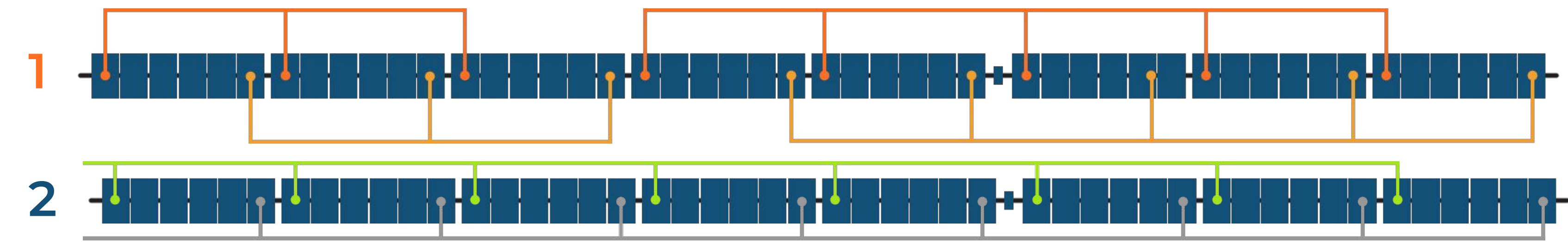


# Harness Configuration

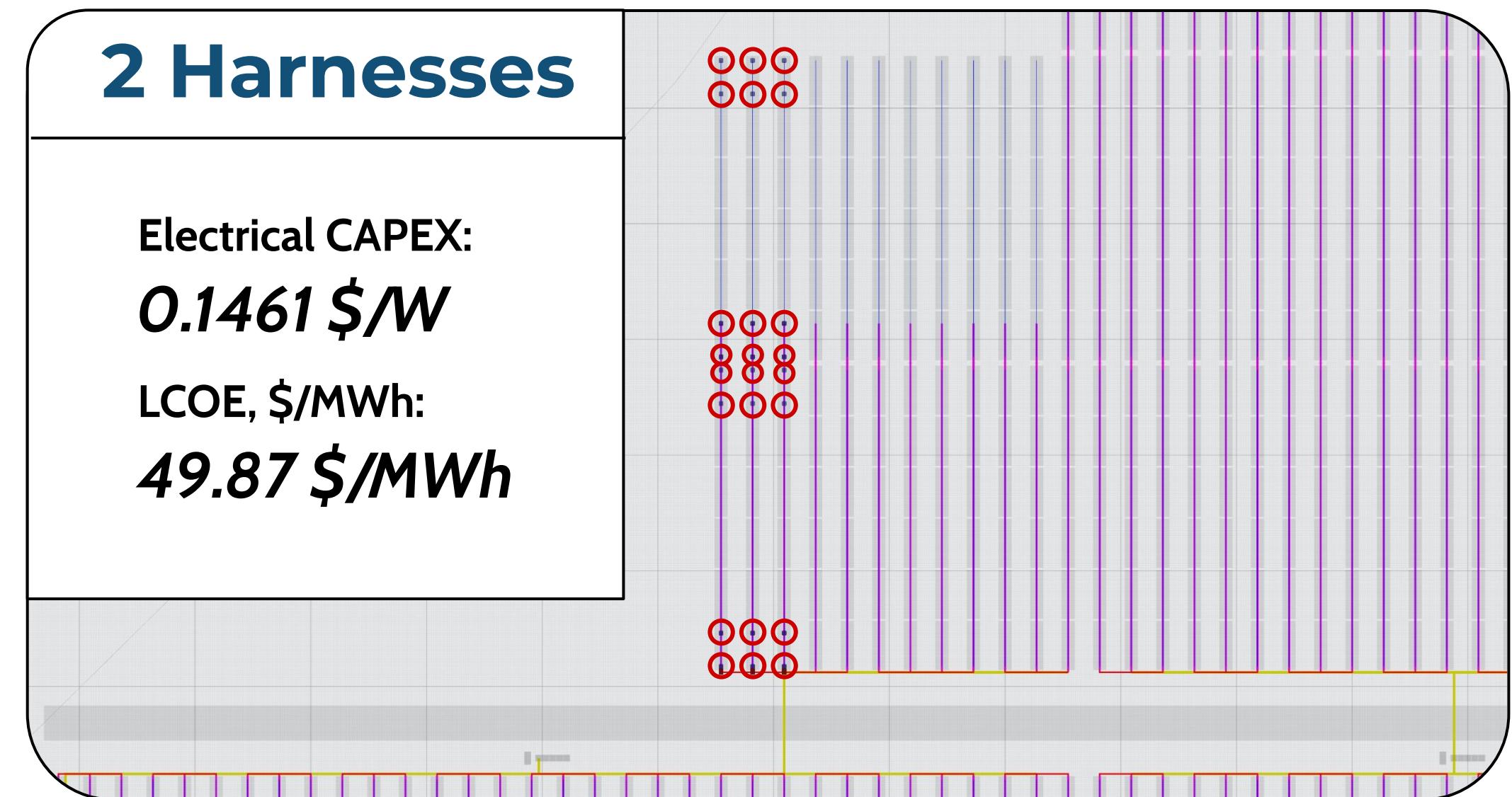
With two harnesses, the cost increased, but the voltage drop was reduced by half - from 3% to 1.5%



Using 1 Harness lowers EBOS by **11.3%**  
**\$2.1 M** saved

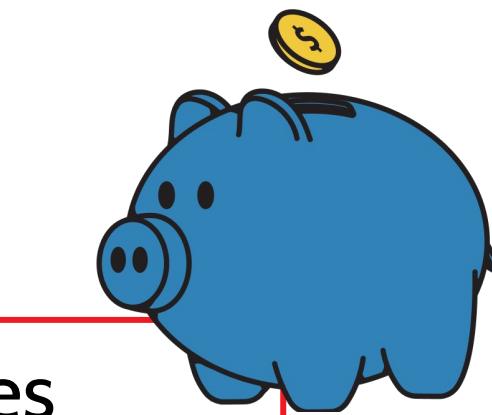


VS

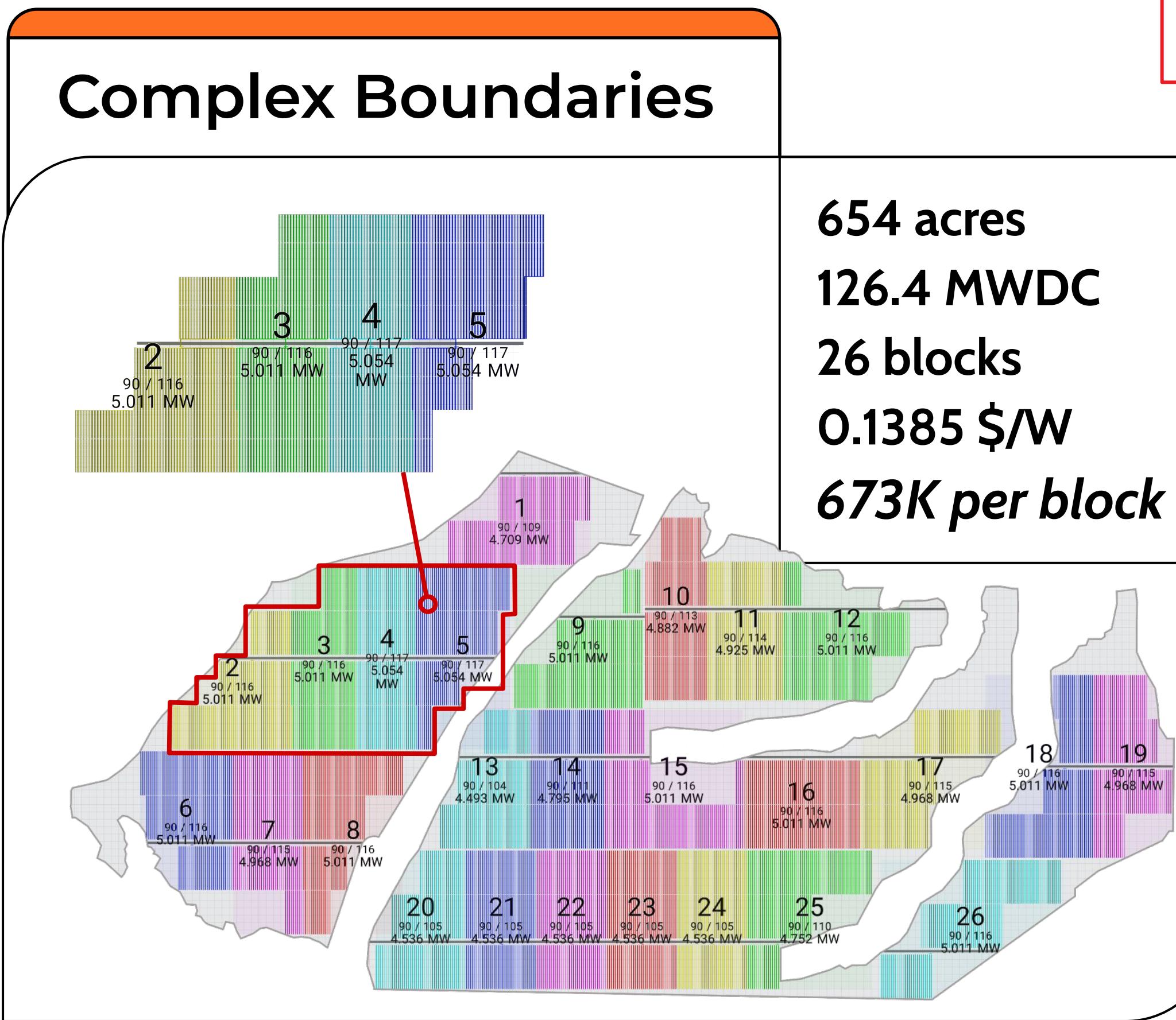


# Boundaries Complexity

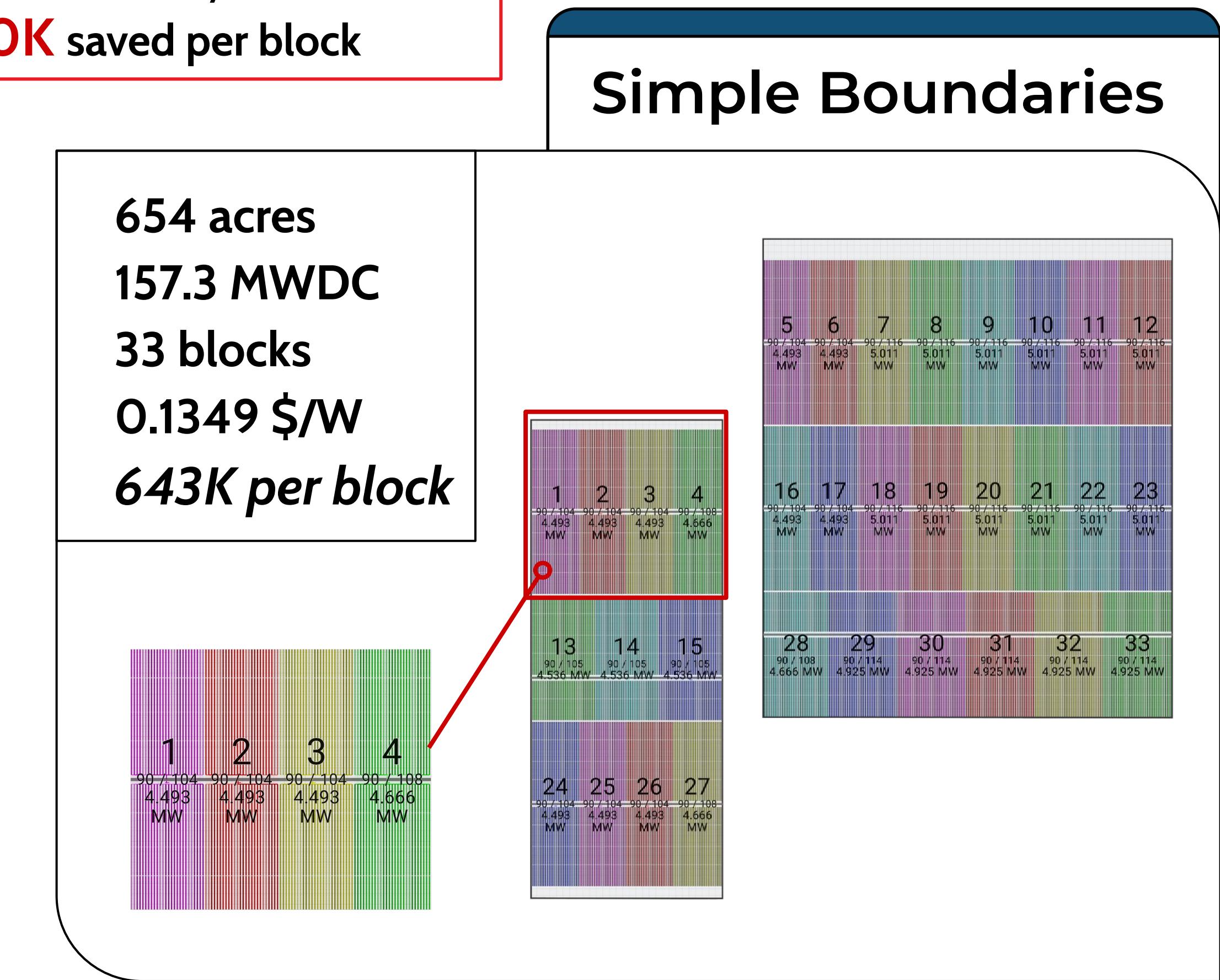
*Same buildable area and DC/AC ratio,  
max DC optimisation*



Using Simple Boundaries  
lowers EBOS by **2.6%**  
**\$30K** saved per block

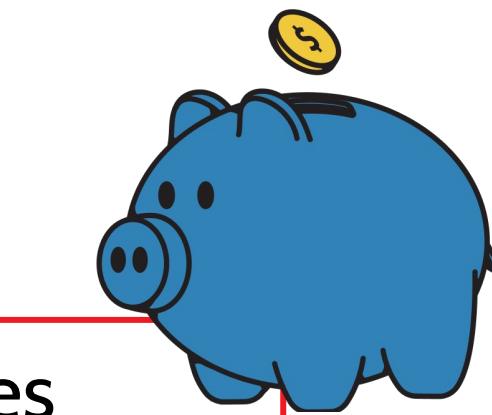


VS

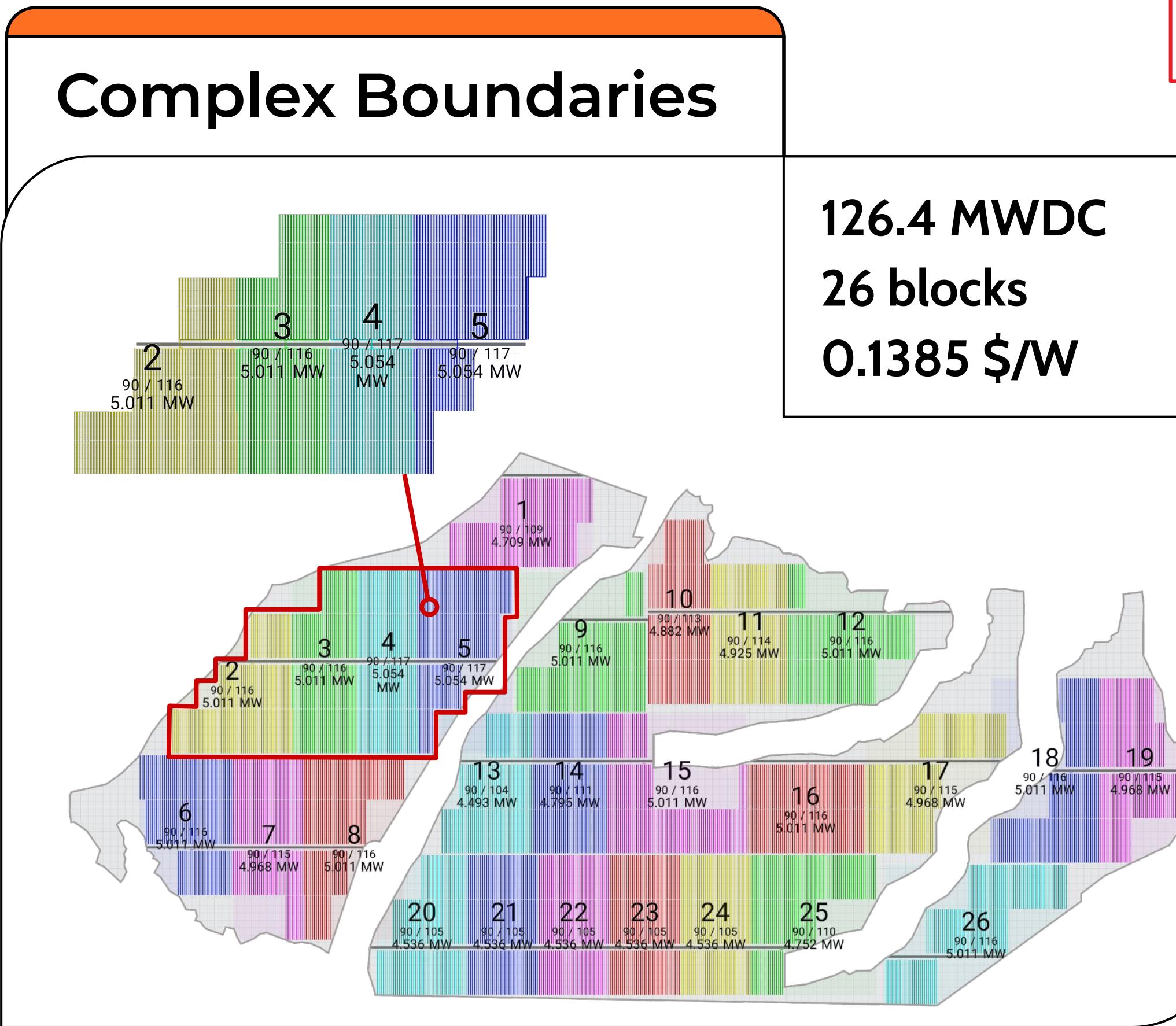


# Boundaries Complexity

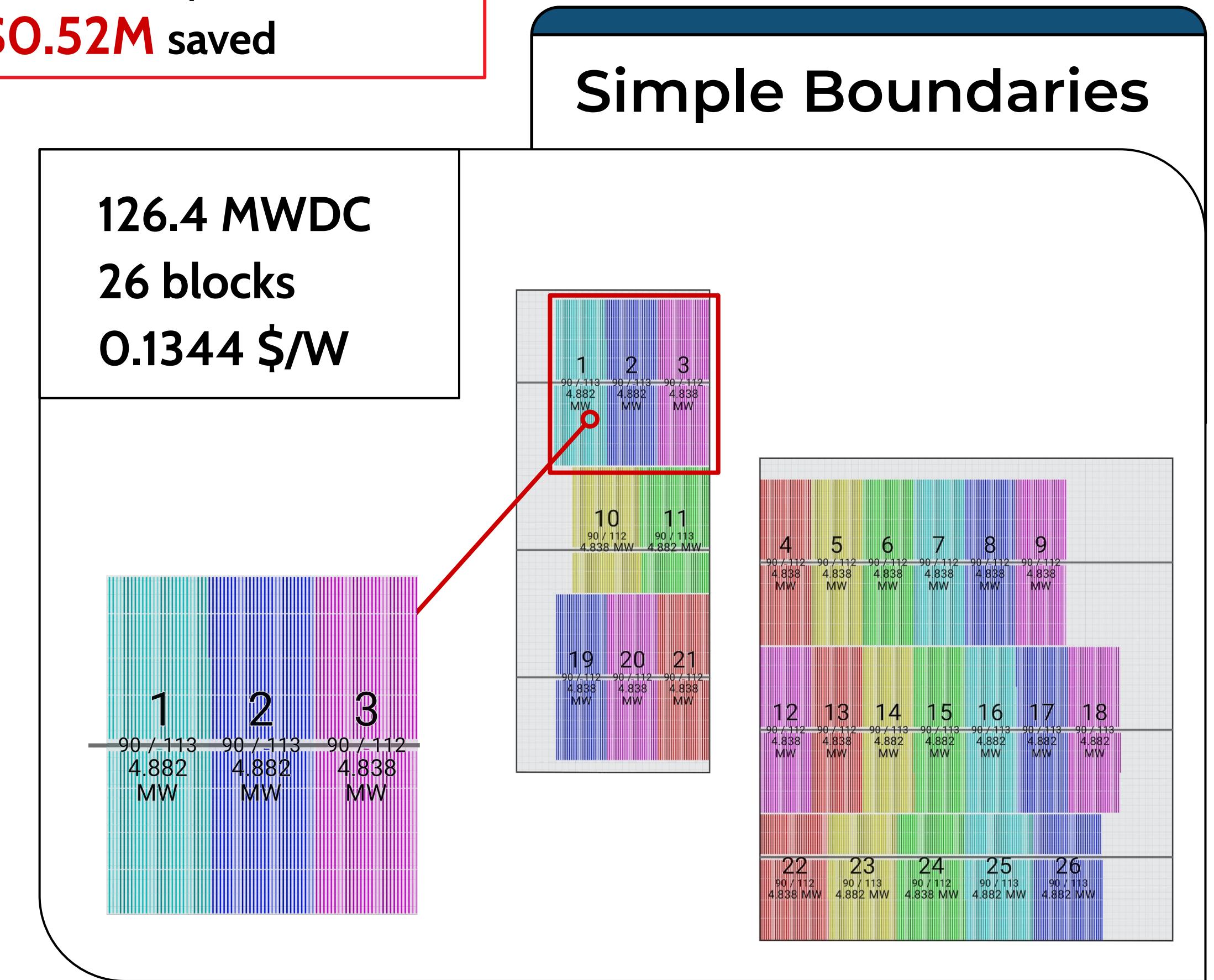
*Same buildable area, same DC/AC ratio,  
same number of blocks*



Using Simple Boundaries  
lowers EBOS by **3.0%**  
**\$0.52M saved**

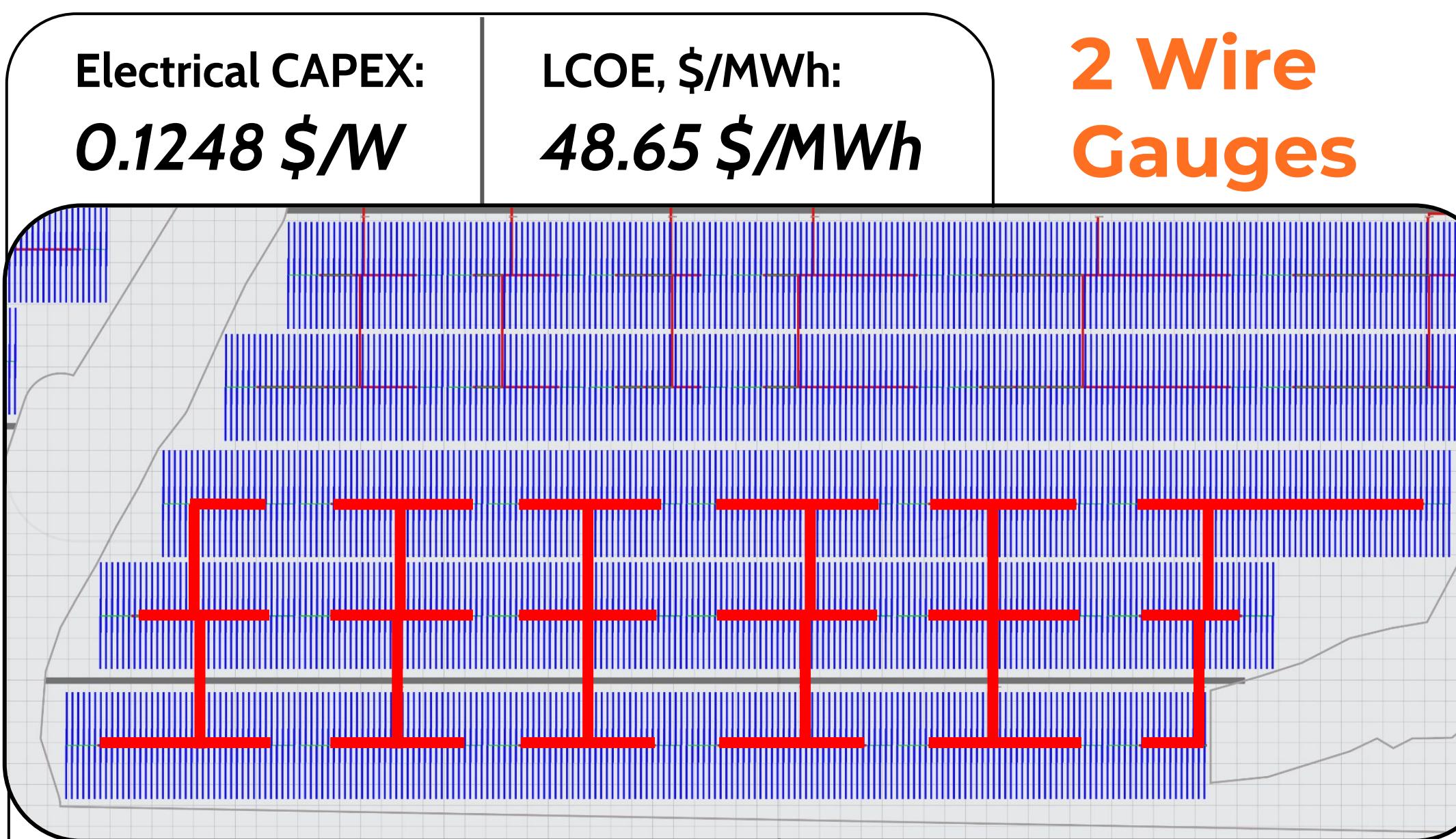


VS

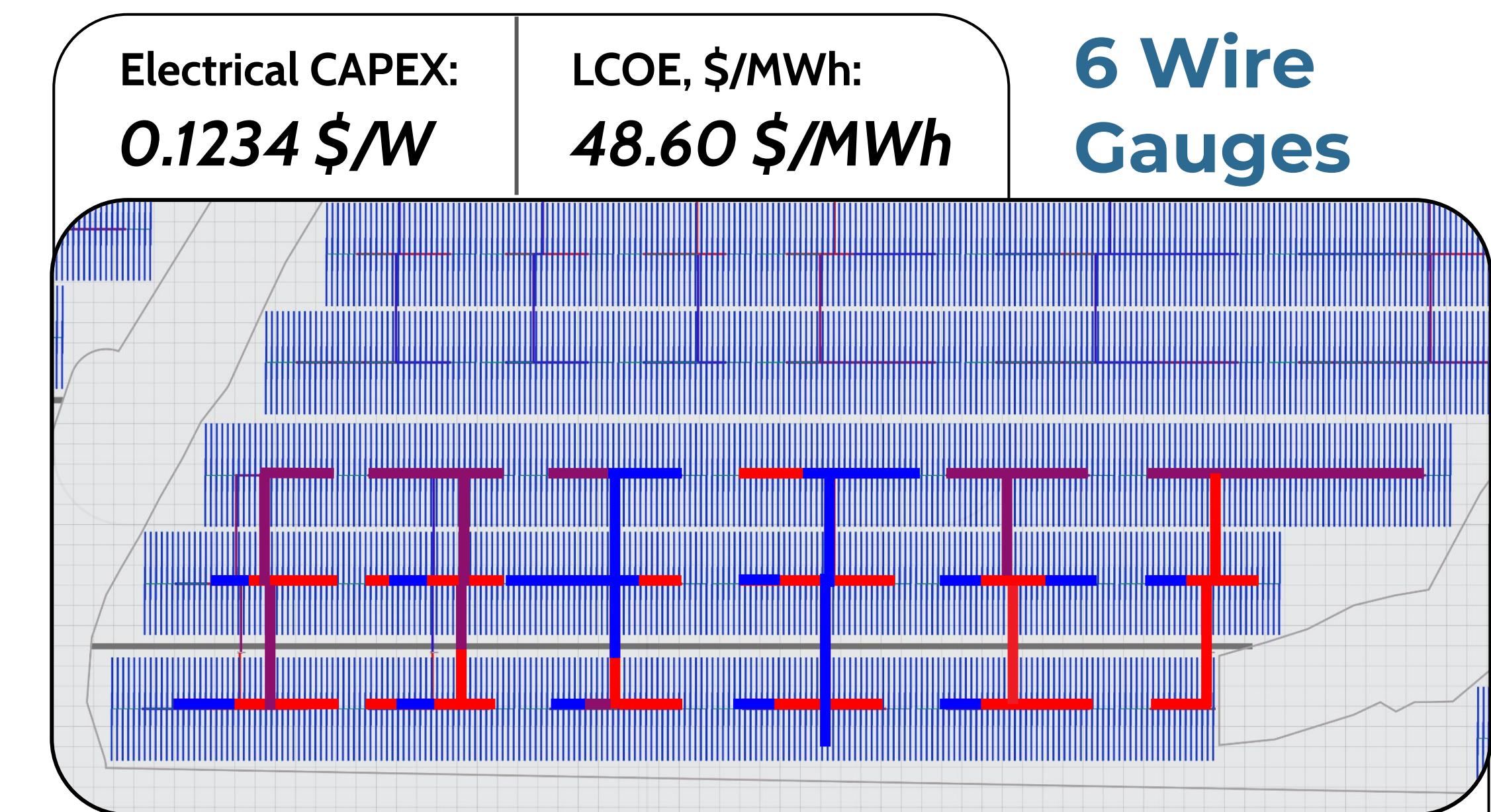


# Wire Gauges Unification

*Affects procurement logistics, labor complexity, and splicing strategies*

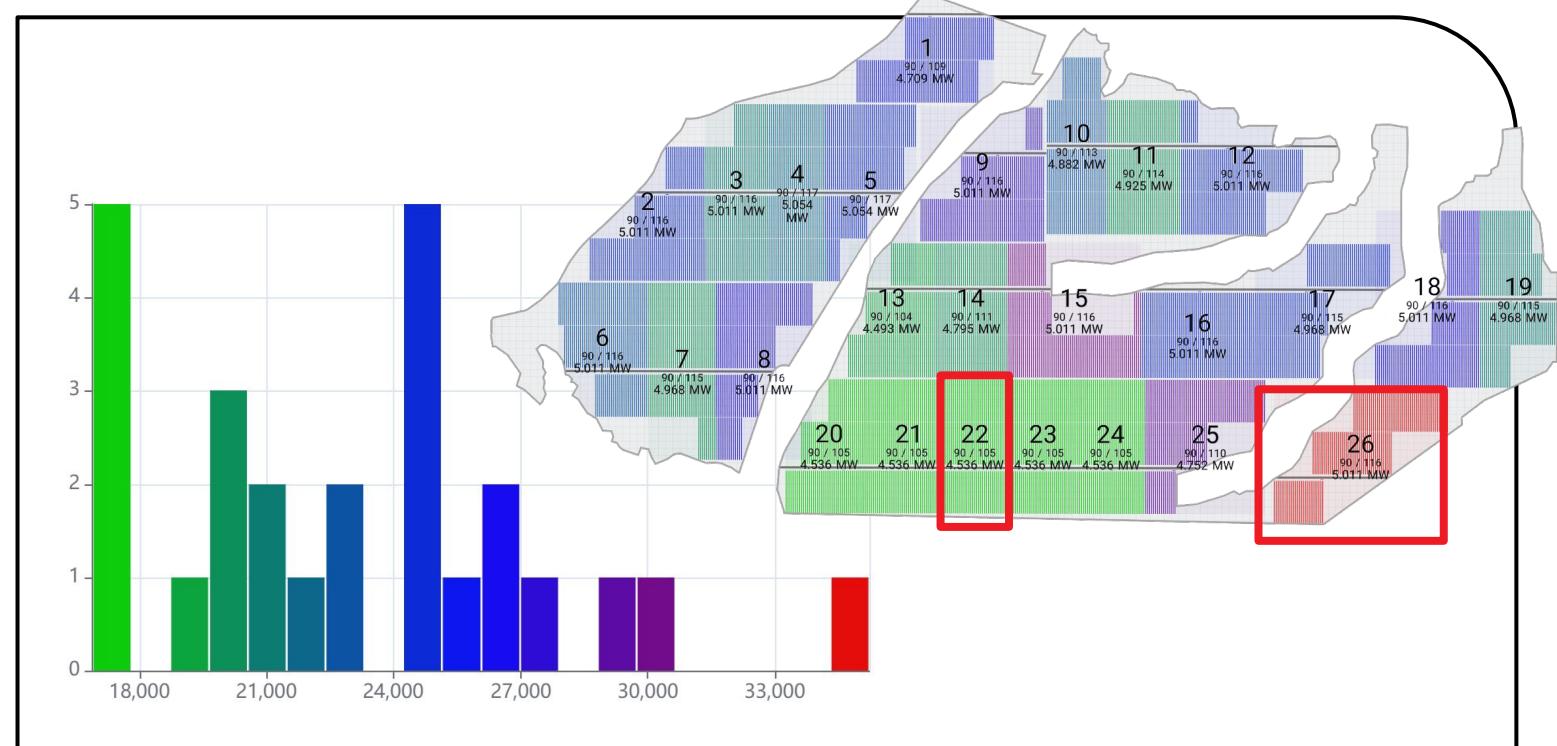


VS



# Block Footprint

*Affects procurement logistics, labor complexity and wire routing*



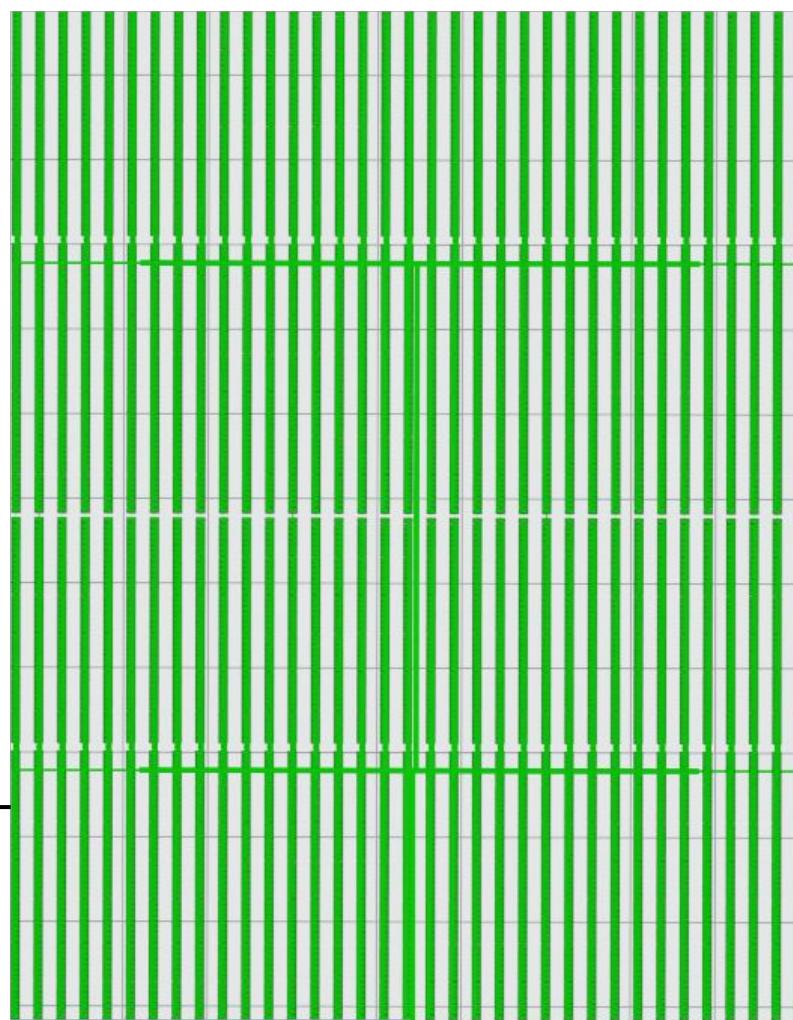
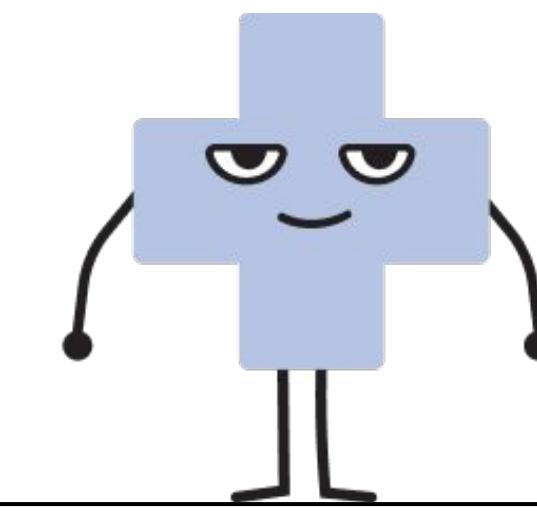
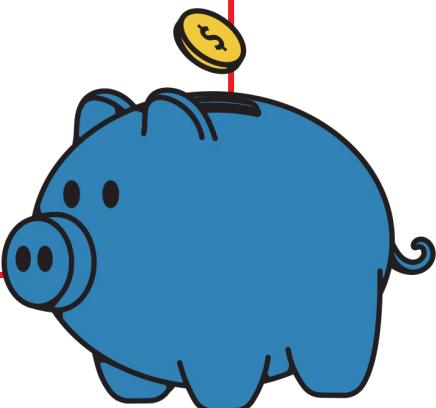
**\$ 716,138**

Electrical CAPEX:  
**0.1429 \$/W**

DC Feeder Length:  
**35,000 ft**

VS

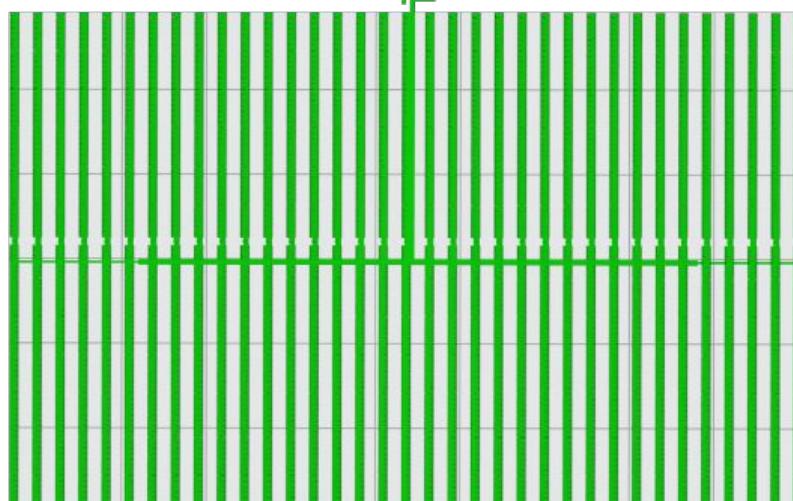
Designing regular shaped blocks lowers EBOS by **9.4%**  
**\$1.7M saved**



**\$ 587,087**

Electrical CAPEX:  
**0.1294 \$/W**

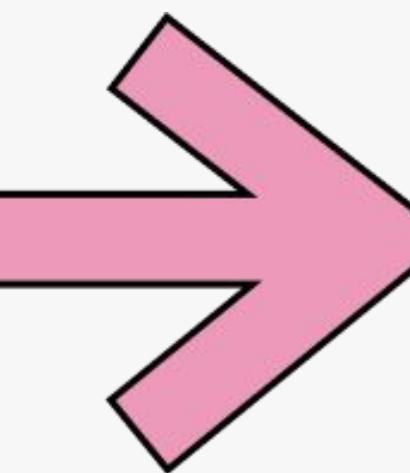
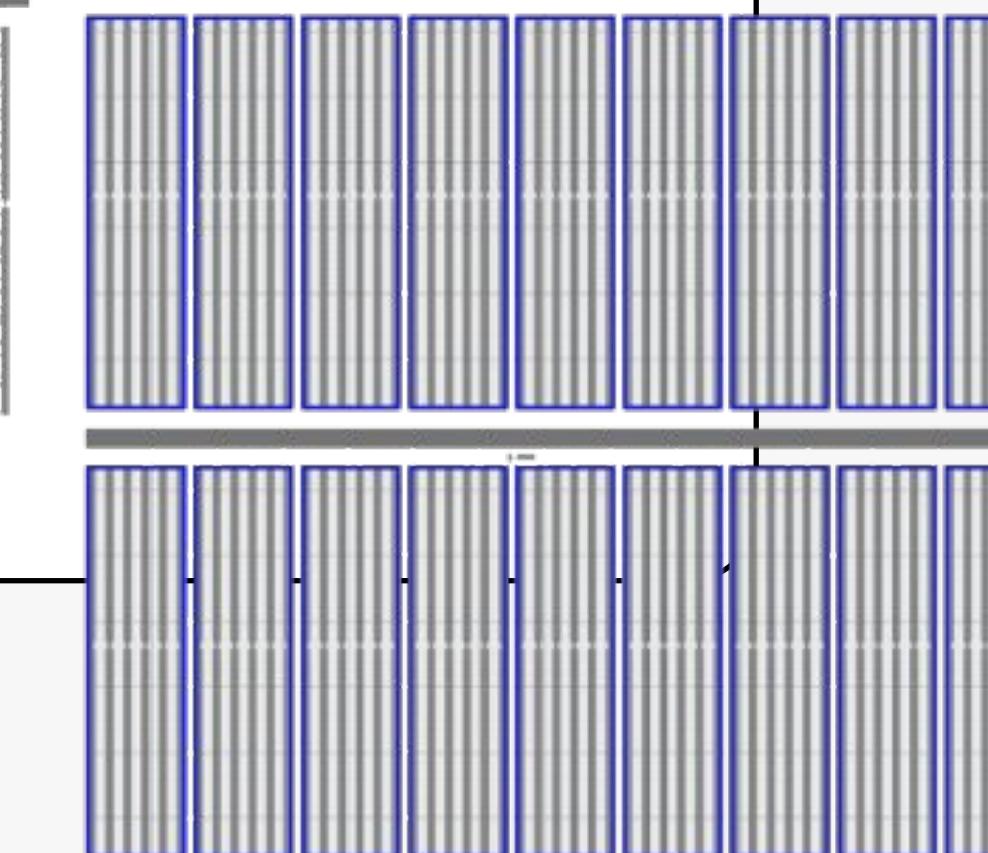
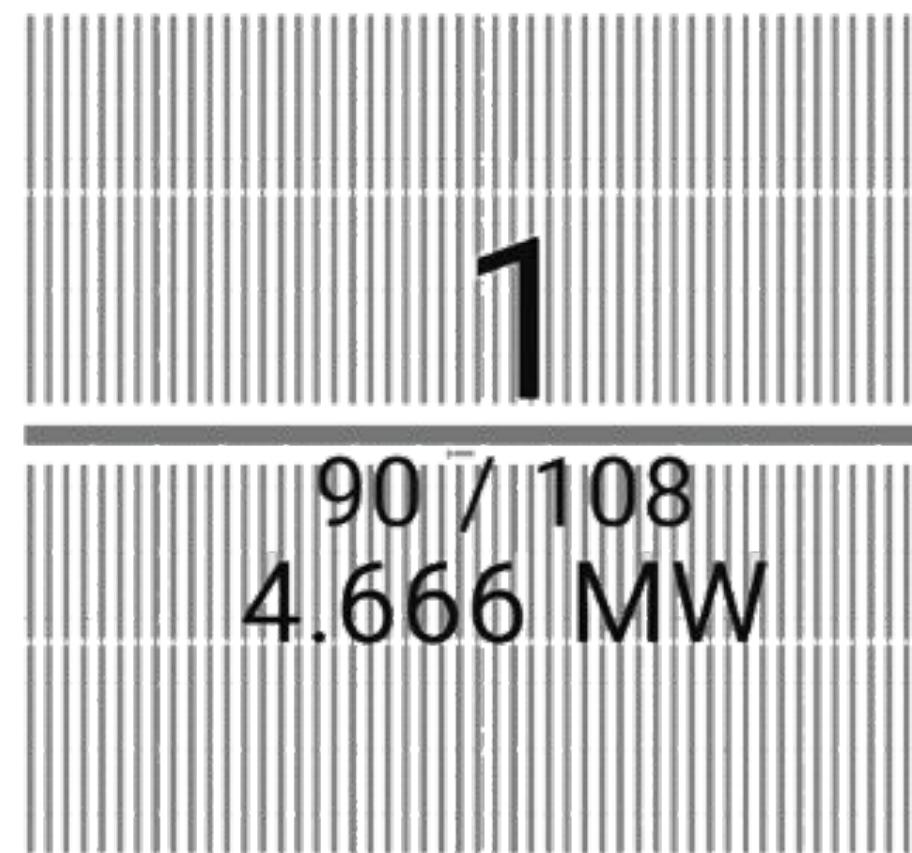
DC Feeder Length:  
**17,000 ft**



# Designed Standard Blocks vs Reality

## Designed Standard Blocks

\$/W (Ideal):  
**\$ 0.1186**



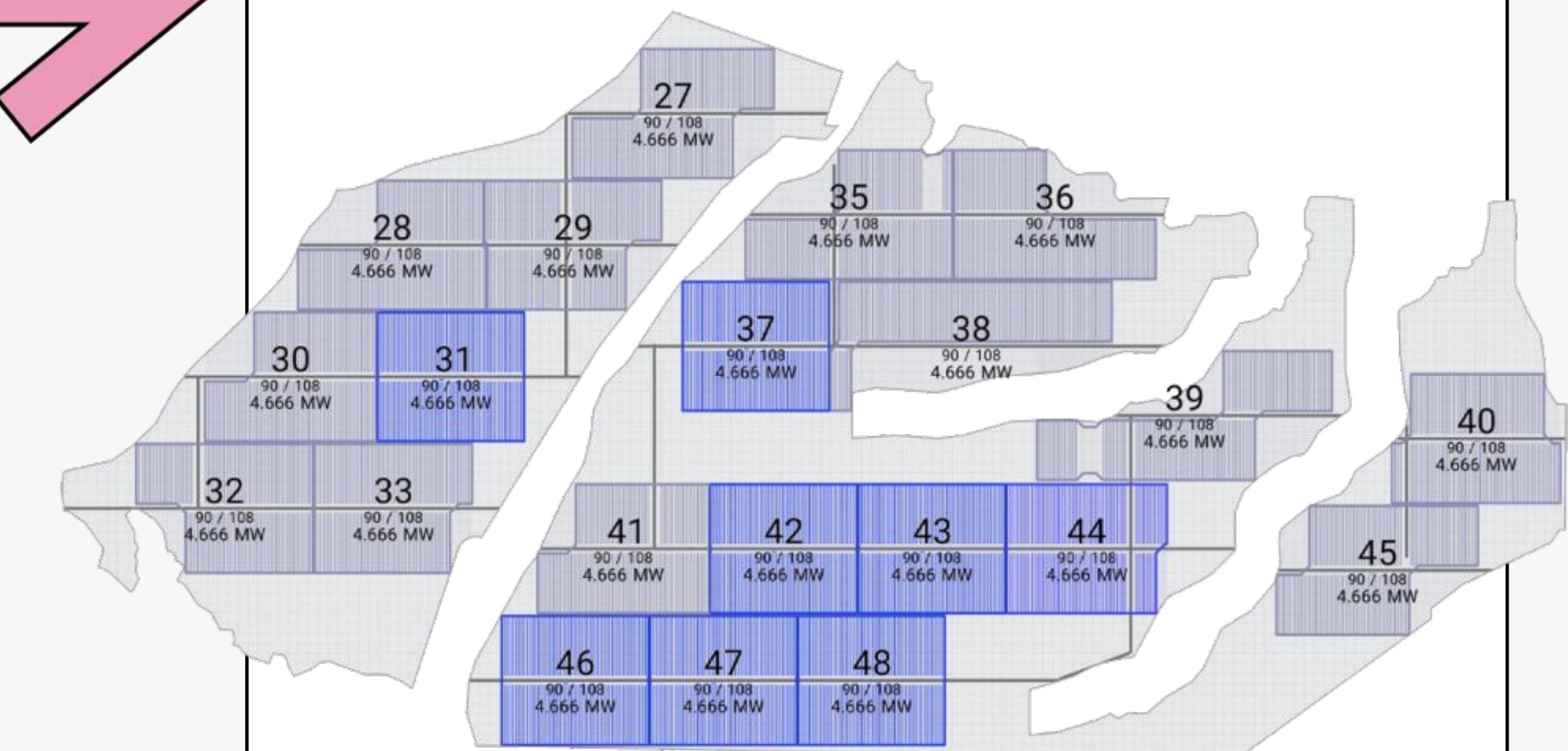
## Designed Layout

Total Blocks: 21

Standard Blocks: 8

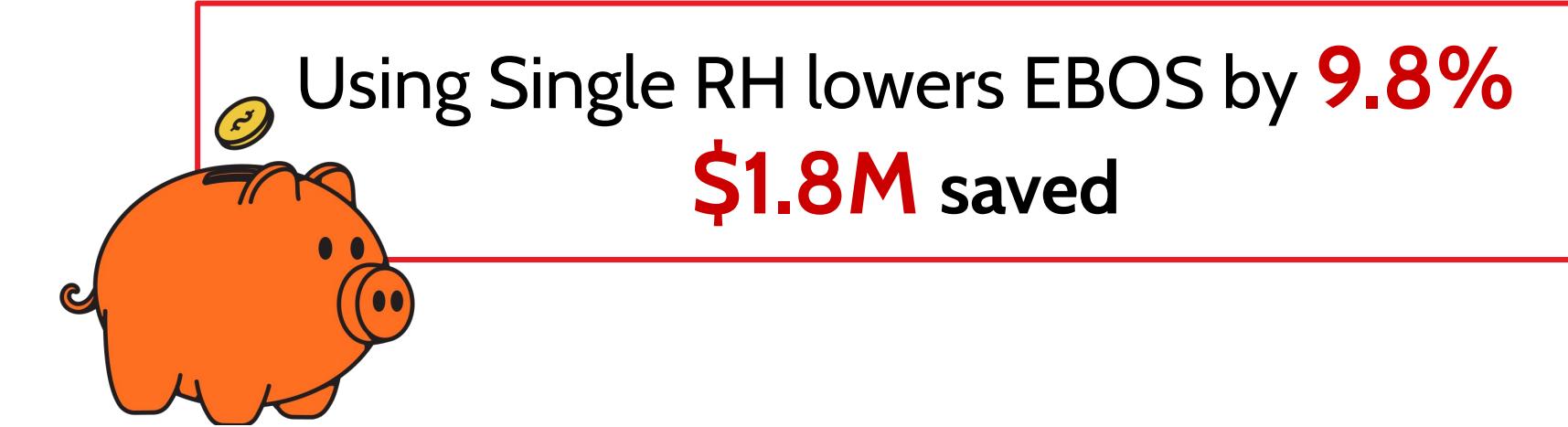
Standard Block Coverage: 38% (8/21)

\$/W (Actual):  
**\$ 0.1215**



# Row Height

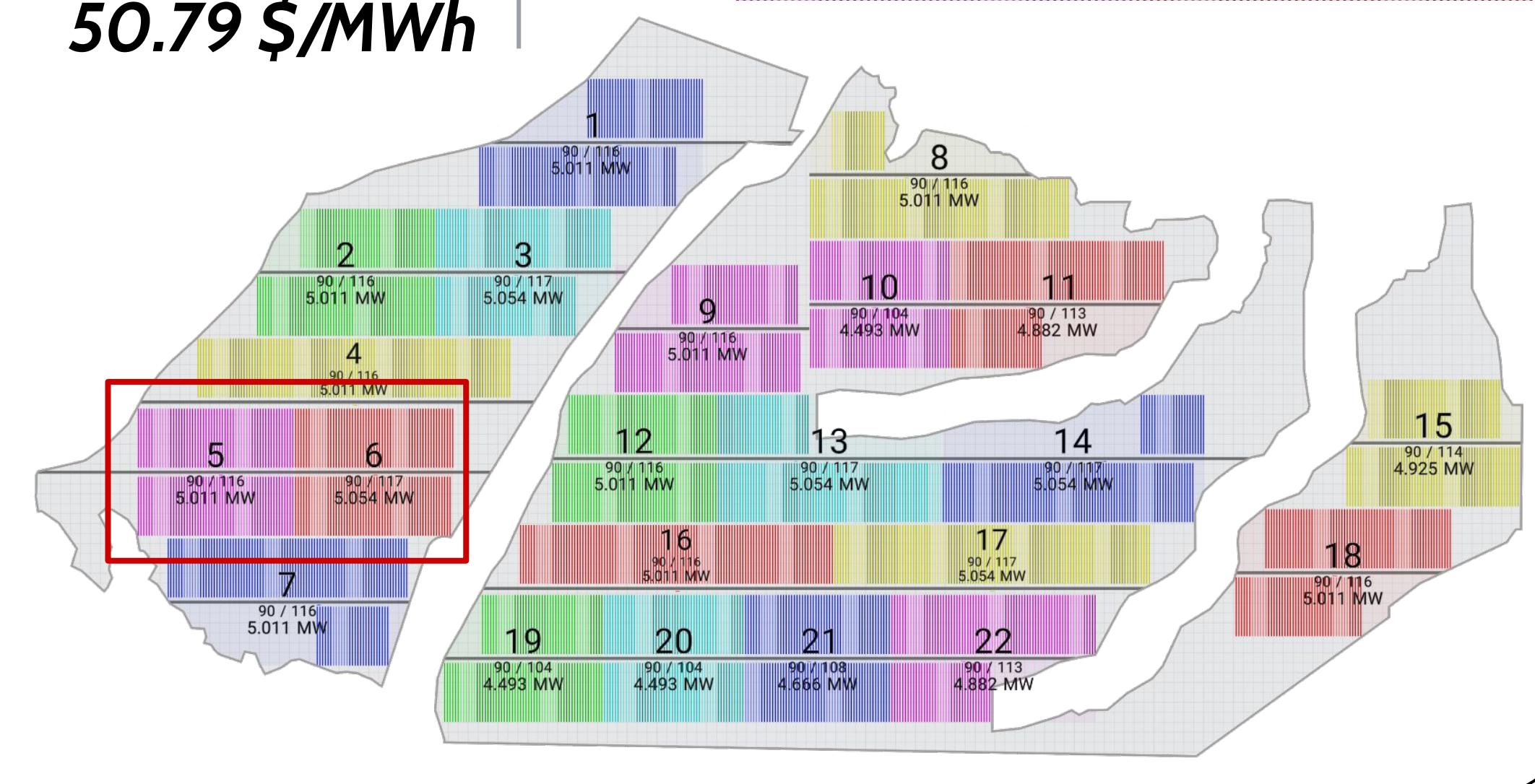
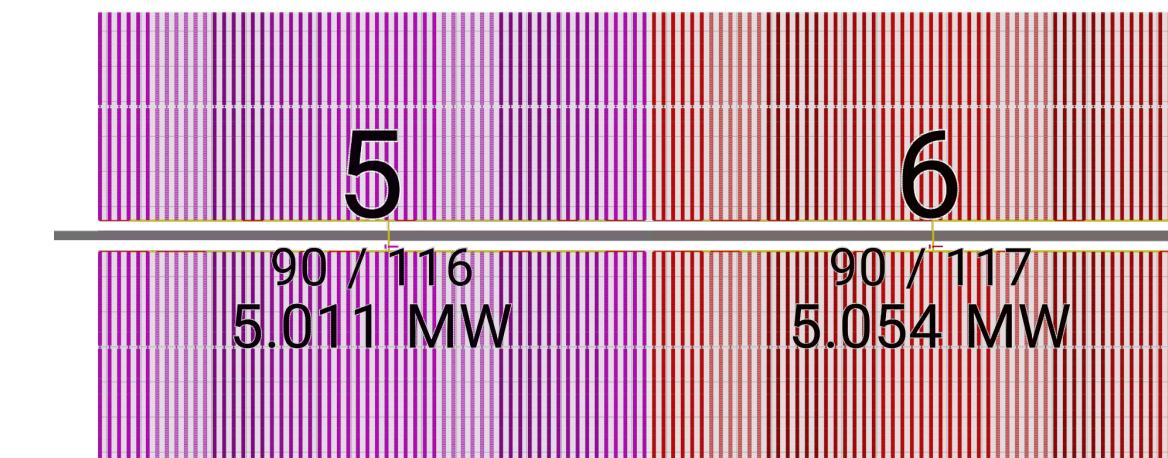
*Complex boundaries*



**Single row height (1)**

Electrical CAPEX:  
**0.1285 \$/W**

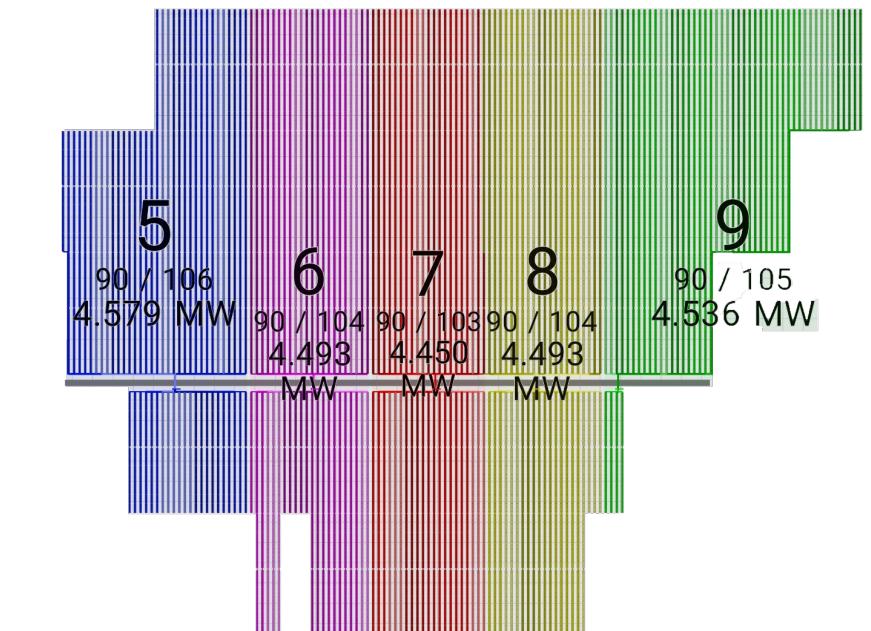
LCOE, \$/MWh:  
**50.79 \$/MWh**



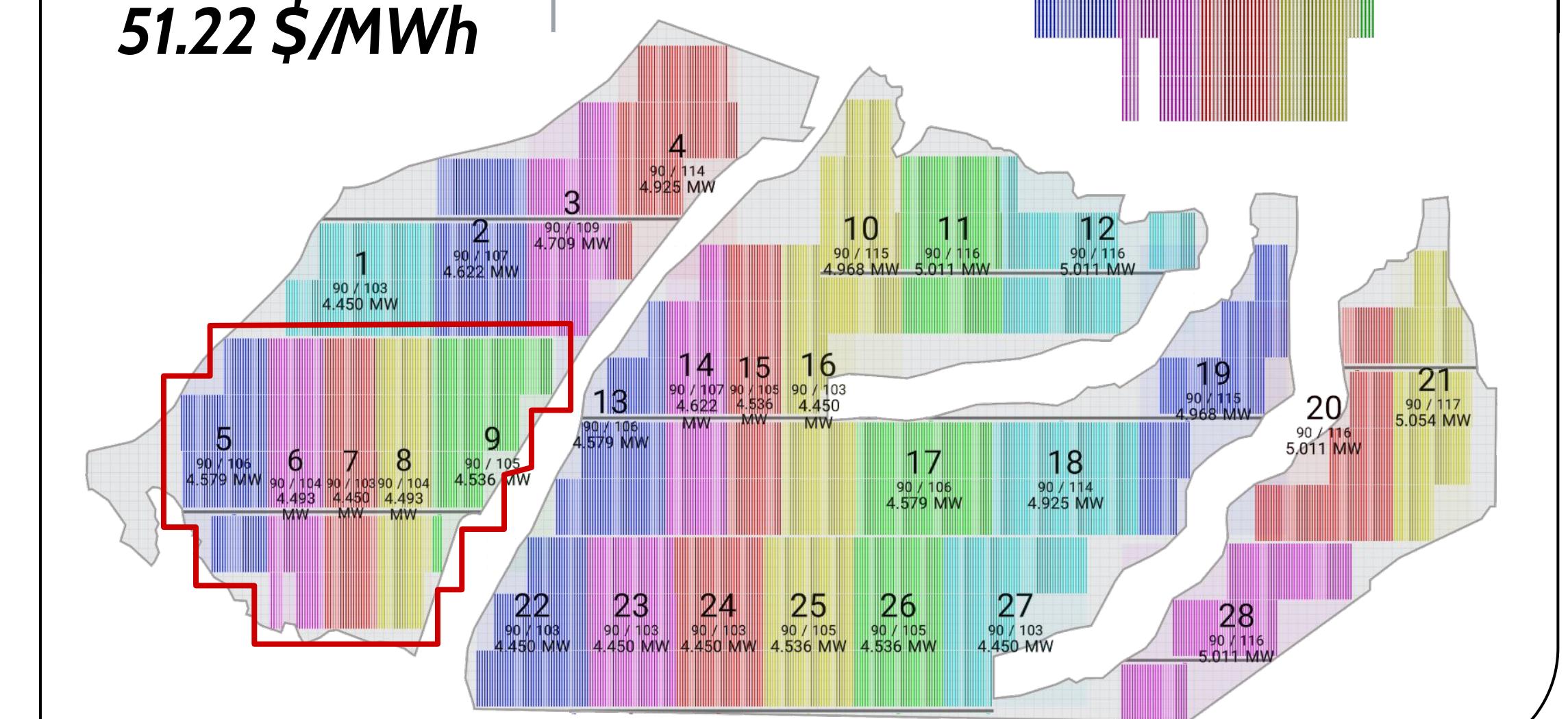
**Multiple row height (3)**

Electrical CAPEX:  
**0.1425 \$/W**

LCOE, \$/MWh:  
**51.22 \$/MWh**



VS



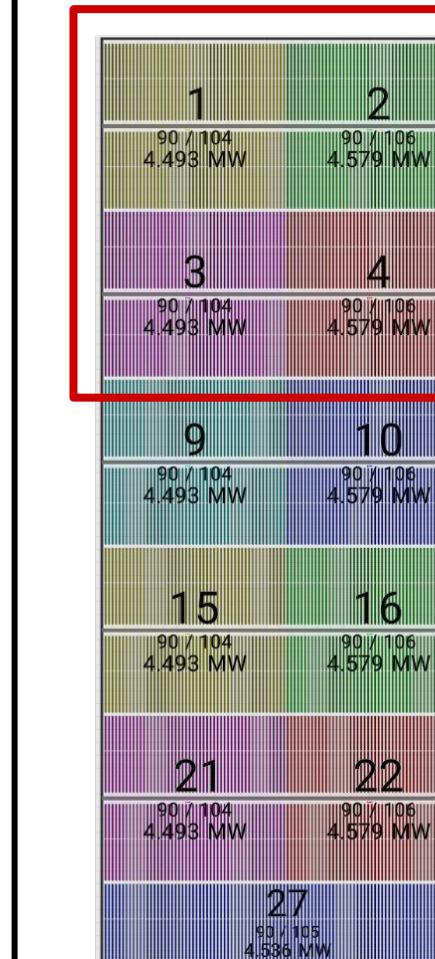
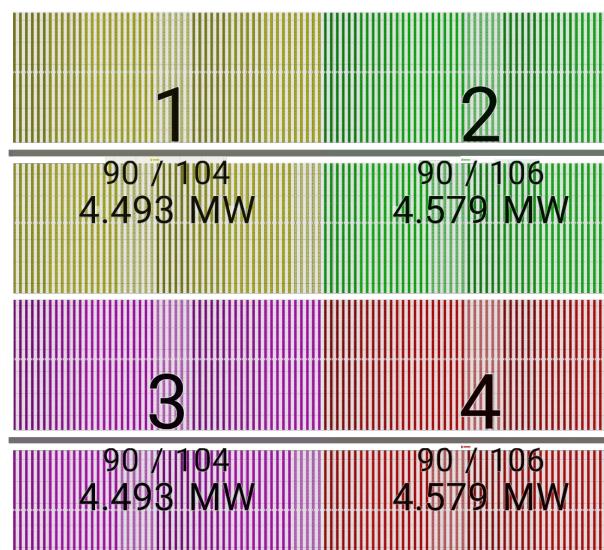
# Row Height

# *Simple boundaries*

# Single row height (1)

# Electrical CAPEX: 0.1323 \$/W

**LCOE, \$/MWh:**  
**49.81 \$/MWh**



Using Single RH lowers EBOS by **9.7%**  
**\$2.2M** saved

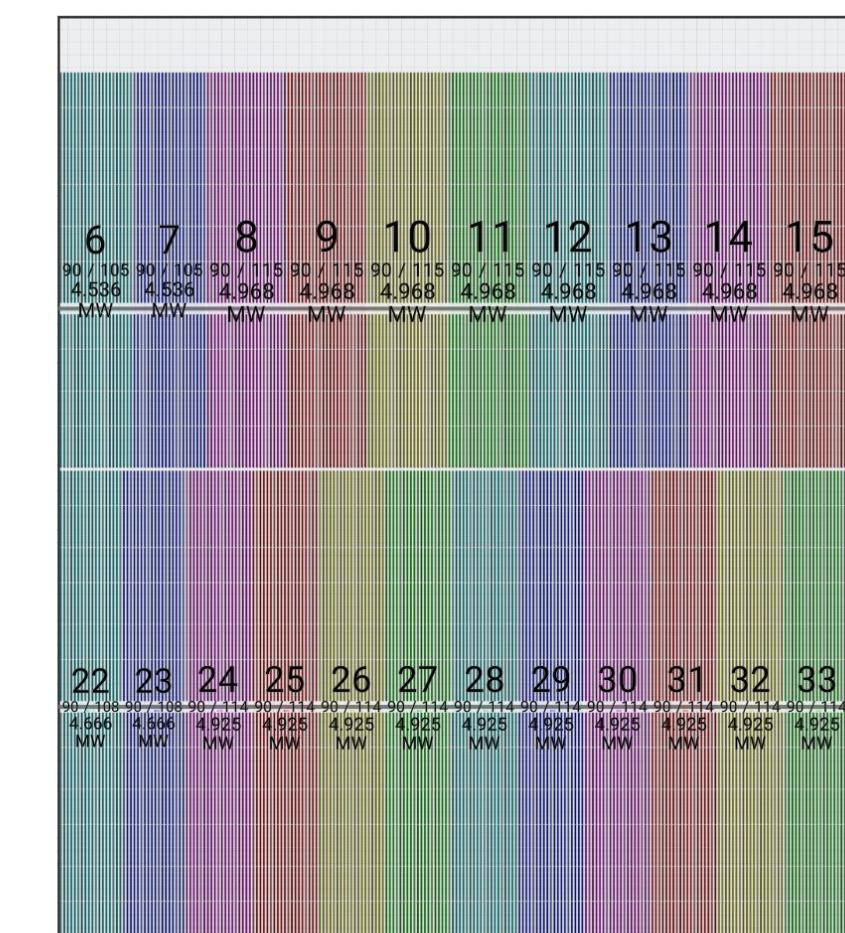
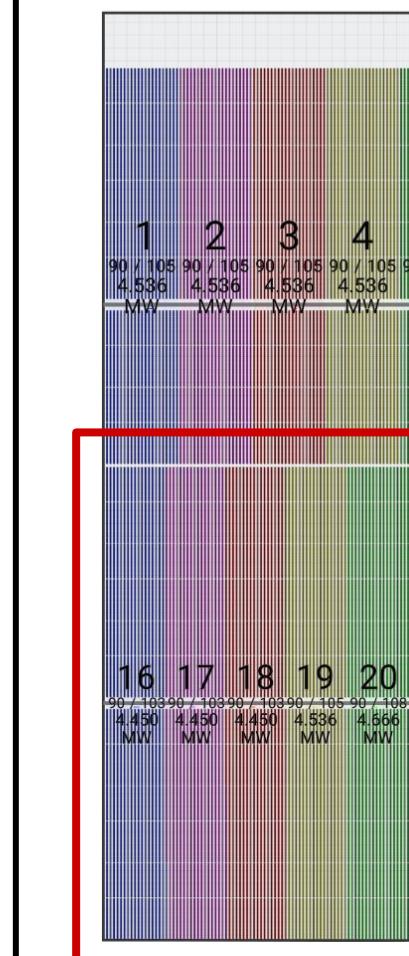
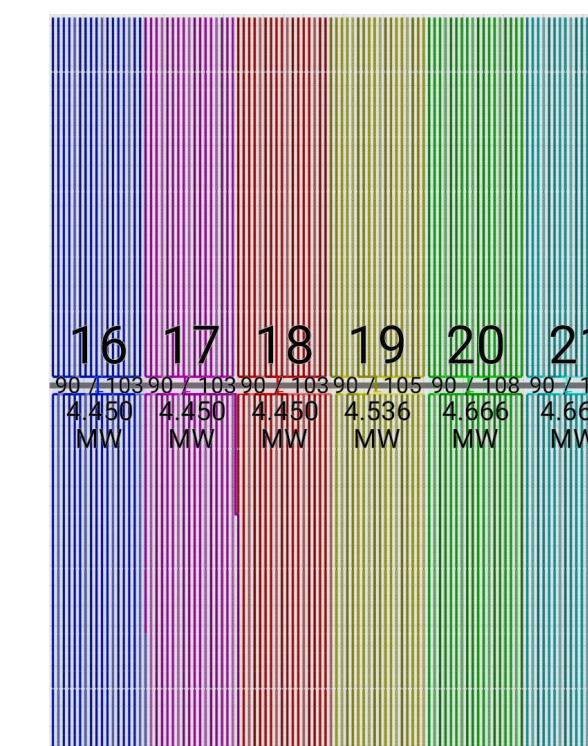
# Multiple row height (3)

# Electrical CAPEX

# 0.1465 \$/W

# LCOE, \$/MWh:

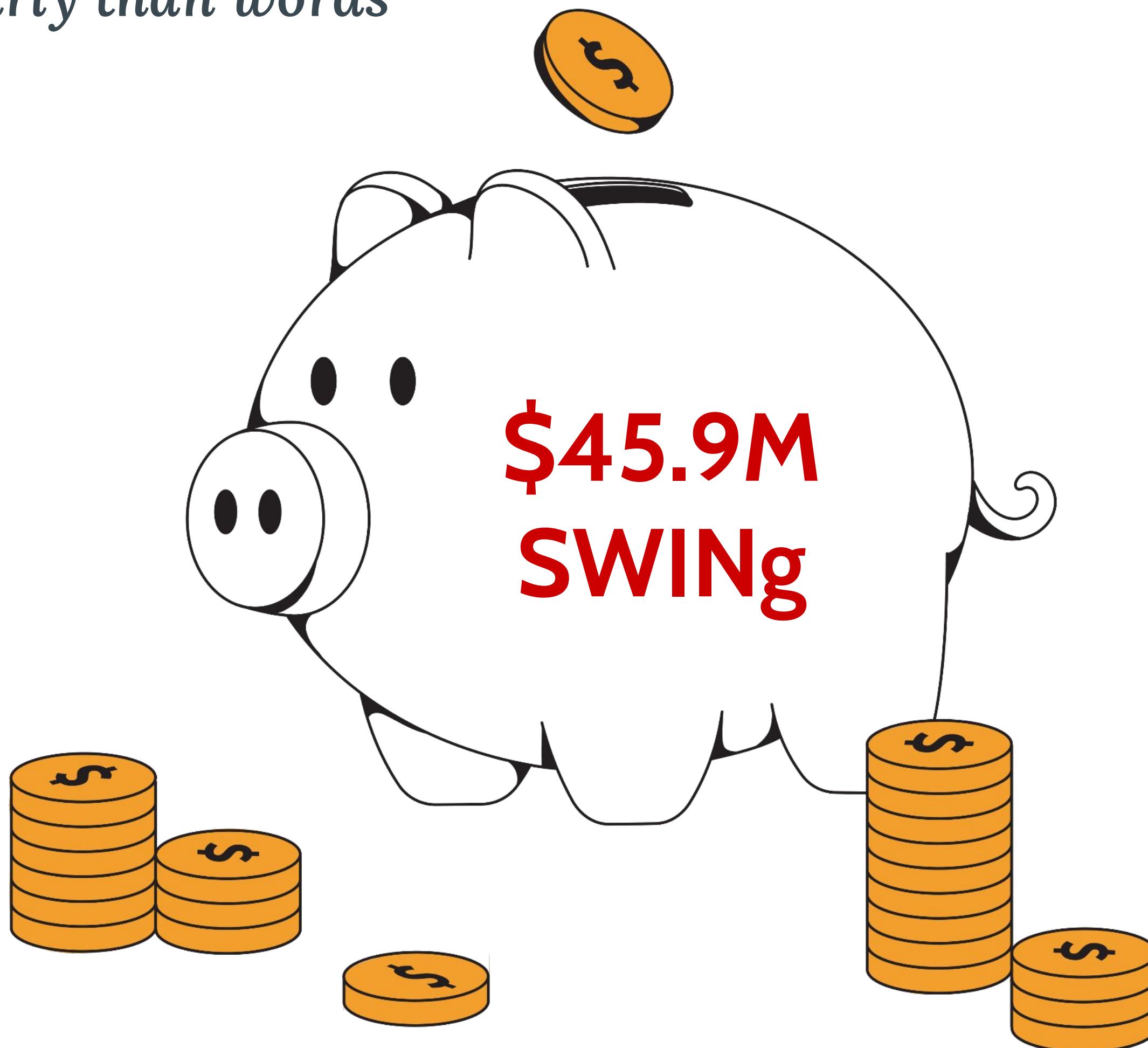
# **50.05 \$/MWh**



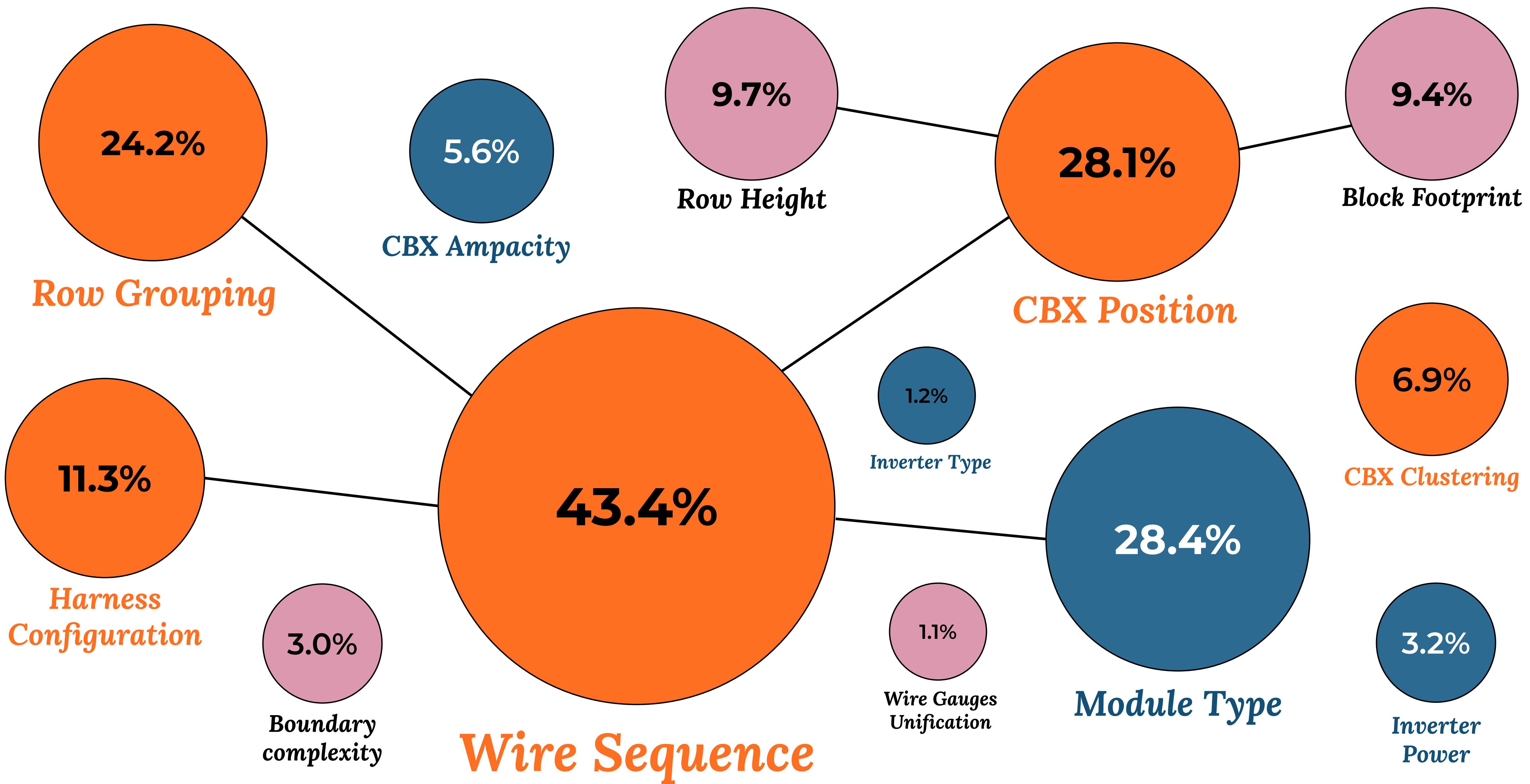
V

# Total Cost Gap

*Sometimes numbers speak more clearly than words*

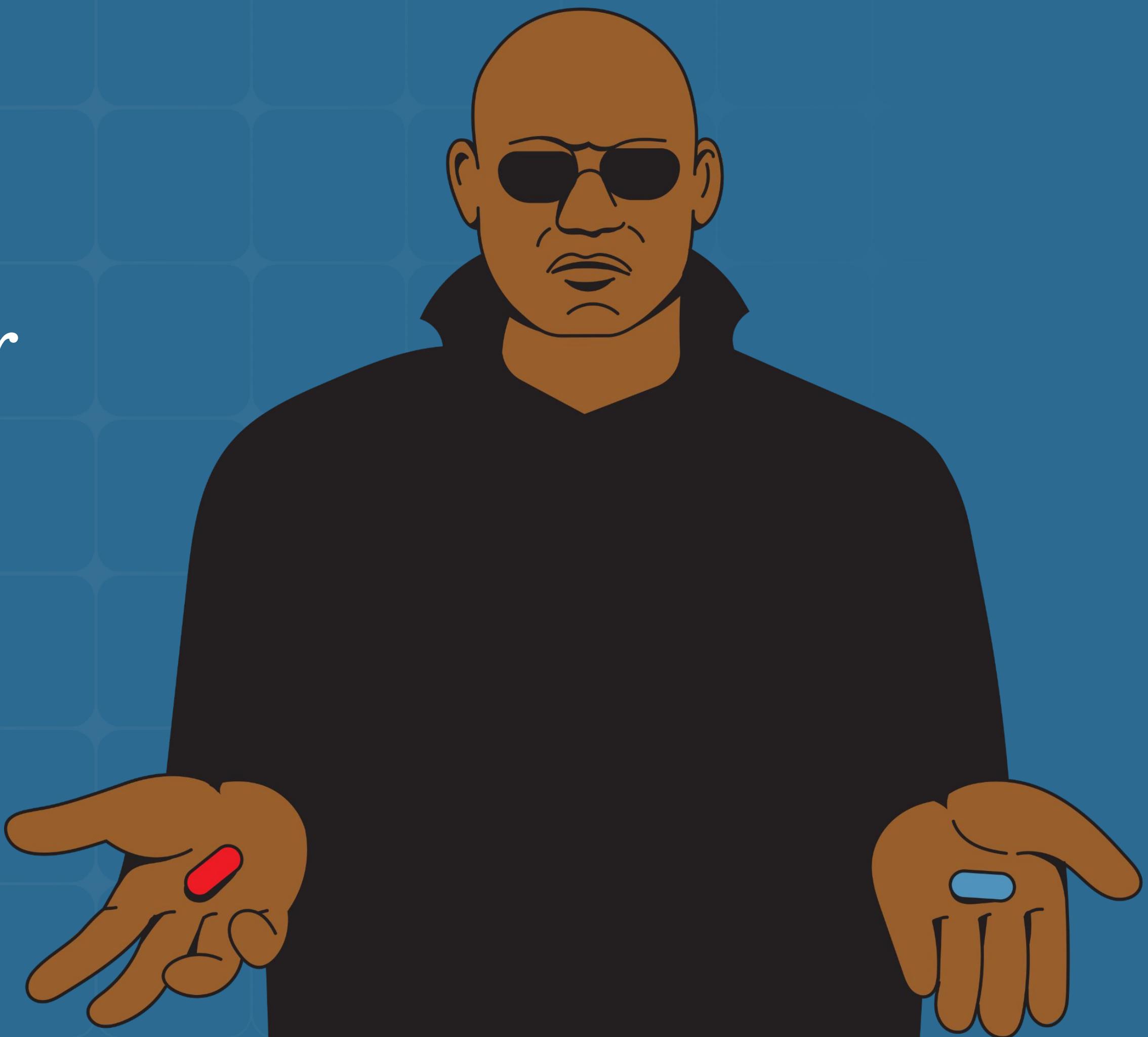


Parameter	DIFF, % EBOS CAPEX	\$, MLN Saved
Inverter Type	1.2	0.2
Inverter Power	3.2	0.5
<b>Module Type</b>	<b>28.4</b>	<b>6.1</b>
CBX Ampacity	5.6	1.0
<b>Wire Sequence</b>	<b>43.4</b>	<b>13.8</b>
CBX Position	28.1	9.2
CBX Clustering	6.9	1.2
<b>Row Grouping</b>	<b>24.2</b>	<b>7.6</b>
Harness Configuration	11.3	2.1
Boundaries Complexity	3.0	0.5
Wire Gauges Unification	1.1	0.2
Block Footprint	9.4	1.7
<b>Row Height</b>	<b>9.7</b>	<b>1.8</b>



# Defining Design Lock-In Risk

*Emphasizes the early anchor decisions vs. late-flex items*



	<i>Electrical parameters</i>	<i>Feasibility</i>	<i>Conceptual design 10%</i>	<i>Preliminary design 30%</i>	<i>Detailed design 60%</i>	<i>IFC 90%</i>	<i>Value</i>
<i>Equipment</i>	Inverter Type	0	1	0	0	0	4
	Inverter Power	0	0	1	0	0	3
	Module Selection	1	0	0	0	0	5
	CBX/LBD Selection	0	0	1	0	0	3
<i>Wiring Architecture</i>	Wire Sequence	0	0	0	1	0	2
	CBX/LBD Position	0	0	1	0	0	3
	CBX/LBD Clustering	0	0	1	0	0	3
	Row Grouping	0	0	1	0	0	3
<i>Site Context &amp; Buildability</i>	Harness Configuration	0	0	0	1	0	2
	Boundaries Complexity	1	0	0	0	0	5
	Wire Gauges Unification	0	0	0	0	1	1
	Block Footprint	0	0	1	0	0	3
	Row Height	0	1	0	0	0	4

# Defining System Interaction

*It's not over engineering if the systems are best friends!*



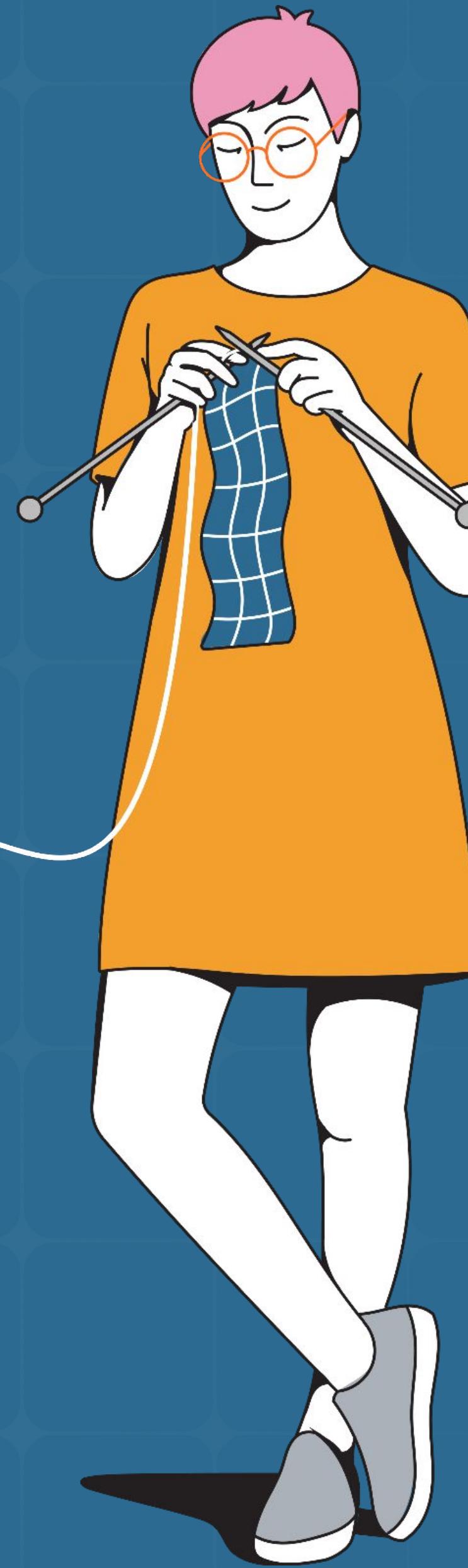
# Cross-System Interaction Heatmap

*How many downstream design decisions, systems, or disciplines are affected when this parameter changes?*

	Energy	Layout	Blocking	Electrical Engineering	Procurement	Value
Inverter Type	1	1	1	1	1	5
Inverter Power	1	1	1	1	1	5
Module Selection	1	1	1	1	1	5
CBX/LBD Selection	0	0	0	1	1	2
Wire Sequence	0	0	0	1	1	2
CBX/LBD Position	0	0	0	1	0	1
CBX/LBD Clustering	0	0	0	1	0	1
Row Grouping	0	0	0	1	1	2
Harness Configuration	0	0	0	1	1	2
Boundaries Complexity	0	1	1	0	0	2
Wire Gauges Unification	1	0	0	1	1	3
Block Footprint	0	1	1	0	0	2
Row Height	0	1	1	0	0	2

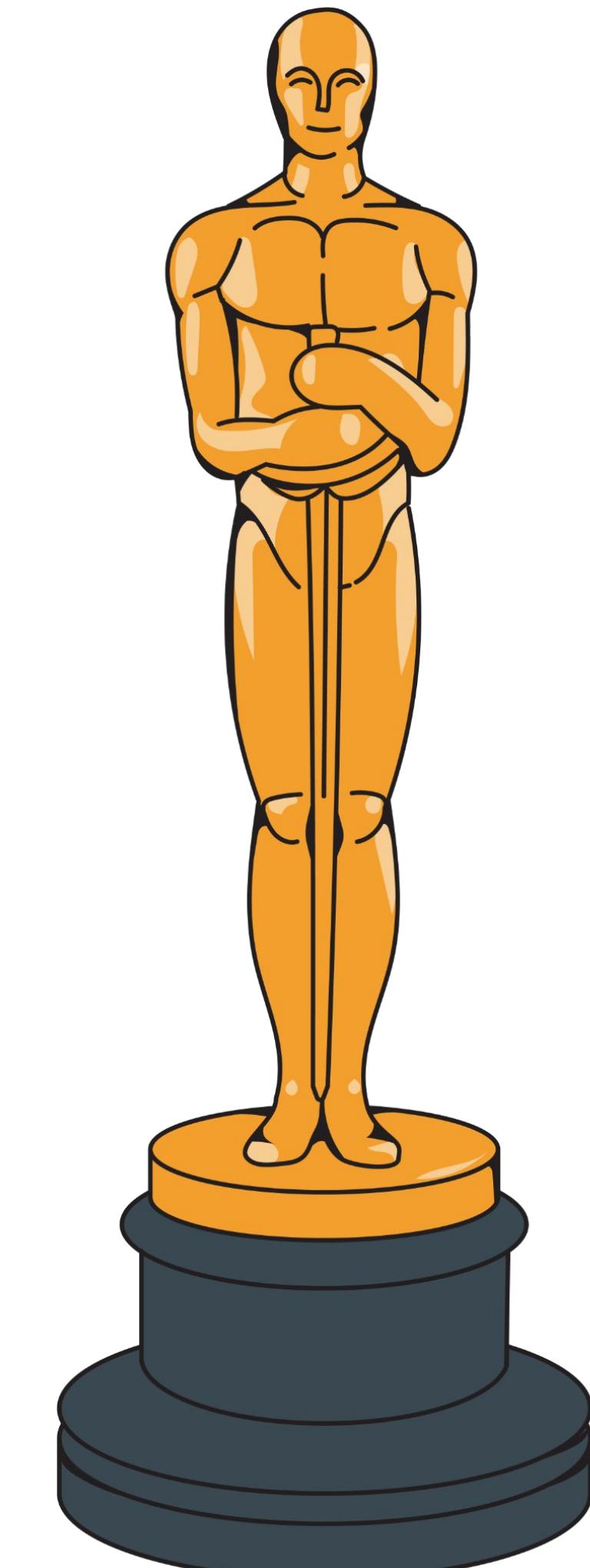
# Needle Movers

*Interpreting the results*

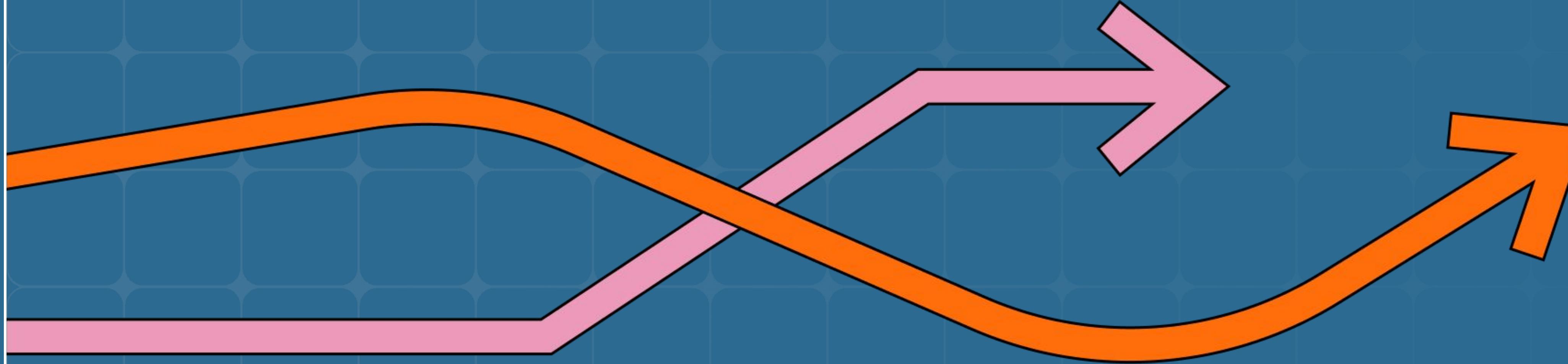


# Scored Parameter List

<b><i>Electrical Parameters</i></b>	<b><i>Cost Impact</i></b>	<b><i>Lock-In-Risk</i></b>	<b><i>System Interaction</i></b>	<b><i>Total Score</i></b>
Module Selection	3	5	5	13
	1	4	5	10
	5	2	2	9
	2	5	2	9
	1	3	5	9
Inverter Type	4	3	1	8
	3	3	2	8
	1	4	2	7
	1	3	2	6
	1	3	2	6
Wire Sequence	1	2	2	5
	1	2	2	5
	1	1	3	5
	1	1	3	5
	1	1	3	5
Boundaries Complexity	2	5	2	9
	1	3	5	9
	2	5	2	9
	1	3	5	9
	2	5	2	9
Inverter Power	1	3	5	9
	1	3	5	9
	1	3	5	9
	1	3	5	9
	1	3	5	9
CBX/LBD Position	4	3	1	8
	3	3	2	8
	1	4	2	7
	1	3	2	6
	1	3	2	6
Row Grouping	3	3	2	8
	1	4	2	7
	1	3	2	6
	1	3	2	6
	1	3	2	6
Row Height	1	4	2	7
	1	3	2	6
	1	3	2	6
	1	3	2	6
	1	3	2	6
CBX/LBD Selection	1	3	2	6
	1	3	2	6
	1	3	2	6
	1	3	2	6
	1	3	2	6
Block Footprint	1	3	2	6
	1	3	2	6
	1	3	2	6
	1	3	2	6
	1	3	2	6
CBX/LBD Clustering	1	3	1	5
	1	2	2	5
	1	2	2	5
	1	1	3	5
	1	1	3	5
Harness Configuration	1	2	2	5
	1	2	2	5
	1	1	3	5
	1	1	3	5
	1	1	3	5
Wire Gauges Unification	1	1	3	5
	1	1	3	5
	1	1	3	5
	1	1	3	5
	1	1	3	5



 <b>PVFARM</b>	Module Selection: <b>thin-film</b>	Module Selection: <b>crystalline</b>	Inverter Type: <b>String</b>	Inverter Type: <b>Central</b>	Wire Sequence: <b>H→W→F</b>	Wire Sequence: <b>H→EWT→F</b>	Wire Sequence: <b>E→EWT→F</b>	Wire Sequence: <b>NST→F</b>	Boundaries Complexity	Inverter Power	CBX/LBD Position: <b>End of Row</b>	CBX/LBD Position: <b>Middle of Row</b>	Row Grouping: <b>Single Array</b>	Row Grouping: <b>Multi Array</b>	Row Height: <b>One SAT</b>
<b>Module Selection: thin-film</b>															
<b>Module Selection: crystalline</b>															
<b>Inverter Type: String</b>															
<b>Inverter Type: Central</b>															
<b>Wire Sequence: H→W→F</b>															
<b>Wire Sequence: H→EWT→F</b>															
<b>Wire Sequence: E→EWT→F</b>															
<b>Wire Sequence: NST→F</b>															
<b>Boundaries Complexity</b>															
<b>Inverter Power</b>															
<b>CBX/LBD Position: End of Row</b>															
<b>CBX/LBD Position: Middle of Row</b>															
<b>Row Grouping: Single Array</b>															
<b>Row Grouping: Multi Array</b>															
<b>Row Height: One SAT</b>															



## Ways to Design EBOS

*There's more than one way to do it right –  
it all depends on the context, including  
inputs, goals, and constraints*

# Thank you!

*Any questions?*

